

## Features

- Operated at Low Logic Level Gate Drive
- P-Channel Switch with Low  $R_{DS(on)}$
- Epoxy Meets UL 94 V-0 Flammability Rating
- ESD Protected up to 2KV(HBM)
- Moisture Sensitivity Level 1
- Halogen Free. "Green" Device (Note 1)
- Lead Free Finish/RoHS Compliant ("P" Suffix Designates RoHS Compliant. See Ordering Information)

## Maximum Ratings

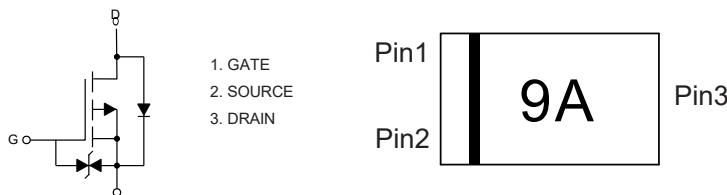
- Operating Junction Temperature Range: -55°C to +150°C
- Storage Temperature Range: -55°C to +150°C
- Maximum Thermal Resistance: 381°C/W Junction to Ambient (Note 2)

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	$V_{DS}$	-20	V
Gate-Source Voltage	$V_{GS}$	$\pm 12$	V
Drain Current-Continuous	$I_D$	-0.65	A
		-0.41	
Pulsed Drain Current (Note 3)	$I_{DM}$	-2	A
Power Dissipation (Note 4)	$P_D$	328	mW

Note:

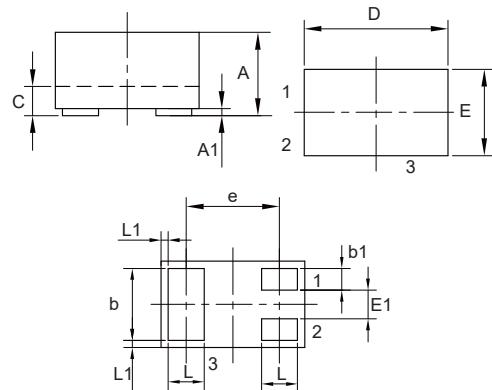
1. Halogen free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
2. The value of  $R_{\theta JA}$  is measured with the device mounted on 1in<sup>2</sup> FR-4 board with 2oz. Copper, in a still air environment with  $T_A = 25^\circ C$ . The Power dissipation  $P_{DSM}$  is based on  $R_{\theta JA} \leq 10s$  and the maximum allowed junction temperature of 150°C. The value in any given application depends on the user's specific board design.
3. Repetitive rating; pulse width limited by max. junction temperature.
4.  $P_D$  is based on max. junction temperature, using junction-case thermal resistance.

## Internal Structure and Marking Code



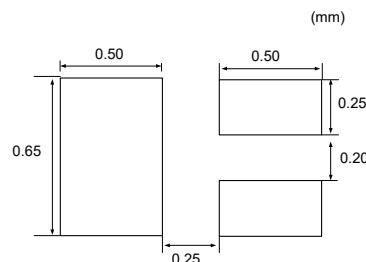
## P-Channel MOSFET

DFN1006-3



DIM	INCHES		MM		NOTE
	MIN	MAX	MIN	MAX	
A	0.017	0.022	0.42	0.55	
A1	0.000	0.002	0.00	0.05	
b	0.018	0.022	0.45	0.55	
b1	0.004	0.008	0.10	0.20	
c	0.005	0.007	0.12	0.18	
D	0.037	0.041	0.95	1.05	
E	0.022	0.026	0.55	0.65	
E1	0.006	0.010	0.15	0.25	
e	0.026		0.65		TYP.
L	0.008	0.012	0.20	0.30	
L1	0.0002		0.05		TYP.

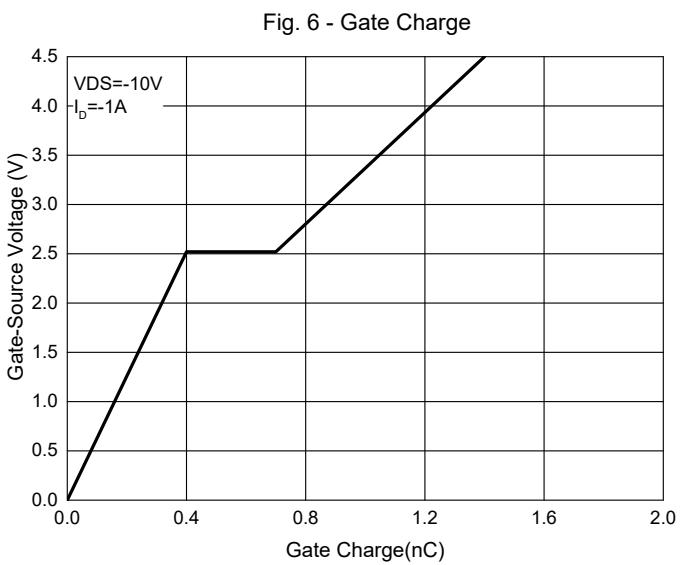
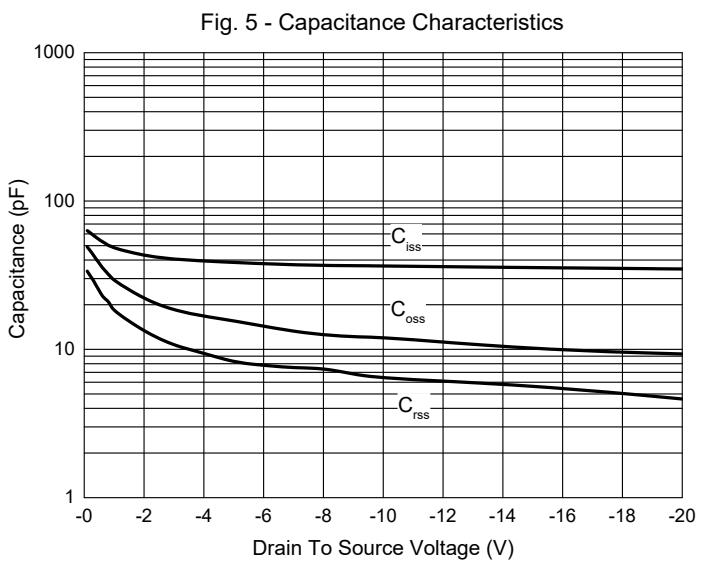
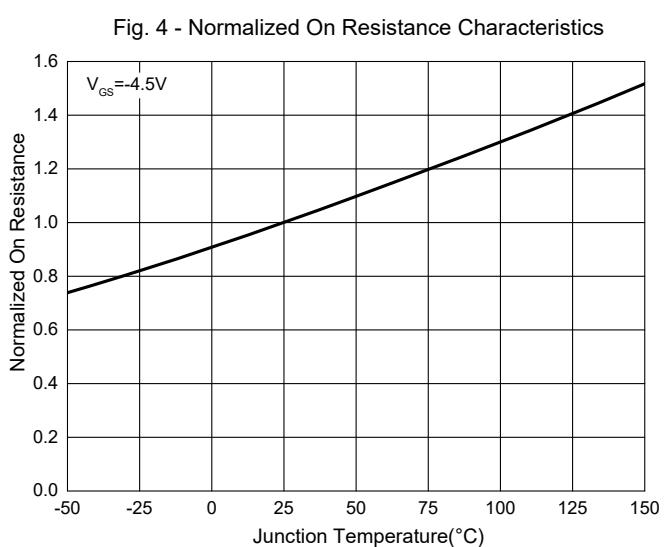
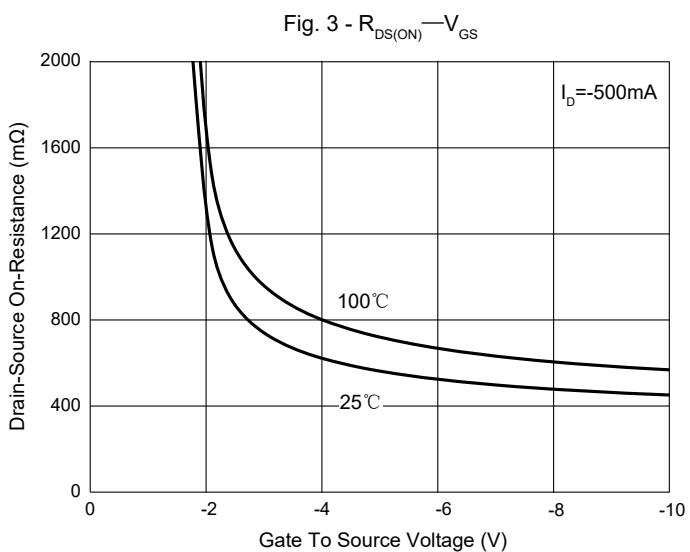
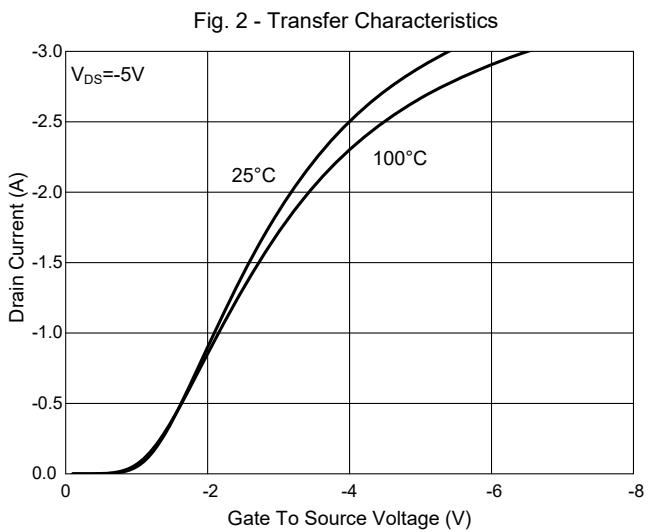
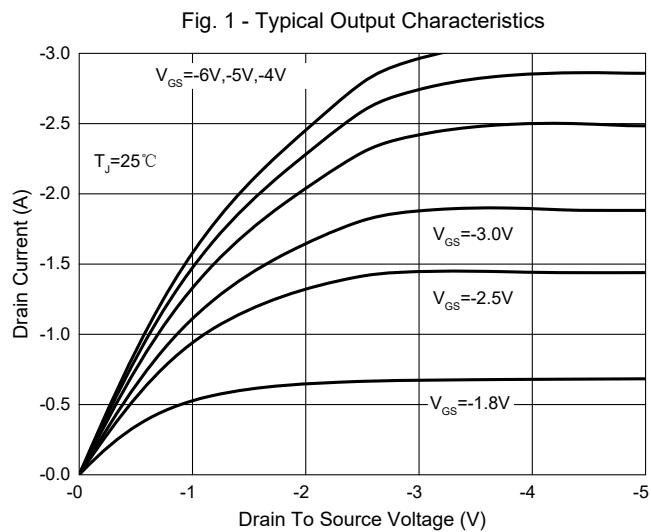
Suggested Solder Pad Layout



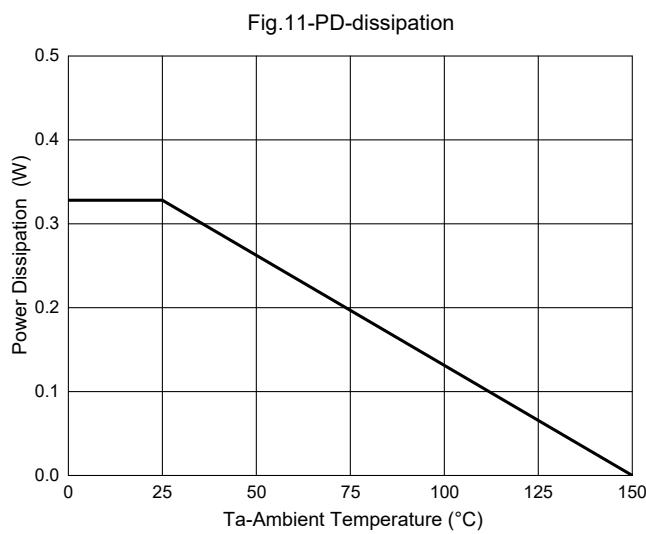
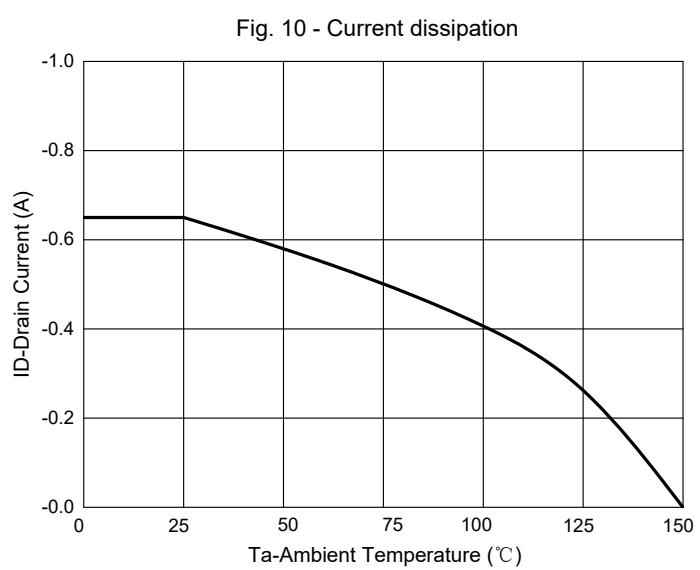
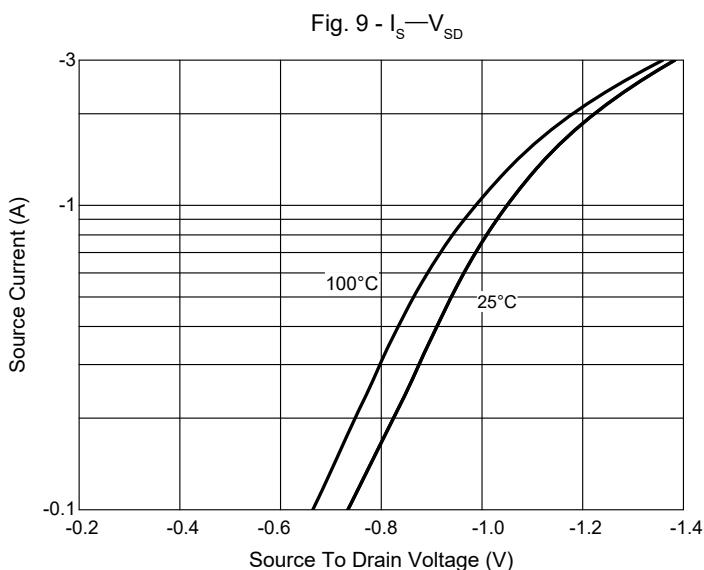
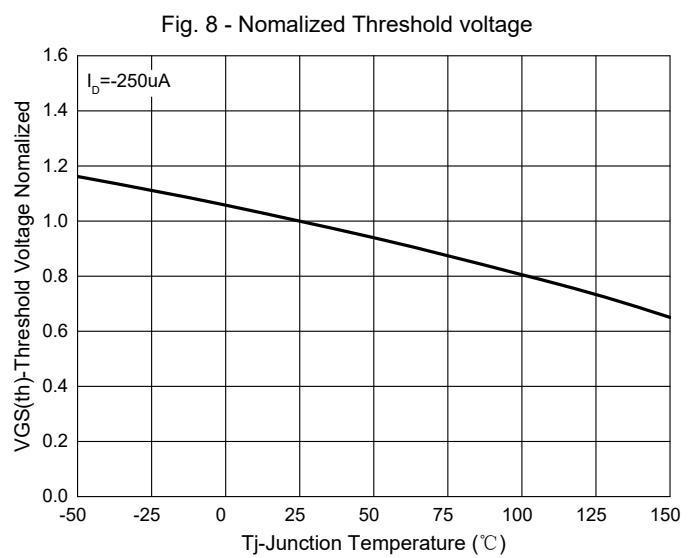
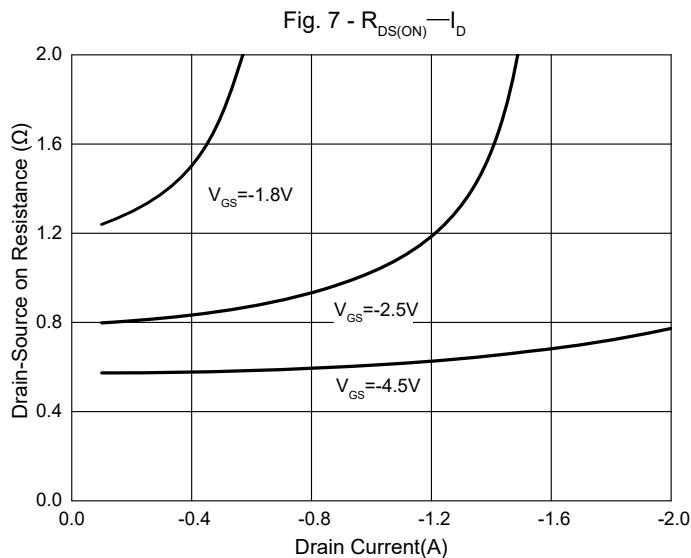
**Electrical Characteristics @ 25°C (Unless Otherwise Specified)**

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
<b>Static Characteristics</b>						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS}=0V, I_D=-250\mu A$	-20			V
Gate-Source Leakage Current	$I_{GSS}$	$V_{DS}=0V, V_{GS}=\pm 10V$			$\pm 10$	$\mu A$
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS}=-20V, V_{GS}=0V$			-1	$\mu A$
Gate-Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=-250\mu A$	-0.35	-0.62	-1.2	V
Drain-Source On-Resistance	$R_{DS(on)}$	$V_{GS}=-4.5V, I_D=-0.5A$		580	850	$m\Omega$
		$V_{GS}=-2.5V, I_D=-0.3A$		855	1200	
		$V_{GS}=-1.8V, I_D=-0.2A$		1350	2000	
Gate Resistance	$R_g$	F=1 MHz, Open drain		85		$\Omega$
<b>Diode Characteristics</b>						
Continuous Body Diode Current	$I_S$				-0.65	A
Diode Forward Voltage	$V_{SD}$	$V_{GS}=0V, I_S=-0.65A$			-1.2	V
Reverse Recovery Time	$t_{rr}$	$I_F=-1A, dI_F/dt=20A/\mu s$		32		ns
Reverse Recovery Charge	$Q_{rr}$			3.1		nC
<b>Dynamic Characteristics</b>						
Input Capacitance	$C_{iss}$	$V_{DS}=-10V, V_{GS}=0V, f=1MHz$		36		$pF$
Output Capacitance	$C_{oss}$			12		
Reverse Transfer Capacitance	$C_{rss}$			6.4		
Total Gate Charge	$Q_g$	$V_{DD}=-10V, V_{GS}=-4.5V, I_D=-0.5A$		1.4		$nC$
Gate-Source Charge	$Q_{gs}$			0.4		
Gate-Drain Charge	$Q_{gd}$			0.3		
Turn-On Delay Time	$t_{d(on)}$	$V_{DD}=-10V, V_{GS}=-4.5V, R_G=3\Omega, I_D=-0.5A$		3.2		ns
Turn-On Rise Time	$t_r$			18.6		
Turn-Off Delay Time	$t_{d(off)}$			8.3		
Turn-Off Fall Time	$t_f$			21.4		

## Curve Characteristics



## Curve Characteristics



## Curve Characteristics

Fig. 12 - Safe Operation Area

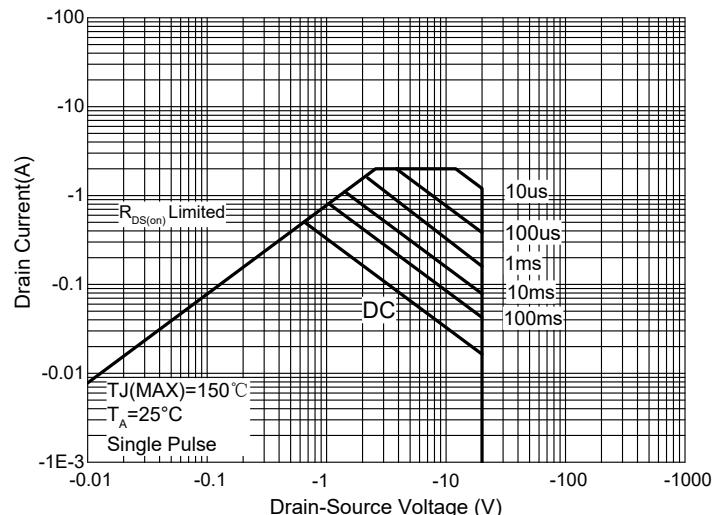
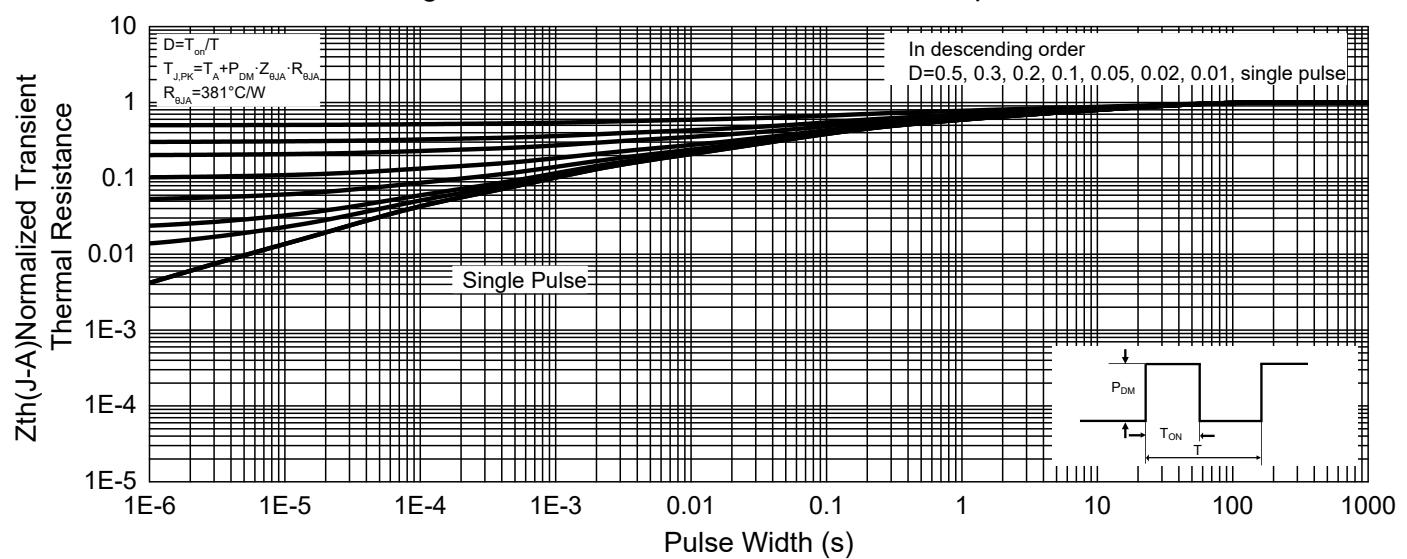


Fig. 13 - Normalized Transient Thermal Impedance



## Ordering Information

Device	Packing
Part Number-TP	Tape&Reel:10Kpcs/Reel

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