

## Features

- Split Gate Trench MOSFET Technology
- Moisture Sensitivity Level 1
- Halogen Free."Green" Device<sup>(Note1)</sup>
- Epoxy Meets UL 94 V-0 Flammability Rating
- Lead Free Finish/RoHS Compliant<sup>(Note2)</sup>("P" Suffix Designates RoHS Compliant. See Ordering Information)

## Maximum Ratings

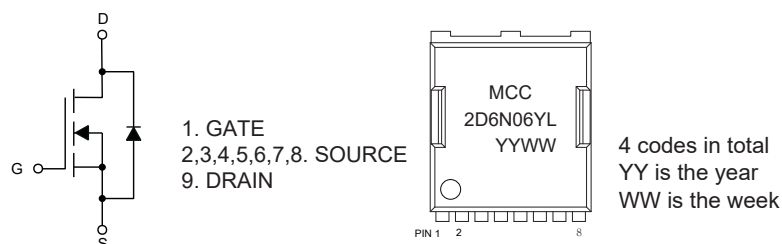
- Operating Junction Temperature Range : -55°C to +175°C
- Storage Temperature Range: -55°C to +175°C
- Thermal Resistance: 40°C/W Junction to Ambient<sup>(Note3)</sup>
- Thermal Resistance: 0.95°C/W Junction to Case

Parameter		Symbol	Rating	Unit
Drain-Source Voltage		V <sub>DS</sub>	60	V
Gate-Source Voltage		V <sub>GS</sub>	±20	V
Continuous Drain Current	T <sub>C</sub> =25°C	I <sub>D</sub>	150	A
	T <sub>C</sub> =100°C		106	
Pulsed Drain Current <sup>(Note 4)</sup>		I <sub>DM</sub>	600	A
Total Power Dissipation <sup>(Note 5)</sup>		P <sub>D</sub>	158	W
Single Pulsed Avalanche Energy <sup>(Note 6)</sup>		E <sub>AS</sub>	242	mJ

Note:

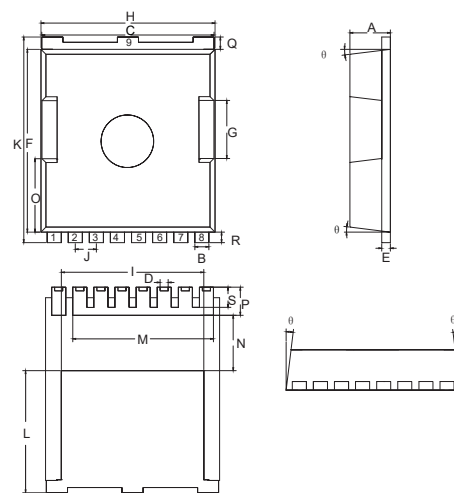
1. Halogen free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
2. High Temperature Solder Exemption Applied, see EU Directive Annex 7(a)-I.
3. The value of  $R_{\theta JA}$  is measured with the device mounted on 1in<sup>2</sup> FR-4 board with 2oz. Copper, in a still air environment with  $T_A=25^\circ\text{C}$ .
4. Repetitive rating; pulse width limited by max. junction temperature.
5.  $P_D$  is based on max. junction temperature, using junction-case thermal resistance.
6.  $T_J=25^\circ\text{C}$ ,  $V_{DD}=30\text{V}$ ,  $V_{GS}=10\text{V}$ ,  $R_G=25\Omega$ ,  $L=1\text{mH}$ .

## Internal Structure and Marking Code



# N-CHANNEL MOSFET

## TOLL-8L



DIM	INCHES		MM		NOTE
	MIN	MAX	MIN	MAX	
A	0.087	0.094	2.20	2.40	
B	0.028	0.035	0.70	0.90	
C	0.382	0.390	9.70	9.90	
D	0.017	0.020	0.42	0.50	
E	0.016	0.024	0.40	0.60	
F	0.405	0.417	10.28	10.58	
G	0.122	0.138	3.10	3.50	
H	0.382	0.398	9.70	10.10	
I	0.311	0.327	7.90	8.30	
J	0.047		1.20		BSC
K	0.452	0.468	11.48	11.88	
L	0.266	0.281	6.75	7.15	
M	0.315		8.00		
N	0.118	0.130	3.00	3.30	
O	0.157	0.172	3.98	4.38	
P	0.055	0.071	1.40	1.80	
Q	0.024	0.031	0.60	0.80	
R	0.020	0.028	0.50	0.70	
S	0.039	0.051	1.00	1.30	
θ	4°	10°	4°	10°	

**Electrical Characteristics @ 25°C (Unless Otherwise Specified)**

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Static Characteristics						
Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	V <sub>GS</sub> =0, I <sub>D</sub> =250μA	60			V
Gate-Source Leakage Current	I <sub>GSS</sub>	V <sub>DS</sub> =0V, V <sub>GS</sub> =±20V			±100	nA
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =60V, V <sub>GS</sub> =0V			1	μA
Gate-Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250μA	1.0	1.8	2.5	V
Drain-Source On-Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =75A		1.9	2.6	mΩ
		V <sub>GS</sub> =4.5V, I <sub>D</sub> =20A		2.8	4.0	
Gate Resistance	R <sub>G</sub>	f=1 MHz, Open Drain		2.0		Ω
Diode Characteristics						
Continuous Body Diode Current	I <sub>S</sub>				150	A
Diode Forward Voltage	V <sub>SD</sub>	V <sub>GS</sub> =0V, I <sub>S</sub> =75A			1.2	V
Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> =30A, dI <sub>F</sub> /dt=100A/μs		41.8		ns
Reverse Recovery Charge	Q <sub>rr</sub>			40.3		nC
Dynamic Characteristics						
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> =30V,V <sub>GS</sub> =0V,f=1MHz		3117		pF
Output Capacitance	C <sub>oss</sub>			1481		
Reverse Transfer Capacitance	C <sub>rss</sub>			88		
Total Gate Charge	Q <sub>g</sub>	V <sub>DS</sub> =30V,V <sub>GS</sub> =10V,I <sub>D</sub> =75A		55.5		nC
Gate-Source Charge	Q <sub>gs</sub>			12		
Gate-Drain Charge	Q <sub>gd</sub>			14.2		
Turn-On Delay Time	t <sub>d(on)</sub>	V <sub>DD</sub> =30V, V <sub>GS</sub> =10V I <sub>DS</sub> =75A,R <sub>G</sub> =6Ω		14.4		ns
Turn-On Rise Time	t <sub>r</sub>			169		
Turn-Off Delay Time	t <sub>d(off)</sub>			60.8		
Turn-Off Fall Time	t <sub>f</sub>			220		

## Curve Characteristics

Fig. 1 - Typical Output Characteristics

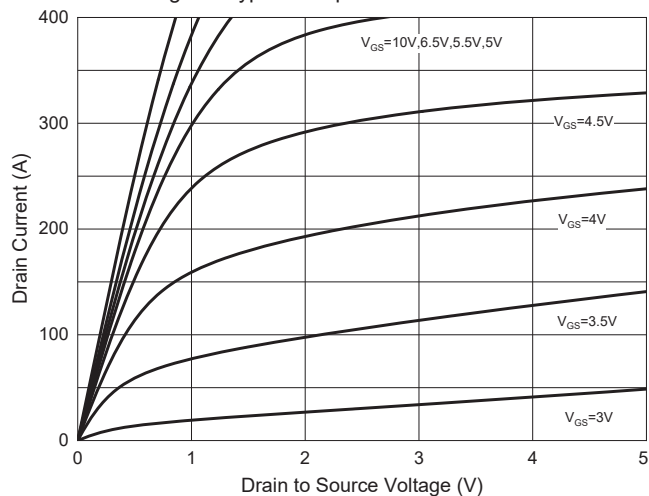


Fig. 2 - Transfer Characteristics

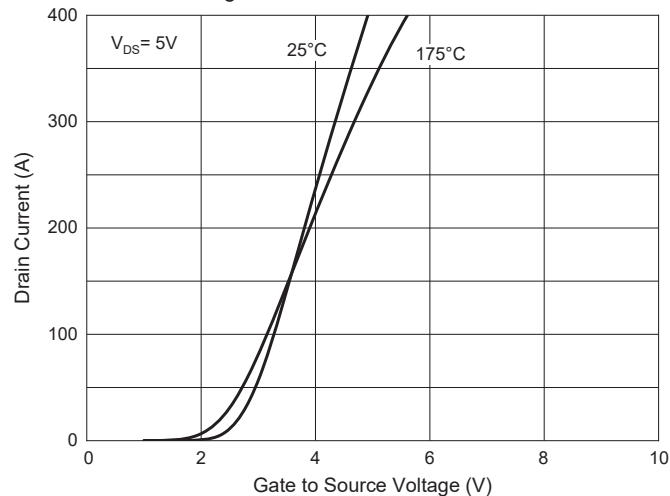


Fig. 3 -  $R_{DS(ON)}-V_{GS}$

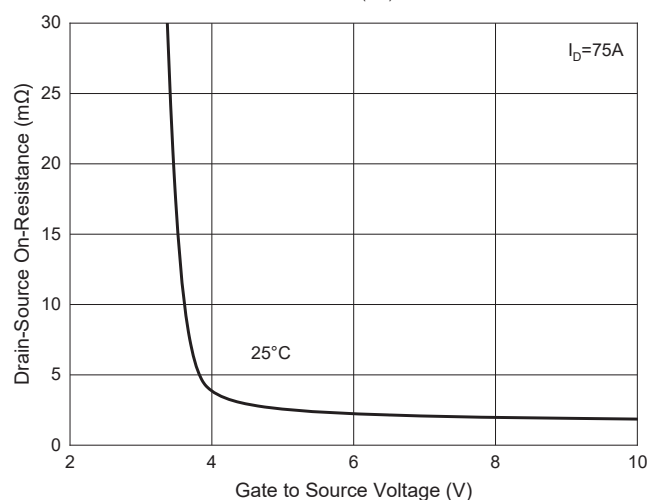


Fig. 4 -  $R_{DS(ON)}-I_D$

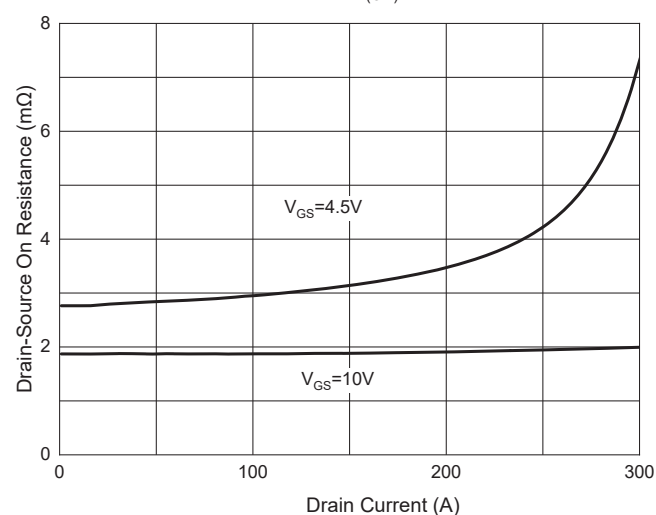


Fig. 5 - Capacitance Characteristics

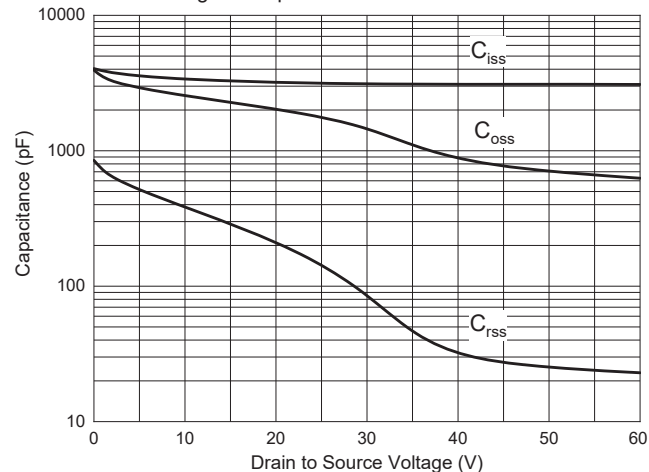
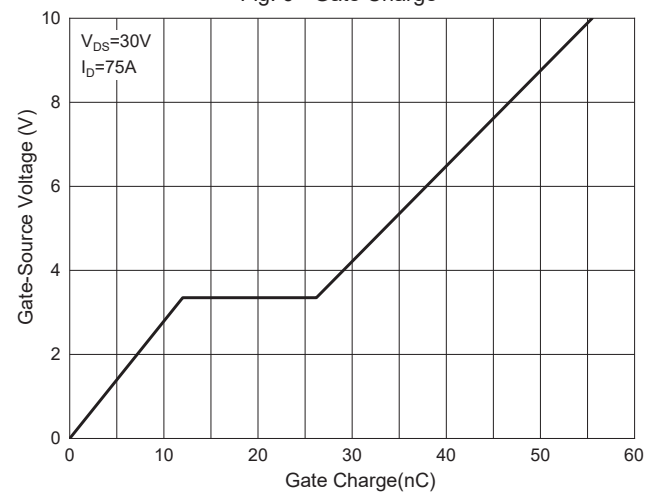


Fig. 6 - Gate Charge



## Curve Characteristics

Fig. 7 - Normalized Threshold Voltage

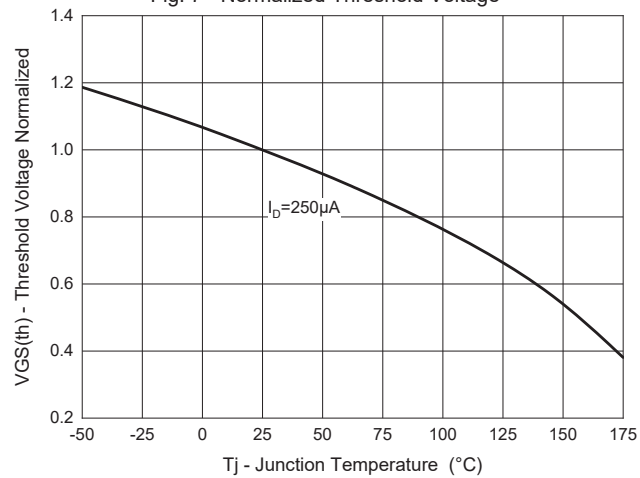


Fig. 8 - Normalized On Resistance Characteristics

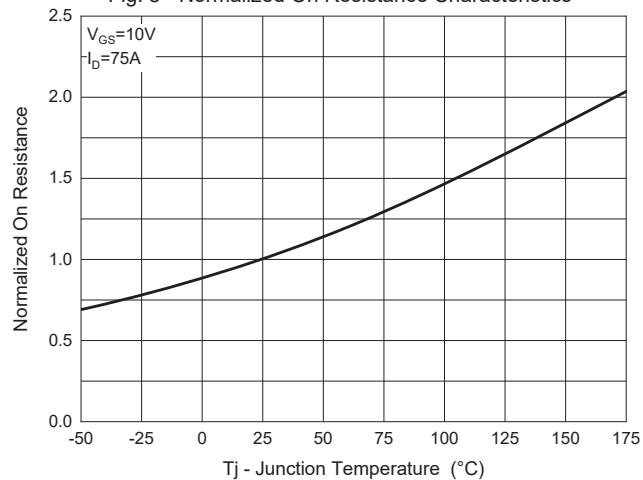


Fig. 9 -  $I_S - V_{SD}$

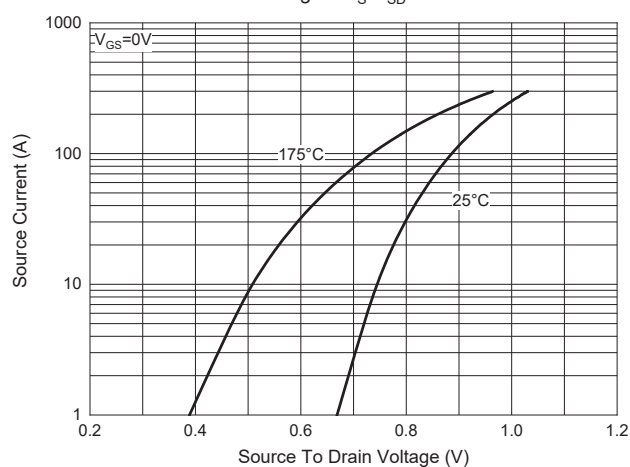


Fig. 10 - Drain Current

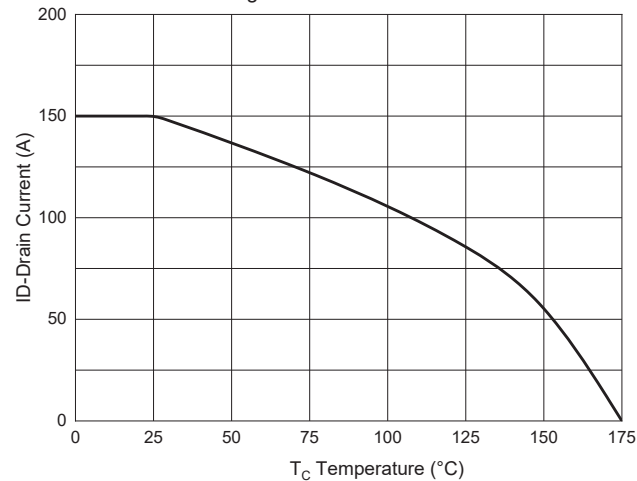
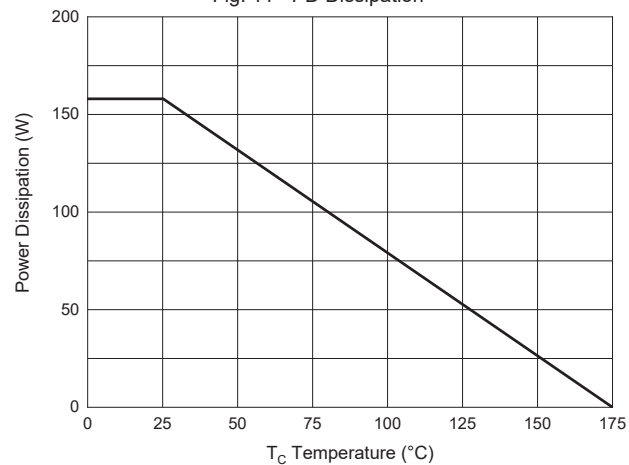


Fig. 11 - PD Dissipation



## Curve Characteristics

Fig. 12 - Safe Operation Area

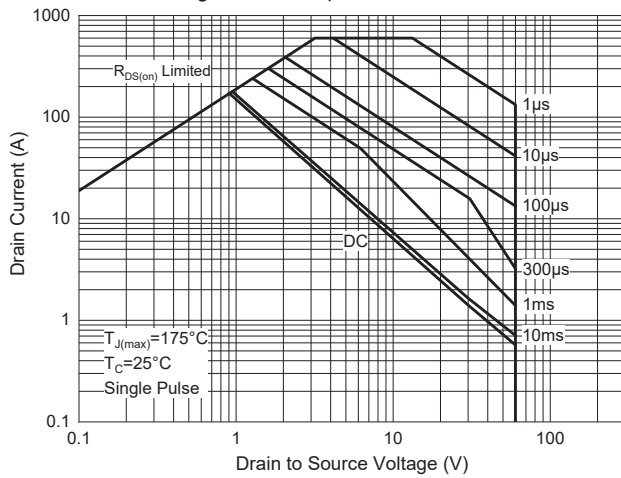
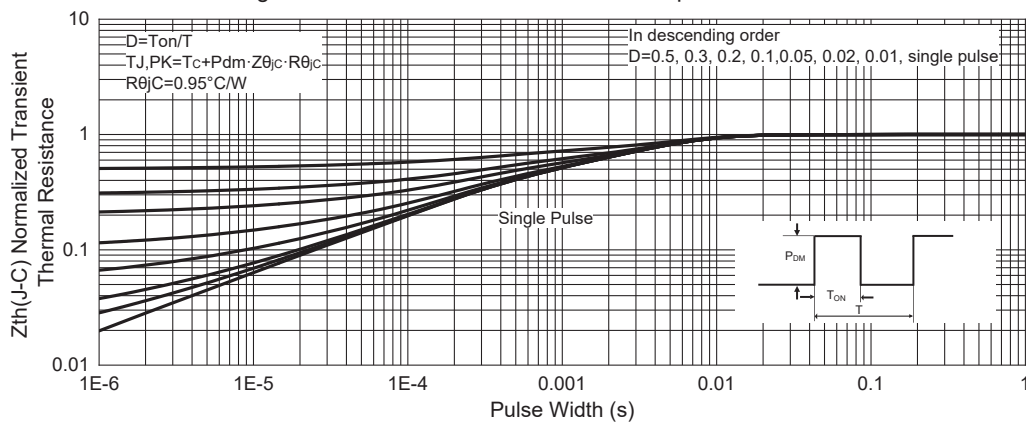


Fig. 13 - Normalized Transient Thermal Impedance



## Ordering Information

Device	Packing
Part Number-TP	Tape&Reel: 2Kpcs/Reel

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