

Features

- AEC-Q101 Qualified
- Trench Power LV MOSFET Technology
- · Excellent Package for Heat Dissipation
- High Density Cell Design for Low R_{DS(ON)}
- Halogen Free. "Green" Device (Note 1)
- Epoxy Meets UL 94 V-0 Flammability Rating
- Lead Free Finish/RoHS Compliant ("P" Suffix Designates RoHS Compliant. See Ordering Information)
- · Moisture Sensitivity Level 3

Maximum Ratings

• Operating Junction Temperature Range : -55°C to +150°C

• Storage Temperature Range: -55°C to +150°C

• Thermal Resistance: 5.2°C/W Junction to Case

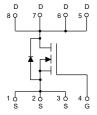
• Thermal Resistance: 54°C/W Junction to Ambient(Note2)

Parameter		Symbol	Rating	Unit	
Drain-Source Voltage		V _{DS}	40	V	
Gate-Source Volltage		V _{GS}	±20	V	
Continuous Drain Current	T _C =25°C	I _D	30	А	
	T _C =100°C		19		
Pulsed Drain Current ^(Note3)		I _{DM}	120	Α	
Total Power Dissipation ^(Note4)		P _D	24	W	
Single Pulsed Avalanche Energy ^(Note5)		E _{AS}	40	mJ	

Note:

- 1. Halogen free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 2. The value of $R_{\theta JA}$ is measured with the device mounted on $1in^2$ FR-4 board with 2oz. Copper, in a still air environment with T_A =25°C. The Power dissipation P_{DSM} is based on $R_{\theta JA}$ t≤ 10s and the maximum allowed junction temperature of 150°C. The value in any given application depends on the user's specific board design.
- 3. Repetitive rating; pulse width limited by max. junction temperature.
- 4. P_D is based on max. junction temperature, using junction-case thermal resistance.
- 5. $T_J=25$ °C, $V_{DD}=40$ V, $V_{GS}=10$ V, L=1mH

Internal Structure and Marking Code

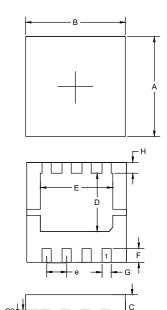




4 codes in total YY is the year WW is the week

N-CHANNEL MOSFET

DFN3333



DIMENSIONS					
DIM	INC	INCHES		М	NOTE
DIIVI	MIN	MAX	MIN	MAX	NOTE
Α	0.126	0.130	3.20	3.30	
В	0.126	0.130	3.20	3.30	
С	0.030	0.033	0.75	0.85	
C1	0.007	0.009	0.18	0.22	
C2		0.002		0.05	
D	0.071	0.079	1.80	2.00	
Е	0.087	0.098	2.20	2.50	
F	0.016	0.020	0.40	0.50	
G	0.010	0.014	0.25	0.35	
Н	0.012	0.016	0.30	0.40	
е	0.024	0.028	0.60	0.70	
		-			

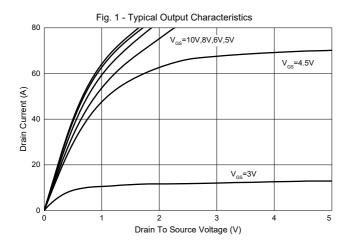


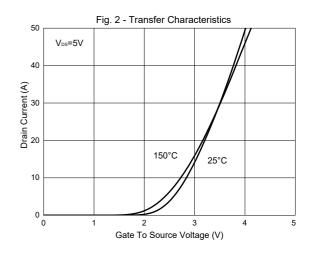
Electrical Characteristics @ 25°C (Unless Otherwise Specified)

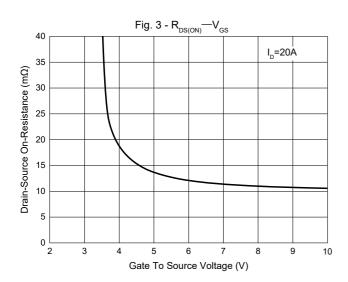
Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit	
Static Characteristics					1		
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} =0V, I _D =250μA	40			V	
Gate-Source Leakage Current	I _{GSS}	V _{DS} =0V, V _{GS} =±20V			±100	nA	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =40V, V _{GS} =0V			1	μA	
Gate-Threshold Voltage	V _{GS(th)}	$V_{DS}=V_{GS}$, $I_{D}=250\mu A$	1.0	1.5	2.5	V	
Drain-Source On-Resistance		V _{GS} =10V, I _D =20A	11 14 14.3 18.5		mΩ		
	R _{DS(on)}	V _{GS} =4.5V, I _D =10A			18.5	- 11152	
Gate Resistance	R_{g}	F=1MHz, Open Drain		3.5		Ω	
Diode Characteristics				1	1		
Continuous Body Diode Current	Is				30	Α	
Diode Forward Voltage	V _{SD}	V _{GS} =0V, I _S =10A			1.2	V	
Reverse Recovery Time	t _{rr}	1 = 20.0 d1 /dt= 200.0 /u.c		13		ns	
Reverse Recovery Charge	Q _{rr}	l _F =20A, dl _F /dt=300A/μs		11		nC	
Dynamic Characteristics							
Input Capacitance	C _{iss}			990			
Output Capacitance	C _{oss}	V _{DS} =25V,V _{GS} =0V,f=1MHz		90		pF	
Reverse Transfer Capacitance	C _{rss}			80			
Total Gate Charge	Q _g			23			
Gate-Source Charge	Q _{gs}	V _{DS} =20V,V _{GS} =10V,I _D =20A		3.5		nC	
Gate-Drain Charge	Q_{gd}			7			
Turn-On Delay Time	t _{d(on)}			4			
Turn-On Rise Time	t _r	V _{DS} =20V,V _{GS} =10V, I _{DS} =20A		57.5			
Turn-Off Delay Time	t _{d(off)}			20		ns	
Turn-Off Fall Time	t _f			2.5			

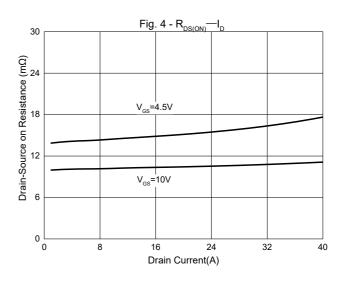


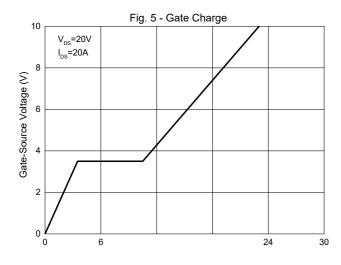
Curve Characteristics

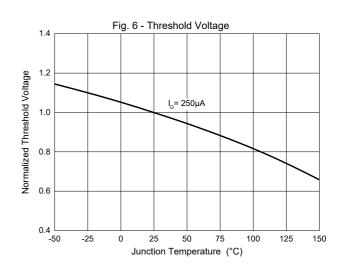






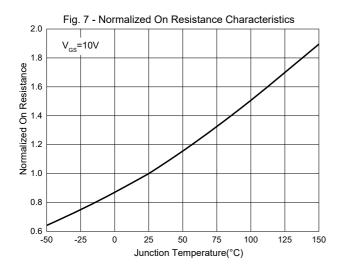


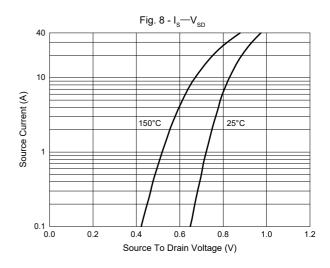


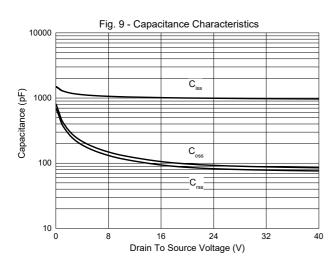


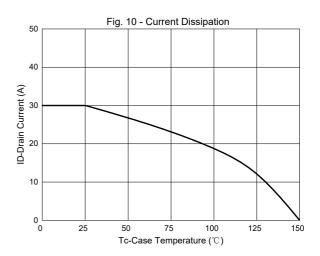


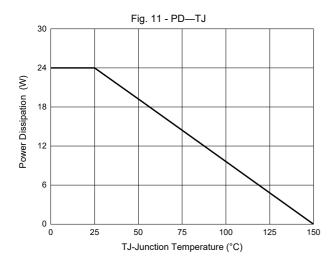
Curve Characteristics





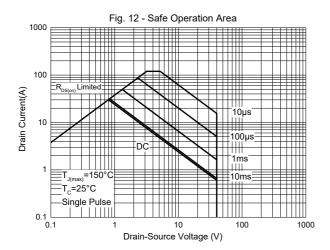


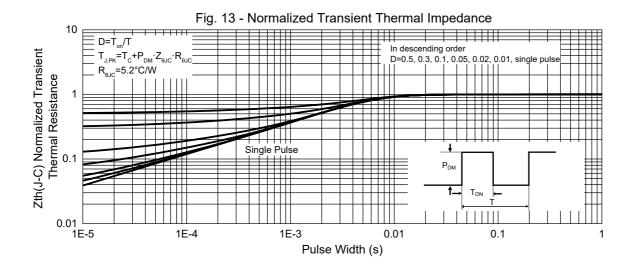






Curve Characteristics







Ordering Information

Device	Packing	
Part Number-TP	Tape&Reel: 5Kpcs/Reel	

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