

**Features**

- Split Gate Trench MOSFET Technology
- Excellent Stability and Uniformity
- Moisture Sensitivity Level 1
- Halogen Free."Green"Device<sup>(Note1)</sup>
- Epoxy Meets UL 94 V-0 Flammability Rating
- Lead Free Finish/RoHS Compliant ("P" Suffix Designates RoHS Compliant. See Ordering Information)

**Maximum Ratings**

- Operating Junction Temperature Range : -55°C to +175°C
- Storage Temperature Range: -55°C to +175°C
- Thermal Resistance: 40°C/W Junction to Ambient(Steady-State)<sup>(Note2)</sup>
- Thermal Resistance: 0.4°C/W Junction to Case

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	V <sub>DS</sub>	100	V
Gate-Source Voltage	V <sub>GS</sub>	±20	V
Continuous Drain Current	I <sub>D</sub>	T <sub>C</sub> =25°C	260
		T <sub>C</sub> =100°C	184
Pulsed Drain Current <sup>(Note3)</sup>	I <sub>DM</sub>	1040	A
Total Power Dissipation <sup>(Note4)</sup>	P <sub>D</sub>	375	W
Avalanche Energy <sup>(Note5)</sup>	E <sub>AS</sub>	1122	mJ

Note:

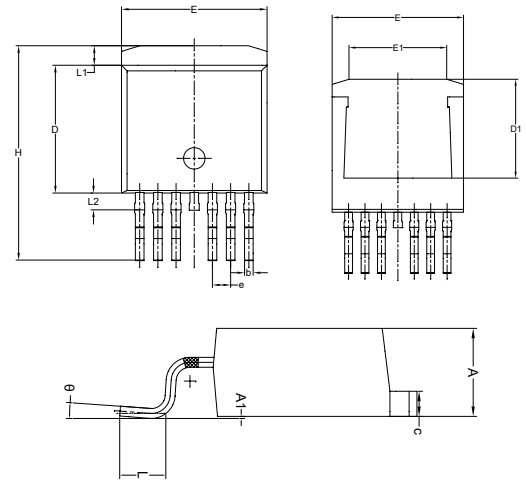
1. Halogen free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
2. The value of R<sub>θJA</sub> is measured with the device mounted on 1in<sup>2</sup> FR-4 board with 2oz. Copper, in a still air environment with T<sub>A</sub>=25°C. The Power dissipation P<sub>DSM</sub> is based on R<sub>θJA</sub> t ≤ 10s and the maximum allowed junction temperature of 175°C. The value in any given application depends on the user's specific board design.
3. Repetitive rating; pulse width limited by max. junction temperature.
4. P<sub>D</sub> is based on max. junction temperature, using junction-case thermal resistance.
5. T<sub>J</sub>=25°C, V<sub>DD</sub>=50V, V<sub>GS</sub>=10V, L=0.5mH

**Internal Structure and Marking Code**

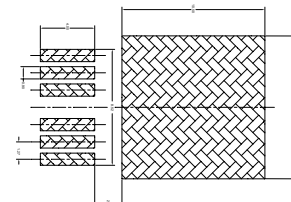


**N-CHANNEL  
MOSFET**

**TO-263-7**



DIM	INCHES		MM		NOTE
	MIN	MAX	MIN	MAX	
A	0.167	0.183	4.24	4.64	
A1	0.000	0.010	0.00	0.25	
b	0.020	0.035	0.50	0.90	
c	0.045	0.055	1.15	1.40	
D	0.347	0.364	8.82	9.25	
D1	0.270		6.86		
E	0.392	0.408	9.96	10.36	
E1	0.256	0.310	6.50	7.89	
e	0.050		1.27		TYP.
H	0.575	0.625	14.61	15.88	
L	0.070	0.110	1.78	2.79	
L1	0.039	0.056	0.98	1.42	
L2		0.070		1.78	
θ			0°	8°	



**Electrical Characteristics @ 25°C (Unless Otherwise Specified)**

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
<b>Static Characteristics</b>						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS}=0V, I_D=1mA$	100			V
Gate-Source Leakage Current	$I_{GSS}$	$V_{DS}=0V, V_{GS}=\pm 20V$			$\pm 100$	nA
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS}=80V, V_{GS}=0V$			1	$\mu A$
Gate-Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=250\mu A$	2	2.8	4	V
Drain-Source On-Resistance	$R_{DS(on)}$	$V_{GS}=10V, I_D=30A$		1.4	1.8	m $\Omega$
Gate Resistance	$R_g$	F=1 MHz, Open drain		1.4		$\Omega$
<b>Diode Characteristics</b>						
Continuous Body Diode Current	$I_S$				260	A
Diode Forward Voltage	$V_{SD}$	$V_{GS}=0V, I_S=30A$			1.3	V
Reverse Recovery Time	$t_{rr}$	$I_S=130A, di/dt=100A/\mu s$		89.2		ns
Reverse Recovery Charge	$Q_{rr}$			156.8		nC
<b>Dynamic Characteristics</b>						
Input Capacitance	$C_{iss}$	$V_{DS}=30V, V_{GS}=0V, f=1MHz$		10589		pF
Output Capacitance	$C_{oss}$			5099		
Reverse Transfer Capacitance	$C_{rss}$			318		
Total Gate Charge	$Q_g$	$V_{DS}=50V, V_{GS}=10V, I_D=130A$		161		nC
Gate-Source Charge	$Q_{gs}$			38		
Gate-Drain Charge	$Q_{gd}$			50		
Turn-On Delay Time	$t_{d(on)}$	$V_{DS}=50V, V_{GS}=10V, R_G=6\Omega, I_{DS}=130A$		41		ns
Turn-On Rise Time	$t_r$			174.5		
Turn-Off Delay Time	$t_{d(off)}$			109		
Turn-Off Fall Time	$t_f$			123.7		

## Curve Characteristics

Fig. 1 - Typical Output Characteristics

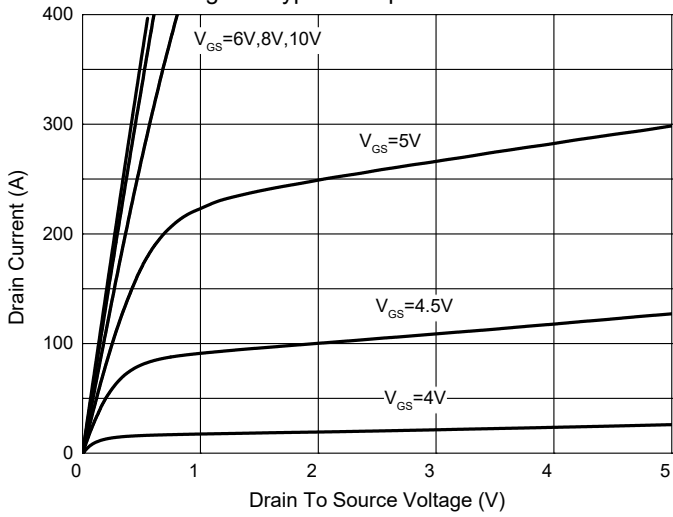


Fig. 2 - Transfer Characteristics

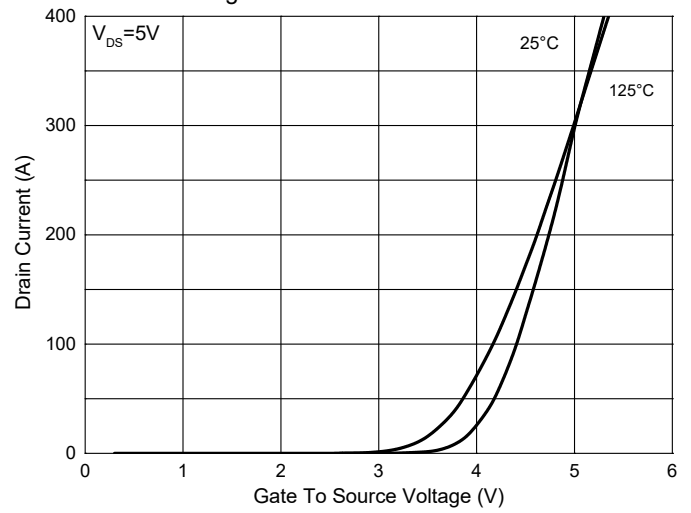


Fig. 3 -  $R_{DS(ON)}$ — $V_{GS}$

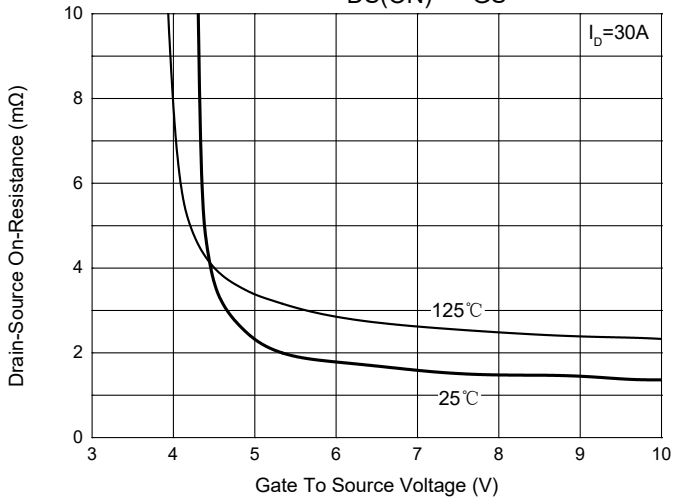


Fig. 4 -  $R_{DS(ON)}$ — $I_D$

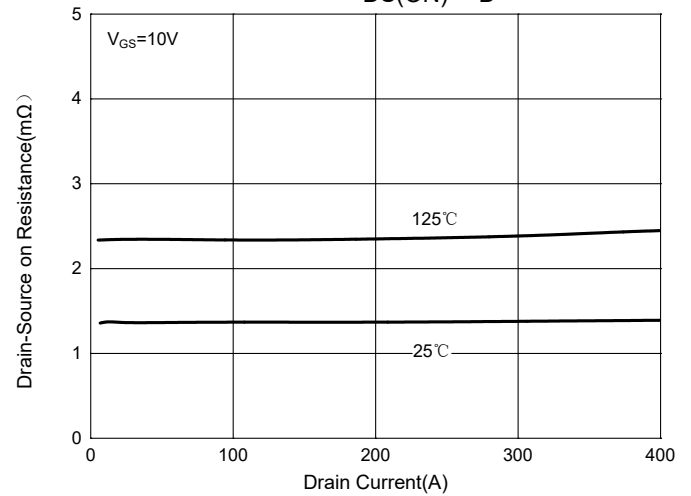


Fig. 5 - Capacitance Characteristics

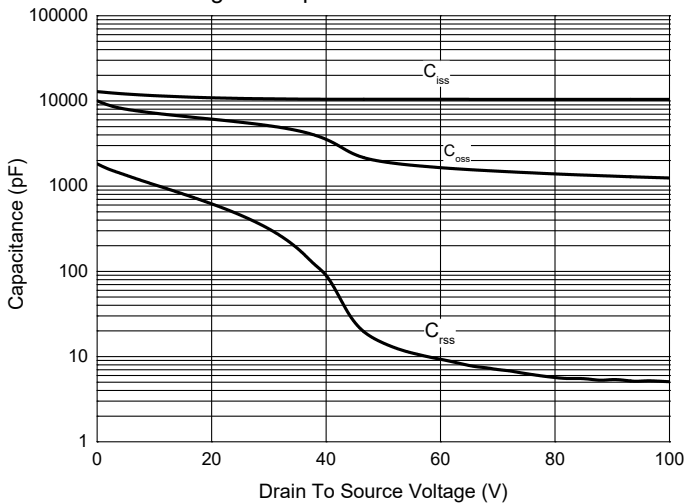
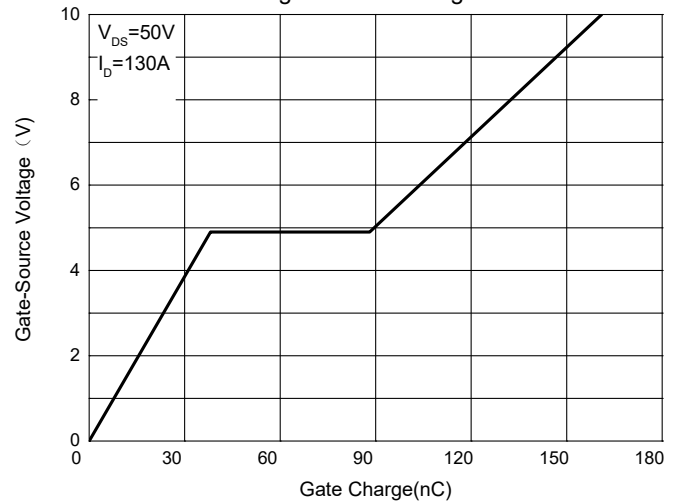


Fig. 6 - Gate Charge



**Curve Characteristics**

Fig. 7 - Normalized Threshold Voltage

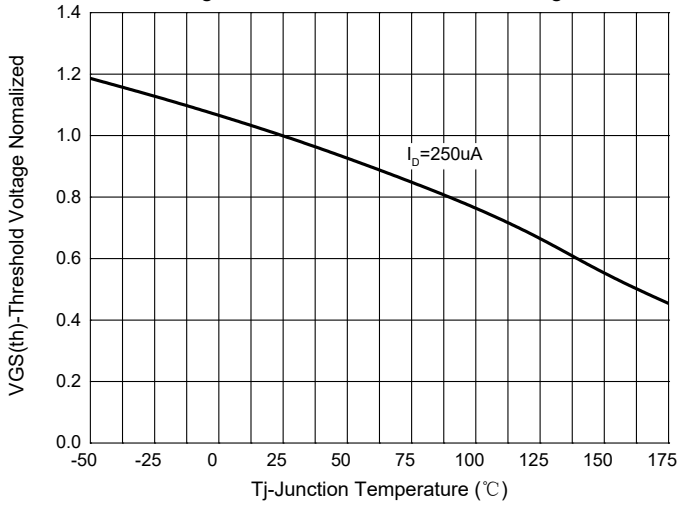


Fig.8-Normalized On Resistance Characteristics

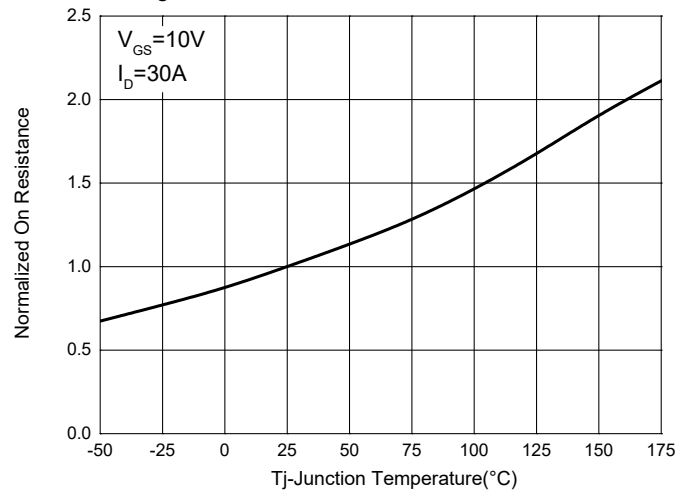


Fig.9 - I<sub>S</sub>—V<sub>SD</sub>

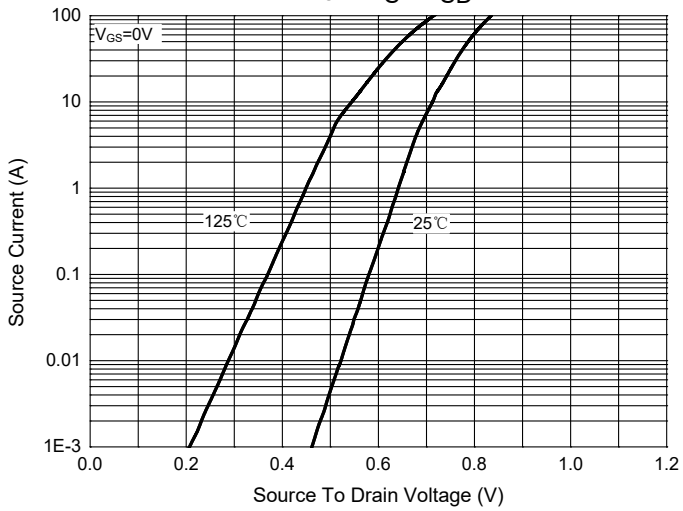


Fig. 10 - Drain Current

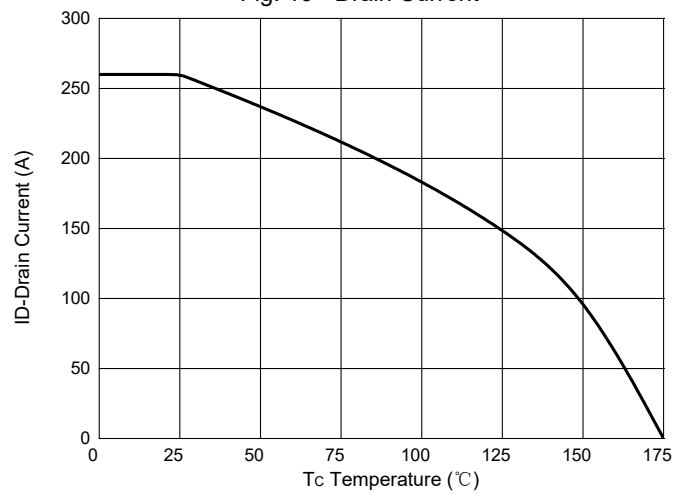
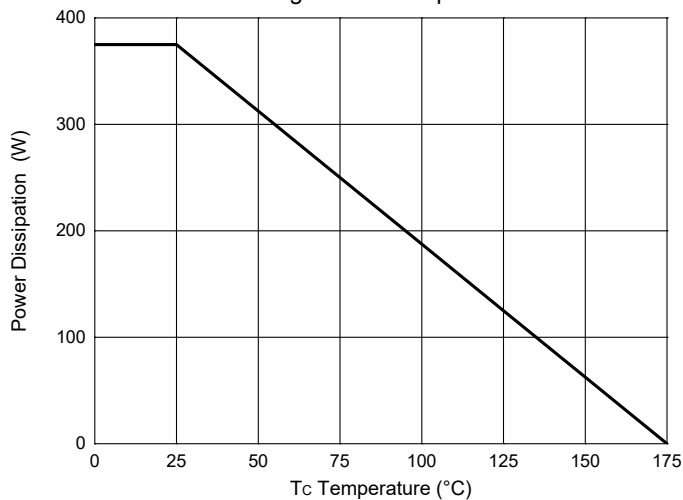


Fig.11-PD Dissipation



Curve Characteristics

Fig. 12 - Safe Operation Area

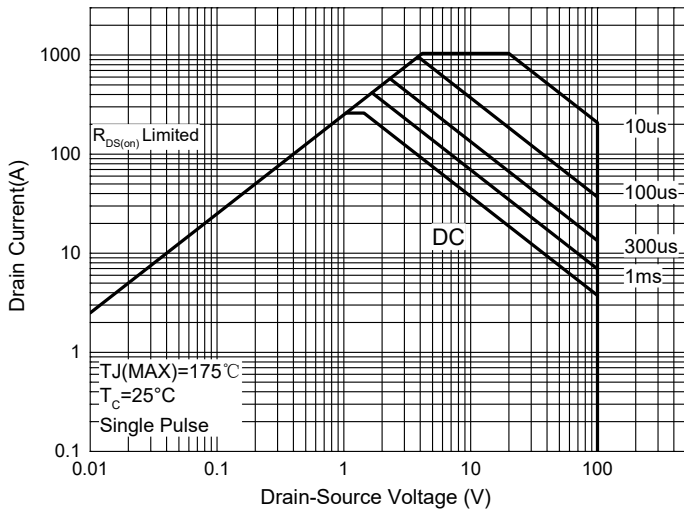
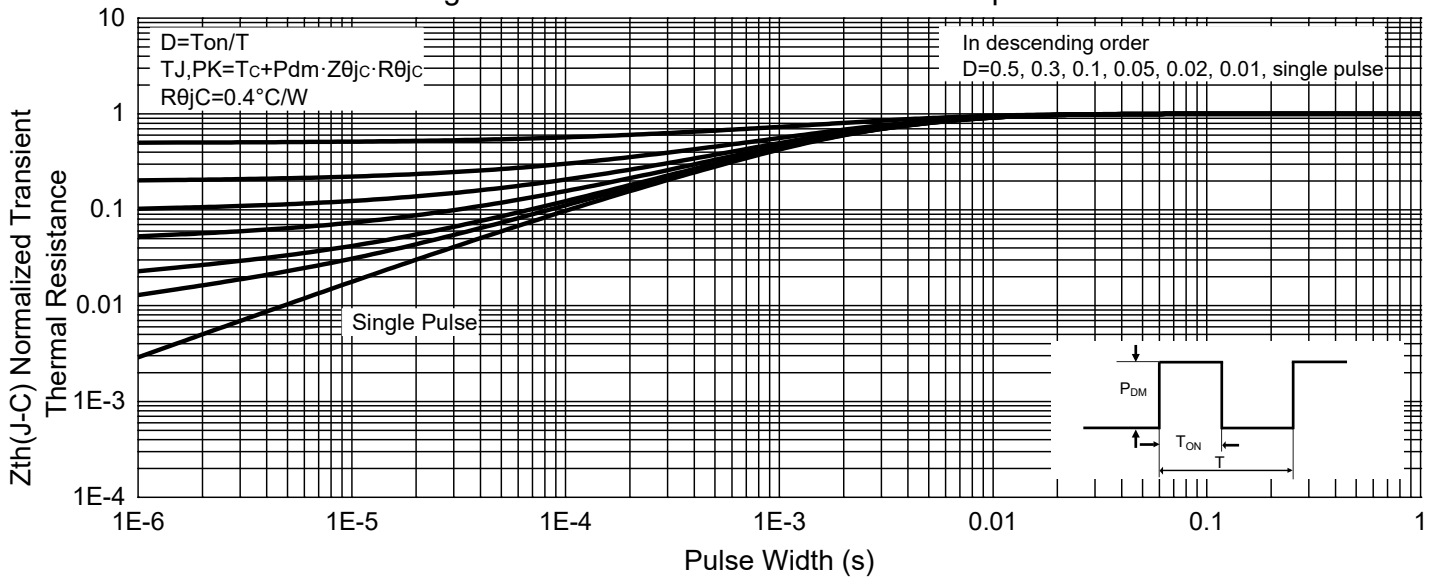


Fig. 13 - Normalized Transient Thermal Impedance



## Ordering Information

Device	Packing
Part Number-TP	Tape&Reel: 800pcs/Reel

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