

## Features

- Split Gate Trench MOSFET Technology
- Low Thermal Resistance
- Moisture Sensitivity Level 1
- Halogen Free. "Green" Device (Note1)
- Epoxy Meets UL 94 V-0 Flammability Rating
- Lead Free Finish/RoHS Compliant<sup>(Note2)</sup> ("P" Suffix Designates RoHS Compliant. See Ordering Information)

## Maximum Ratings

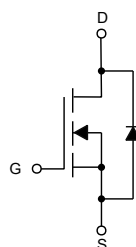
- Operating Junction Temperature Range : -55°C to +175°C
- Storage Temperature Range: -55°C to +175°C
- Thermal Resistance: 40°C/W Junction to Ambient<sup>(Note3)</sup>
- Thermal Resistance: 0.7°C/W Junction to Case

| Parameter                             |                       | Symbol          | Rating | Unit |
|---------------------------------------|-----------------------|-----------------|--------|------|
| Drain-Source Voltage                  |                       | V <sub>DS</sub> | 100    | V    |
| Gate-Source Voltage                   |                       | V <sub>GS</sub> | ±20    | V    |
| Continuous Drain Current              | T <sub>C</sub> =25°C  | I <sub>D</sub>  | 170    | A    |
|                                       | T <sub>C</sub> =100°C |                 | 120    |      |
| Pulsed Drain Current (Note4)          |                       | I <sub>DM</sub> | 680    | A    |
| Total Power Dissipation (Note5)       |                       | P <sub>D</sub>  | 214    | W    |
| Single Pulse Avalanche Energy (Note6) |                       | E <sub>AS</sub> | 900    | mJ   |

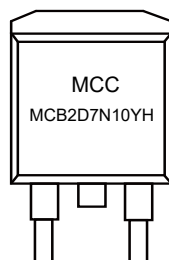
Note:

1. Halogen free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
2. High Temperature Solder Exemption Applied, see EU Directive Annex 7a.
3. The value of  $R_{\theta JA}$  is measured with the device mounted on 1in<sup>2</sup> FR-4 board with 2oz. Copper, in a still air environment with  $T_A = 25^\circ\text{C}$ .
4. Repetitive rating; pulse width limited by max. junction temperature.
5.  $P_D$  is based on max. junction temperature, using junction-case thermal resistance.
6.  $T_J = 25^\circ\text{C}$ ,  $V_{DD} = 100\text{V}$ ,  $V_{GS} = 10\text{V}$ ,  $R_G = 25\Omega$ ,  $L = 2\text{mH}$ .

## Internal Structure and Marking Code

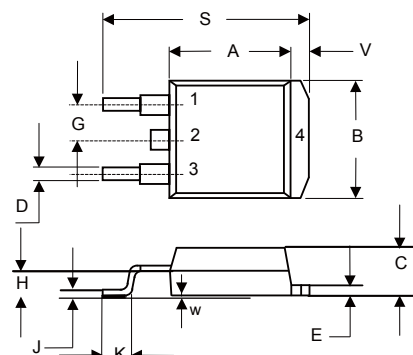


1. Gate
- 2,4. Drain
3. Source



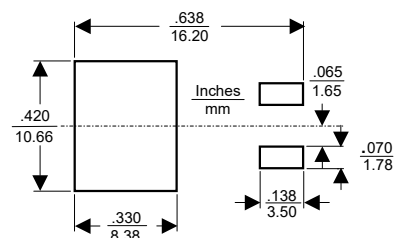
## N-Channel MOSFET

## D2-PAK



| DIM | INCHES |       | MM    |       | NOTE |
|-----|--------|-------|-------|-------|------|
|     | MIN    | MAX   | MIN   | MAX   |      |
| A   | 0.331  | 0.370 | 8.40  | 9.40  |      |
| B   | 0.378  | 0.417 | 9.60  | 10.60 |      |
| C   | 0.165  | 0.189 | 4.20  | 4.80  |      |
| D   | 0.027  | 0.037 | 0.68  | 0.94  |      |
| E   | 0.045  | 0.055 | 1.14  | 1.40  |      |
| G   | 0.1    |       | 2.54  |       | TYP. |
| H   | 0.096  | 0.134 | 2.43  | 3.40  |      |
| J   | 0.011  | 0.025 | 0.28  | 0.64  |      |
| K   | 0.071  | 0.131 | 1.80  | 3.32  |      |
| S   | 0.575  | 0.625 | 14.60 | 15.87 |      |
| V   | 0.042  | 0.058 | 1.07  | 1.47  |      |
| W   | 0.000  | 0.010 | 0.00  | 0.25  |      |

## Suggested Solder Pad Layout

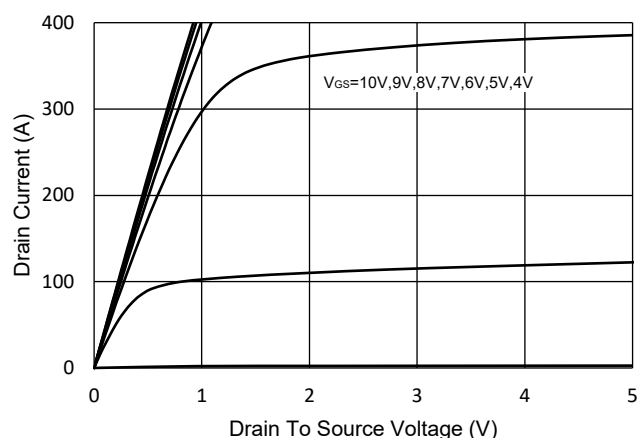


**Electrical Characteristics @ 25°C (Unless Otherwise Specified)**

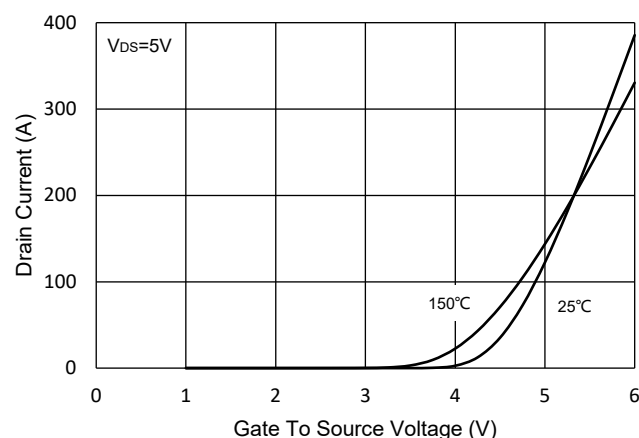
| Parameter                       | Symbol               | Test Conditions                                                                     | Min | Typ   | Max  | Unit |
|---------------------------------|----------------------|-------------------------------------------------------------------------------------|-----|-------|------|------|
| Static Characteristics          |                      |                                                                                     |     |       |      |      |
| Drain-Source Breakdown Voltage  | V <sub>(BR)DSS</sub> | V <sub>GS</sub> =0V, I <sub>D</sub> =250μA                                          | 100 |       |      | V    |
| Gate-Source Leakage Current     | I <sub>GSS</sub>     | V <sub>DS</sub> =0V, V <sub>GS</sub> =±20V                                          |     |       | ±100 | nA   |
| Zero Gate Voltage Drain Current | I <sub>DSS</sub>     | V <sub>DS</sub> =100V, V <sub>GS</sub> =0V                                          |     |       | 1    | μA   |
| Gate-Threshold Voltage          | V <sub>GS(th)</sub>  | V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250μA                            | 2.2 | 3.0   | 3.8  | V    |
| Drain-Source On-Resistance      | R <sub>DS(on)</sub>  | V <sub>GS</sub> =10V, I <sub>D</sub> =50A                                           |     | 2.0   | 2.7  | mΩ   |
| Gate Resistance                 | R <sub>g</sub>       | f=1 MHz, Open drain                                                                 |     | 0.8   |      | Ω    |
| Diode Characteristics           |                      |                                                                                     |     |       |      |      |
| Continuous Body Diode Current   | I <sub>S</sub>       |                                                                                     |     |       | 170  | A    |
| Diode Forward Voltage           | V <sub>SD</sub>      | V <sub>GS</sub> =0V, I <sub>S</sub> =50A                                            |     |       | 1.2  | V    |
| Reverse Recovery Time           | t <sub>rr</sub>      | I <sub>F</sub> =50A, dI/dt=100A/μs                                                  |     | 93    |      | ns   |
| Reverse Recovery Charge         | Q <sub>rr</sub>      |                                                                                     |     | 157   |      | nC   |
| Dynamic Characteristics         |                      |                                                                                     |     |       |      |      |
| Input Capacitance               | C <sub>iss</sub>     | V <sub>DS</sub> =50V, V <sub>GS</sub> =0V, f=1MHz                                   |     | 9440  |      | pF   |
| Output Capacitance              | C <sub>oss</sub>     |                                                                                     |     | 3180  |      |      |
| Reverse Transfer Capacitance    | C <sub>rss</sub>     |                                                                                     |     | 50    |      |      |
| Total Gate Charge               | Q <sub>g</sub>       | V <sub>DS</sub> =50V, V <sub>GS</sub> =10V, I <sub>D</sub> =50A                     |     | 132.5 |      | nC   |
| Gate-Source Charge              | Q <sub>gs</sub>      |                                                                                     |     | 55    |      |      |
| Gate-Drain Charge               | Q <sub>gd</sub>      |                                                                                     |     | 17    |      |      |
| Turn-On Delay Time              | t <sub>d(on)</sub>   | V <sub>DD</sub> =50V, V <sub>GS</sub> =10V, R <sub>G</sub> =3Ω, I <sub>D</sub> =50A |     | 30    |      | ns   |
| Turn-On Rise Time               | t <sub>r</sub>       |                                                                                     |     | 48    |      |      |
| Turn-Off Delay Time             | t <sub>d(off)</sub>  |                                                                                     |     | 59    |      |      |
| Turn-Off Fall Time              | t <sub>f</sub>       |                                                                                     |     | 34    |      |      |

## Curve Characteristics

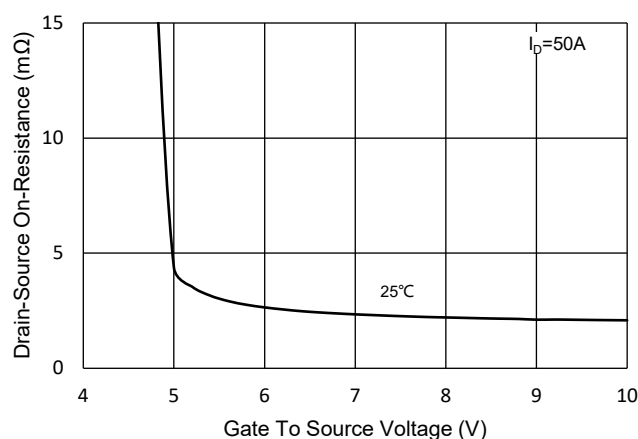
**Fig.1 - Typical Output Characteristics**



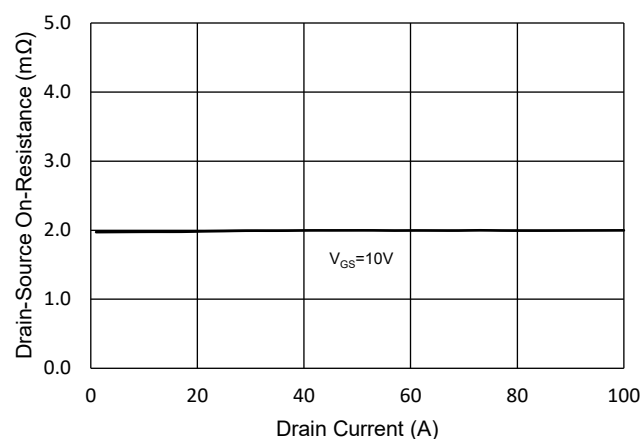
**Fig.2 - Transfer Characteristics**



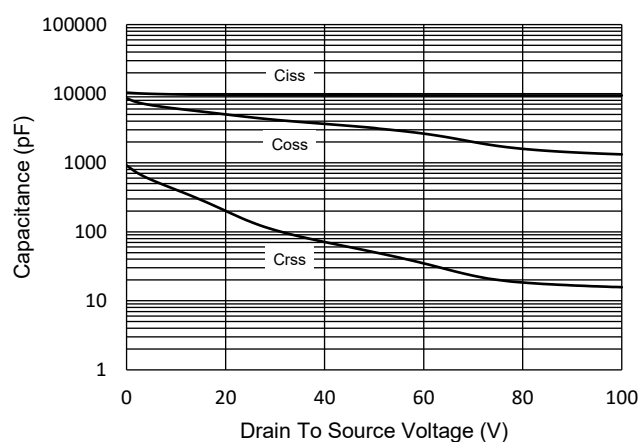
**Fig.3 -  $R_{DS(ON)}$  -  $V_{GS}$**



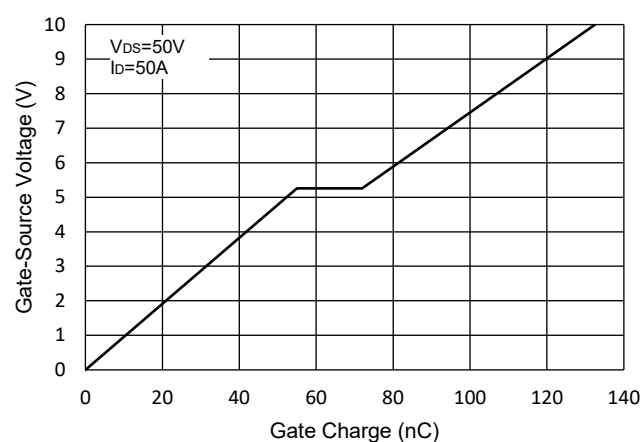
**Fig.4 -  $R_{DS(ON)}$  -  $I_D$**



**Fig.5 - Capacitance Characteristics**



**Fig.6 - Gate Charge**



Curve Characteristics

Fig.7 - Normalized Threshold Voltage

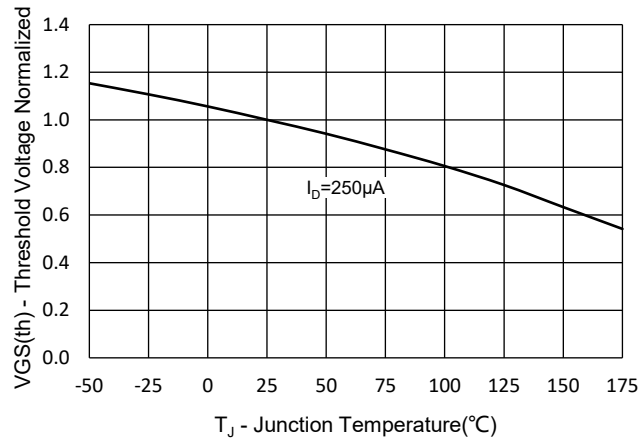


Fig.8 - Normalized On Resistance Characteristics

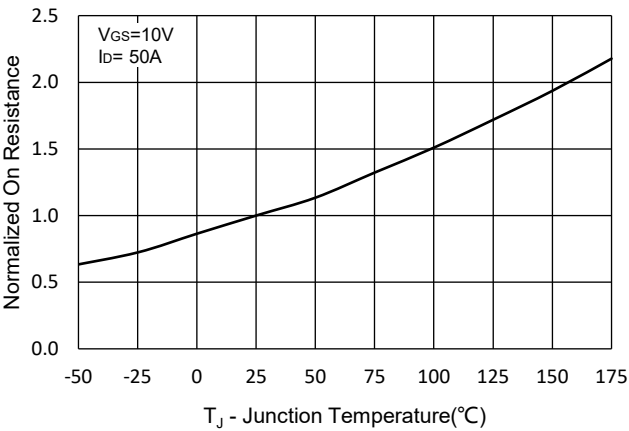


Fig.9 -  $I_S - V_{SD}$

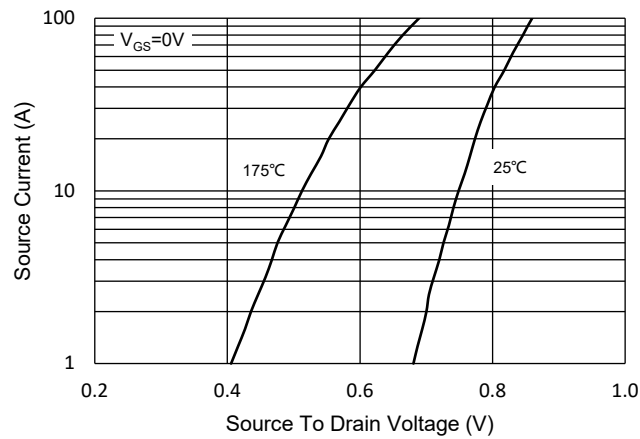


Fig.10 - Drain Current

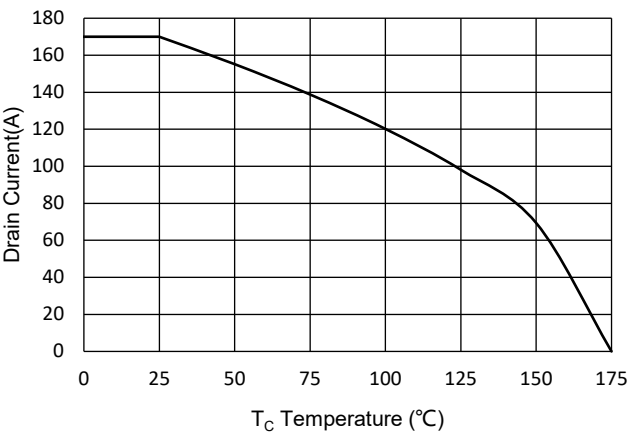
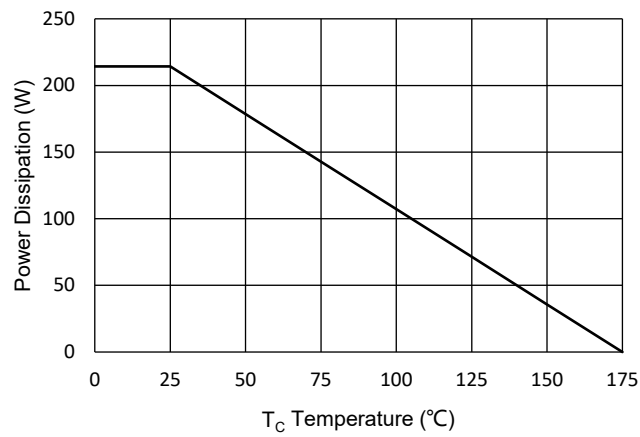


Fig.11 - PD Dissipation



## Curve Characteristics

Fig.12 - Safe Operation Area

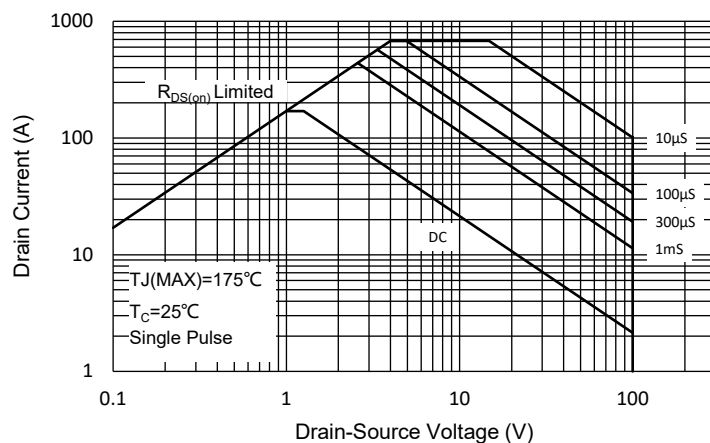
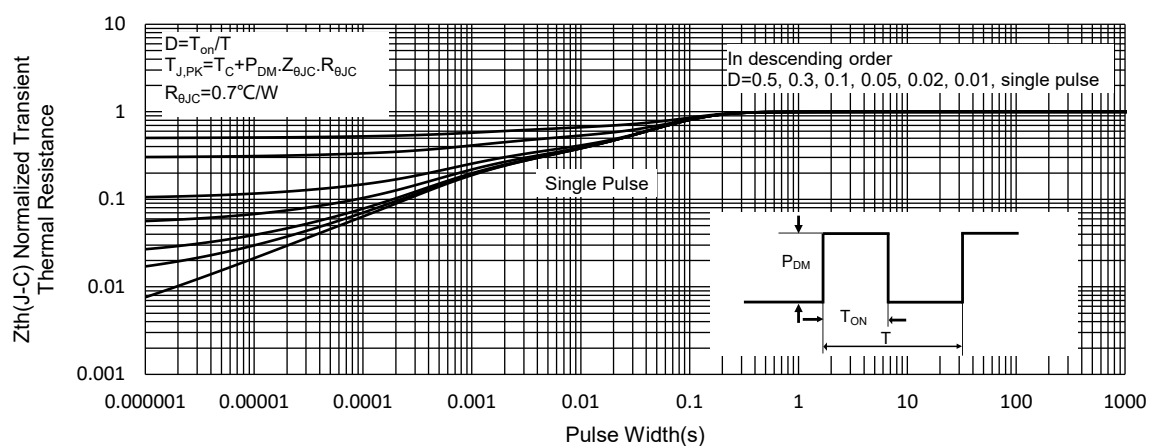


Fig.13 - Normalized Transient Thermal Impedance



## Ordering Information

| Device         | Packing                |
|----------------|------------------------|
| Part Number-TP | Tape&Reel: 800pcs/Reel |

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