

Features

- Excellent Stability and Uniformity
- Split Gate Trench Mosfet Technology
- Lower $R_{DS(ON)}$
- Epoxy Meets UL 94 V-0 Flammability Rating
- Halogen Free "Green" Device (Note 1)
- Lead Free Finish/RoHS Compliant ("P" Suffix Designates RoHS Compliant. See Ordering Information)
- Moisture Sensitivity Level 1

Maximum Ratings

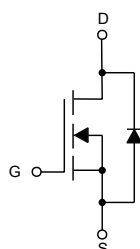
- Operating Junction Temperature Range: -55°C to +150°C
- Storage Temperature Range: -55°C to +150°C
- Thermal Resistance: 28°C/W Junction to Ambient
- Thermal Resistance: 0.48°C/W Junction to Case

Parameter		Symbol	Rating	Unit
Drain -Source Voltage		V _{DS}	60	V
Gate -Source Voltlage		V _{GS}	±20	V
Drain Current-Continuous	T _C =25°C	I _D	200	A
	T _C =100°C		125	
Drain Current-Pulse ^(Note2)		I _{DM}	600	A
Power Dissipation		P _D	260	W
Single Pulsed Avalanche Energy ^(Note3)		E _{AS}	506	mJ

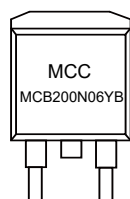
Notes:

1. Halogen free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
2. Pulse Width Limited by Maximum Junction Temperature.
3. EAS Condition: $T_J=25^\circ\text{C}$, $V_{DD}=50\text{V}$, $V_G=10\text{V}$, $R_g=25\Omega$, $L=0.5\text{mH}$, $I_{AS}=45\text{A}$.

Internal Structure and Marking Code

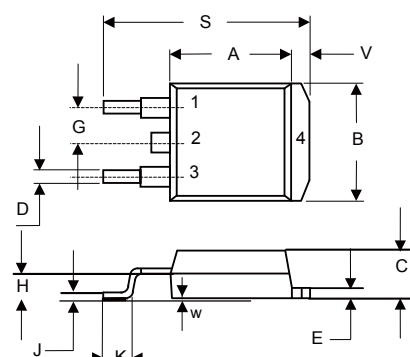


1.GATE
2.DRAIN
3.SOURCE



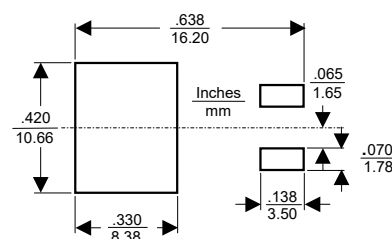
N-CHANNEL MOSFET

D2-PAK



DIMENSIONS					
DIM	INCHES		MM		NOTE
	MIN	MAX	MIN	MAX	
A	0.331	0.370	8.40	9.40	
B	0.378	0.417	9.60	10.60	
C	0.165	0.189	4.20	4.80	
D	0.027	0.037	0.68	0.94	
E	0.045	0.055	1.14	1.40	
G	0.010		2.54		TYP.
H	0.096	0.134	2.43	3.40	
J	0.011	0.025	0.28	0.64	
K	0.071	0.131	1.80	3.32	
S	0.575	0.625	14.60	15.87	
V	0.042	0.058	1.07	1.47	
W	0.000	0.010	0.00	0.25	

Suggested Solder Pad Layout



Electrical Characteristics @ 25°C (Unless Otherwise Noted)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Static Characteristics						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS}=0V, I_D=250\mu A$	60			V
Gate-Source Leakage Current	I_{GSS}	$V_{GS}=\pm 20V$			± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS}=60V, V_{GS}=0V$			1	μA
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=250\mu A$	1.2	1.6	2.2	V
Drain-Source On-Resistance ^(Note4)	$R_{DS(on)}$	$V_{GS}=10V, I_D=20A$		2.35	2.9	m Ω
		$V_{GS}=4.5V, I_D=15A$		2.9	3.9	
Gate resistance	R_G	$V_{GS}=0V, f=1MHz$		2		Ω
Body Diode Voltage	V_{SD}	$I_{SD}=20A, V_{GS}=0V$			1.2	V
Dynamic Characteristics ^(Note 5)						
Input Capacitance	C_{iss}	$V_{DS}=25V, V_{GS}=0V, f=1MHz$		5950		pF
Output Capacitance	C_{oss}			1250		
Reverse Transfer Capacitance	C_{rss}			85		
Total Gate Charge	Q_g	$V_{DS}=50V, V_{GS}=10V, I_D=50A$		93		nC
Gate-Source Charge	Q_{gs}			17		
Gate-Drain Charge	Q_{gd}			14		
Reverse Recovery Charge	Q_{rr}	$I_F=25A, di/dt=100A/\mu s$		73		
Reverse Recovery Time	t_{rr}			68		
Turn-On Delay Time	$t_{d(on)}$	$V_{DS}=30V, I_D=25A, V_{GS}=10V, R_G=2\Omega$		22.5		ns
Turn-On Rise Time	t_r			6.7		
Turn-Off Delay Time	$t_{d(off)}$			80.3		
Turn-Off Fall Time	t_f			26.9		

Note 4. Pulse Test : Pulse Width $\leq 300\mu s$, Duty Cycle $\leq 2\%$.

5. Guaranteed by Design, Not Subject to Production Testing.

Curve Characteristics

Figure1. Output Characteristics

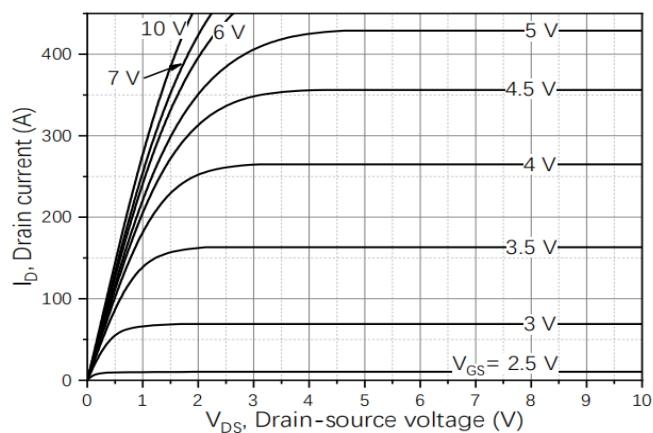


Figure2. Transfer Characteristics

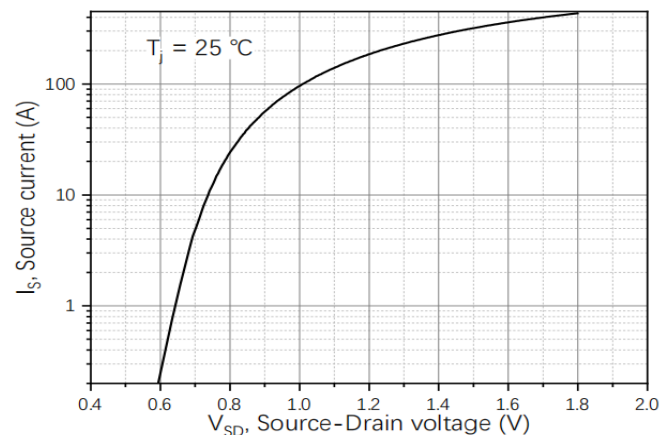


Figure3. Capacitance Characteristics

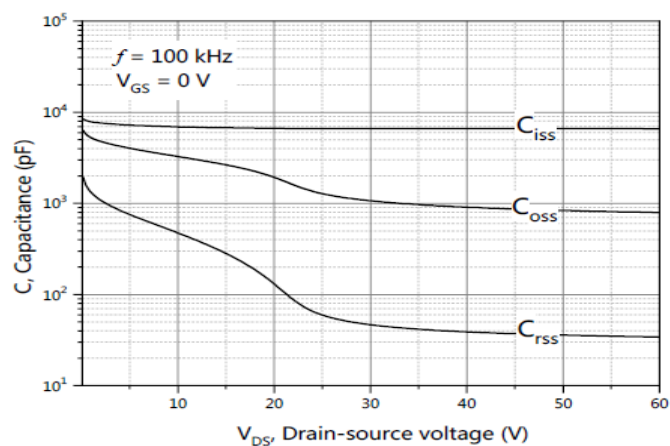


Figure4. Gate Charge

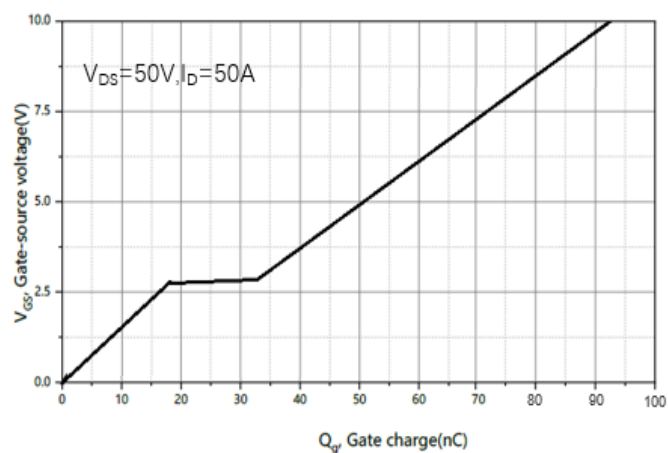


Figure5. Drain-Source on Resistance

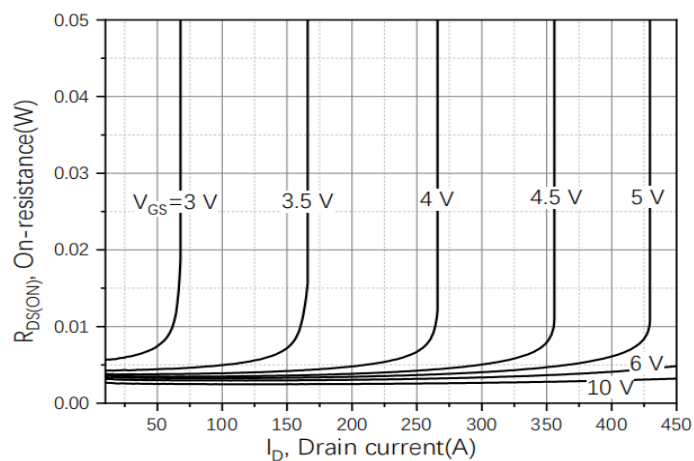
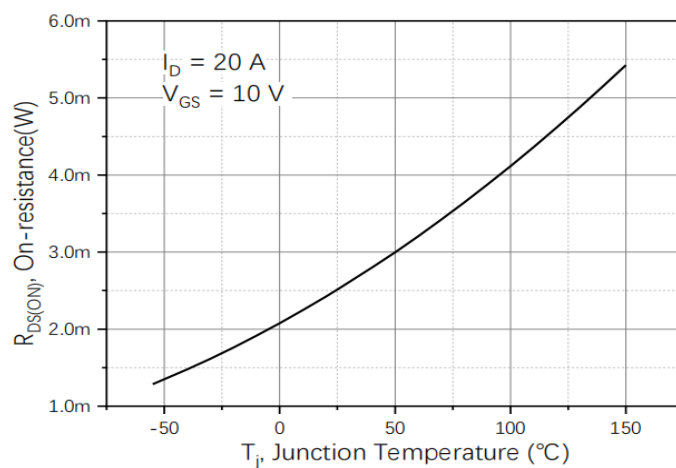


Figure6. Drain-Source on Resistance



Curve Characteristics

Figure7. Drain-source breakdown voltage

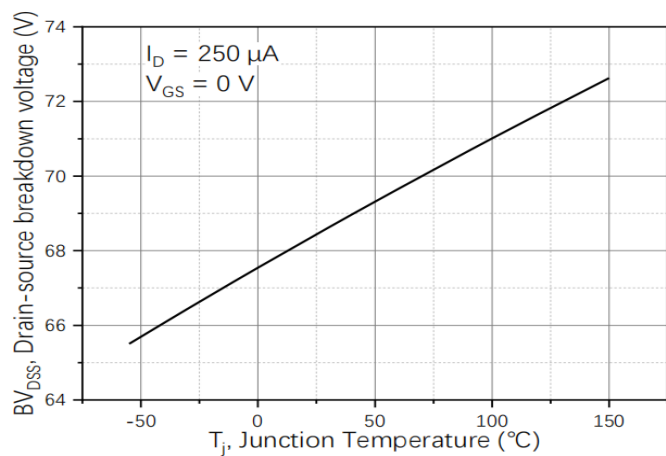
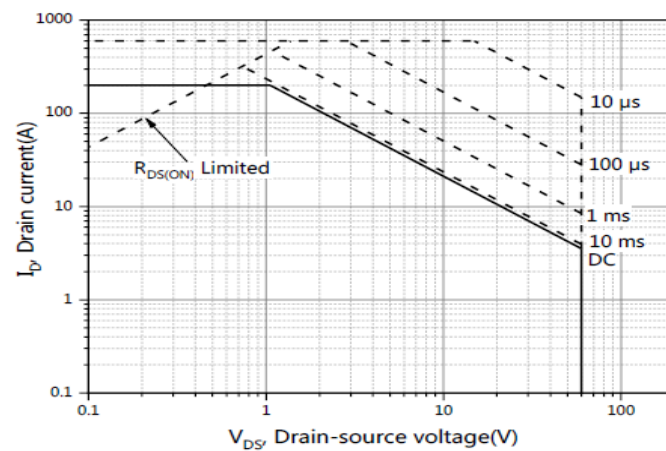


Figure8. Safe Operation Area



Ordering Information

Device	Packing
Part Number-TP	Tape&Reel: 800pcs/Reel

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