

Features

- Split Gate Trench MOSFET Technology
- High Density Cell Design For Ultra Low $R_{DS(on)}$
- Moisture Sensitivity Level 1
- Halogen Free."Green"Device^(Note1)
- Epoxy Meets UL 94 V-0 Flammability Rating
- Lead Free Finish/RoHS Compliant ("P" Suffix Designates RoHS Compliant. See Ordering Information)

Maximum Ratings

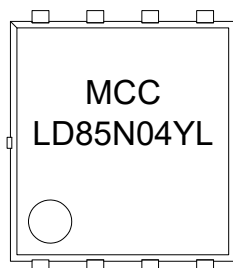
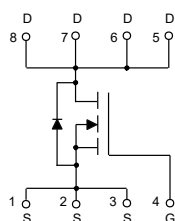
- Operating Junction Temperature Range: -55°C to +175°C
- Storage Temperature Range: -55°C to +175°C
- Thermal Resistance: 50°C/W Junction to Ambient^(Note2)
- Thermal Resistance: 1.1°C/W Junction to Case

Parameter		Symbol	Rating	Unit
Drain-Source Voltage		V _{DS}	40	V
Gate-Source Voltage		V _{GS}	±20	V
Continuous Drain Current	T _C =25°C	I _D	300	A
	T _C =100°C		212	
Pulsed Drain Current ^(Note 3)		I _{DM}	1200	A
Total Power Dissipation ^(Note 4)		P _D	136	W
Single Pulsed Avalanche Energy ^(Note 5)		E _{AS}	1250	mJ

Note:

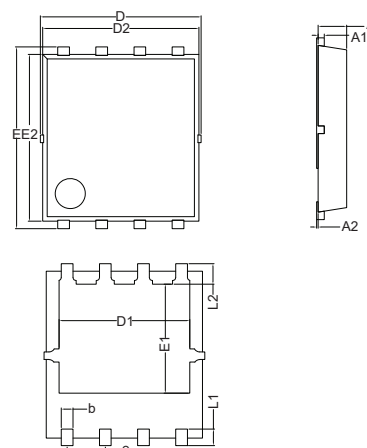
1. Halogen free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
2. The value of $R_{\theta JA}$ is measured with the device mounted on 1in² FR-4 board with 2oz. Copper, in a still air environment with $T_A=25^\circ\text{C}$. The Power dissipation P_{DSM} is based on $R_{\theta JA} t \leq 10\text{s}$ and the maximum allowed junction temperature of 175°C. The value in any given application depends on the user's specific board design.
3. Repetitive rating; pulse width limited by max. junction temperature.
4. P_D is based on max. junction temperature, using junction-case thermal resistance.
5. $T_J=25^\circ\text{C}$, $V_{DD}=40\text{V}$, $V_{GS}=10\text{V}$, $R_G=25\Omega$, $L=4\text{mH}$.

Internal Structure and Marking Code



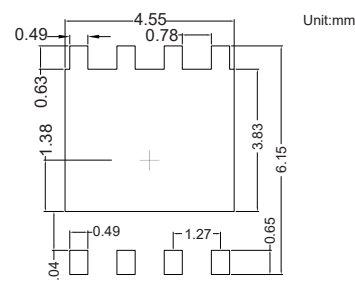
N-Channel MOSFET

DFN5060-C



DIMENSIONS					
DIM	INCHES		MM		NOTE
	MIN	MAX	MIN	MAX	
D	0.203	0.218	5.15	5.55	
D2	0.201	0.209	5.10	5.30	
E	0.234	0.242	5.95	6.15	
E2	0.215	0.222	5.45	5.65	
A	0.033	0.041	0.85	1.05	
A1	0.008		0.203		BSC
A2	0.000	0.004	0.00	0.10	
D1	0.167	0.175	4.25	4.45	
E1	0.139	0.147	3.52	3.73	
L1	0.018	0.026	0.45	0.65	
L2	0.027		0.68		BSC
b	0.012	0.020	0.30	0.50	
e	0.050		1.27		BSC

Suggested Solder Pad Layout



Electrical Characteristics @ 25°C (Unless Otherwise Specified)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Static Characteristics						
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} =0V, I _D =1mA	40			V
Gate-Source Leakage Current	I _{GSS}	V _{DS} =0V, V _{GS} =±20V			±100	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =40V, V _{GS} =0V			1	μA
Gate-Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =250μA	1	1.5	2.5	V
Drain-Source On-Resistance	R _{DS(on)}	V _{GS} =10V, I _D =20A		0.65	0.85	mΩ
		V _{GS} =4.5V, I _D =20A		1	1.4	
Gate Resistance	R _g	f=1 MHz, Open drain		2		Ω
Diode Characteristics						
Continuous Body Diode Current	I _S				300	A
Diode Forward Voltage	V _{SD}	V _{GS} =0V, I _S =50A			1.2	V
Reverse Recovery Time	t _{rr}	I _F =50A, dI/dt=100A/μs		69		ns
Reverse Recovery Charge	Q _{rr}			94		nC
Dynamic Characteristics						
Input Capacitance	C _{iss}	V _{DS} =20V, V _{GS} =0V, f=1MHz		6125		pF
Output Capacitance	C _{oss}			3250		
Reverse Transfer Capacitance	C _{rss}			150		
Total Gate Charge	Q _g	V _{DS} =20V, V _{GS} =10V, I _D =50A		101		nC
Gate-Source Charge	Q _{gs}			21		
Gate-Drain Charge	Q _{gd}			21		
Turn-On Delay Time	t _{d(on)}	V _{DD} =20V, V _{GS} =10V, R _G =3Ω, I _D =50A		16		ns
Turn-On Rise Time	t _r			44		
Turn-Off Delay Time	t _{d(off)}			71		
Turn-Off Fall Time	t _f			45		

Curve Characteristics

Fig.1 - Typical Output Characteristics

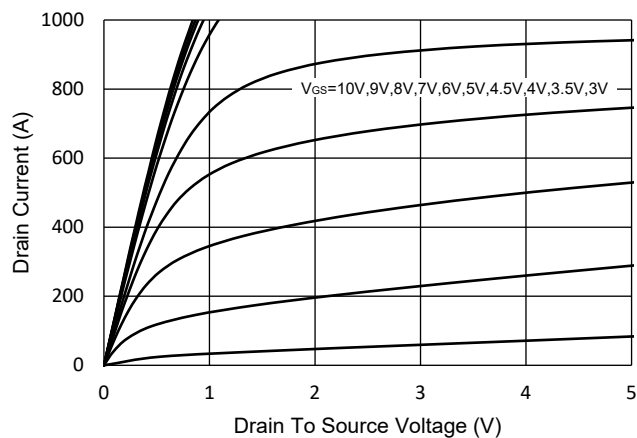


Fig.2 - Transfer Characteristics

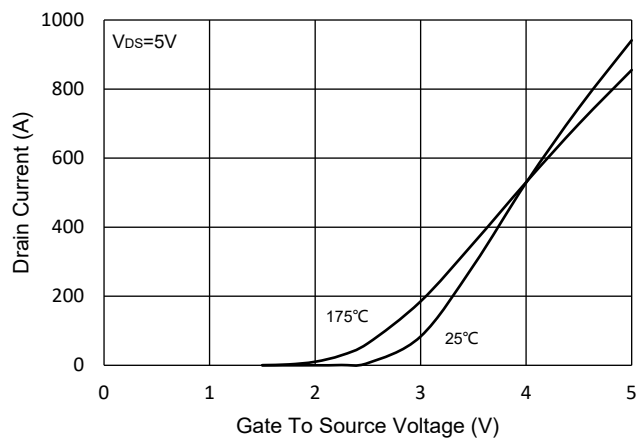


Fig.3 - $R_{DS(ON)} - V_{GS}$

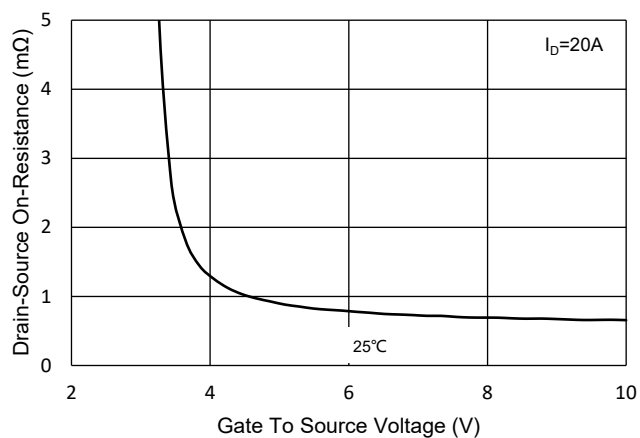


Fig.4 - $R_{DS(ON)} - I_D$

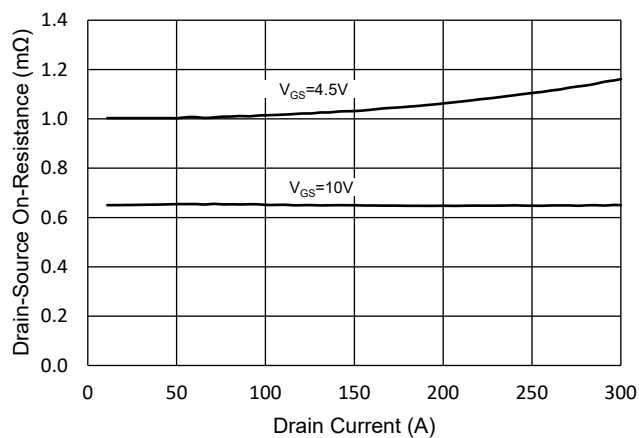


Fig.5 - Capacitance Characteristics

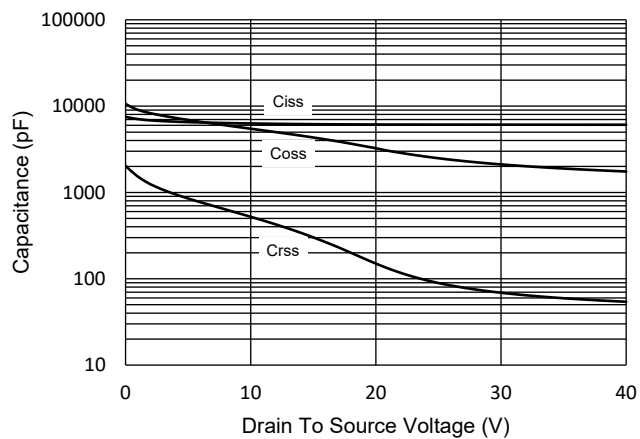
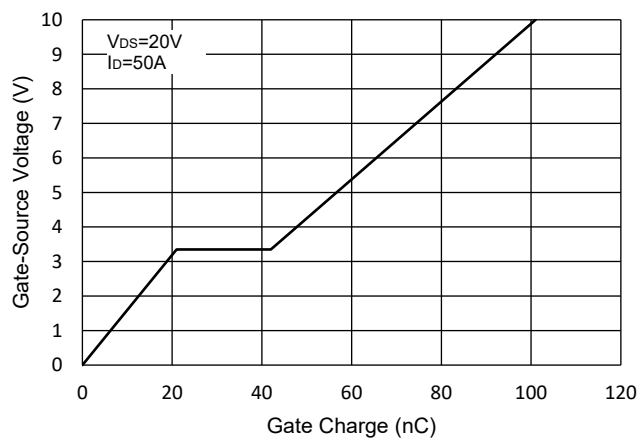


Fig.6 - Gate Charge



Curve Characteristics

Fig.7 - Normalized Threshold Voltage

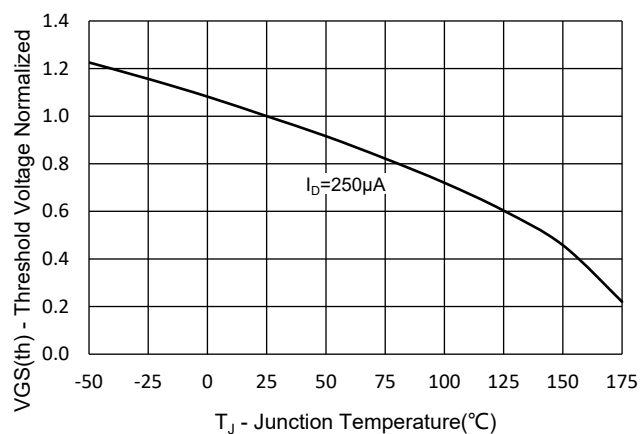


Fig.8 - Normalized On Resistance Characteristics

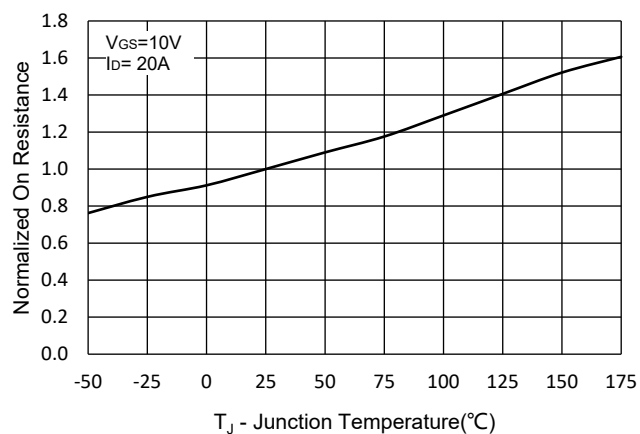


Fig.9 - $I_S - V_{SD}$

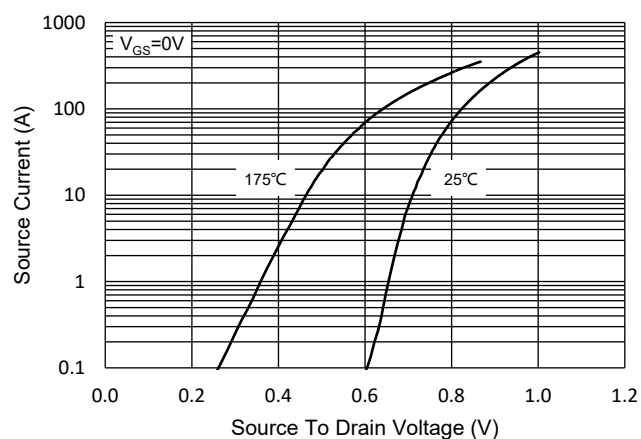


Fig.10 - Drain Current

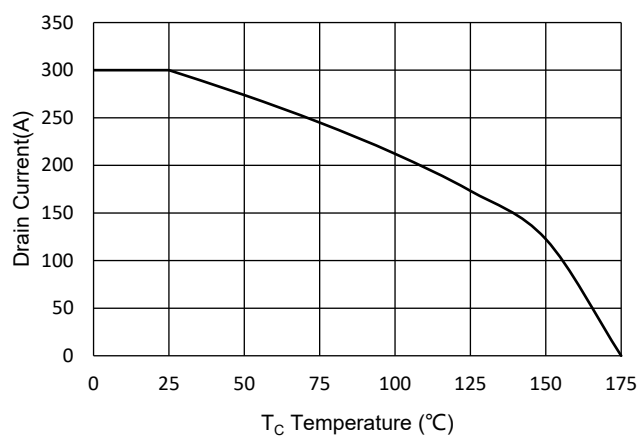
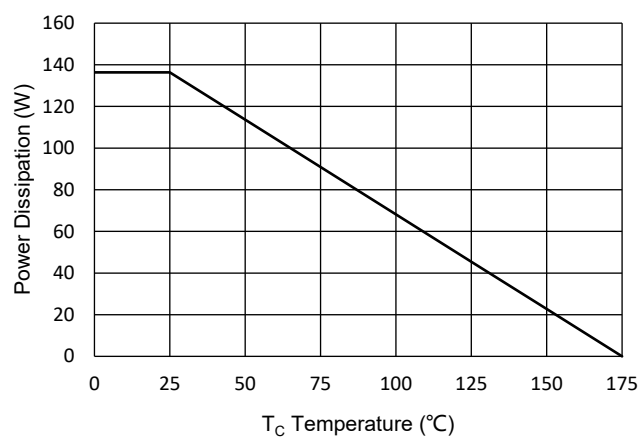


Fig.11 - PD Dissipation



Curve Characteristics

Fig.12 - Safe Operation Area

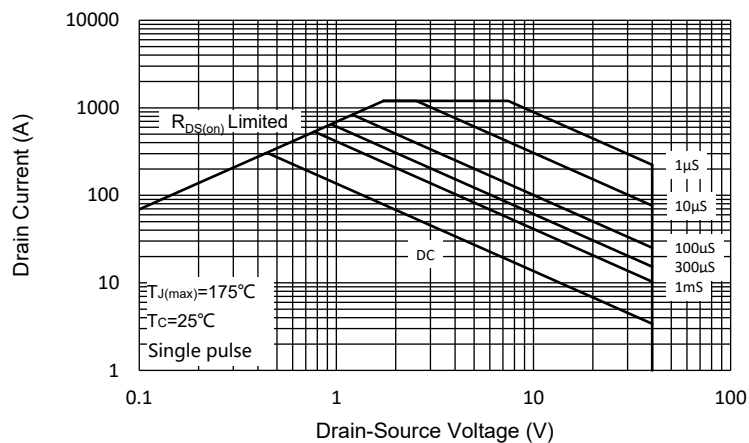
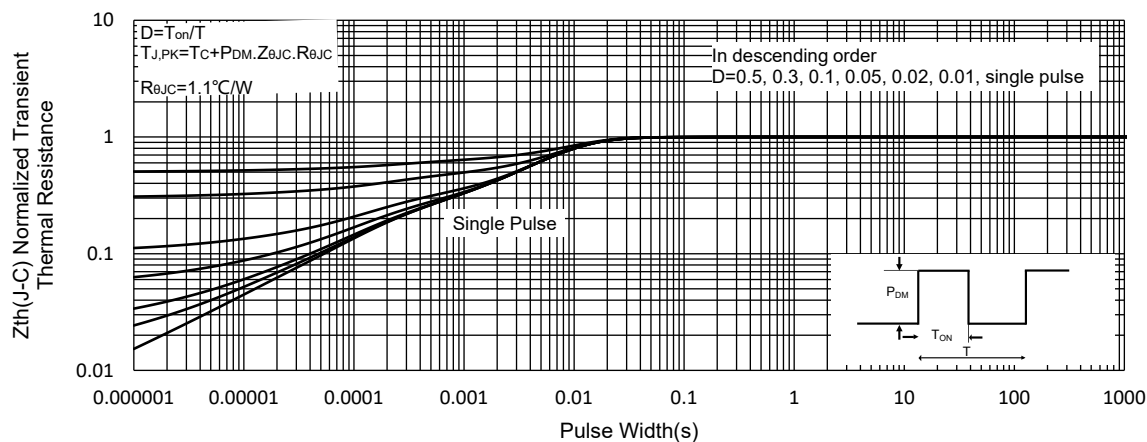


Fig.13 - Normalized Transient Thermal Impedance



Ordering Information

Device	Packing
Part Number-TP	Tape&Reel: 5Kpcs/Reel

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