

Features

- AEC-Q101 Qualified
- Split Gate Trench MOSFET Technology
- High Density Cell Design For Low $R_{DS(on)}$
- Moisture Sensitivity Level 1
- Halogen Free "Green" Device^(Note1)
- Epoxy Meets UL 94 V-0 Flammability Rating
- Lead Free Finish/RoHS Compliant^(Note2) ("P" Suffix Designates RoHS Compliant. See Ordering Information)

Maximum Ratings

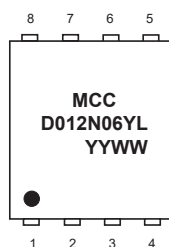
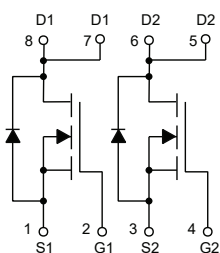
- Operating Junction Temperature Range : -55°C to +175°C
- Storage Temperature Range: -55°C to +175°C
- Thermal Resistance: 62°C/W Junction to Ambient^(Note3)
- Thermal Resistance: 2.7°C/W Junction to Case

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	V_{DS}	60	V
Gate-Source Voltage	V_{GS}	±20	V
Continuous Drain Current	I_D	$T_C=25^\circ\text{C}, V_{GS}=10\text{V},$ Chip Limitation	52
		$T_C=100^\circ\text{C}, V_{GS}=10\text{V}$	37
Pulsed Drain Current ^(Note4)	I_{DM}	208	A
Total Power Dissipation ^(Note5)	P_D	55	W
Single Pulse Avalanche Energy ^(Note 6)	E_{AS}	36	mJ

Note:

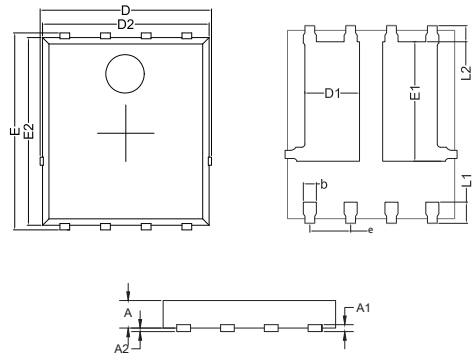
1. Halogen free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
2. High Temperature Solder Exemption Applied, see EU Directive Annex 7(a)-I.
3. The value of $R_{\theta JA}$ is measured with the device mounted on 1in² FR-4 board with 2oz. Copper, in a still air environment with $T_A=25^\circ\text{C}$.
4. Repetitive rating; pulse width limited by max. junction temperature.
5. P_D is based on max. junction temperature, using junction-case thermal resistance.
6. $T_J=25^\circ\text{C}, V_{DD}=50\text{V}, V_{GS}=10\text{V}, R_G=25\Omega, L=0.5\text{mH}$.

Internal Structure and Marking Code



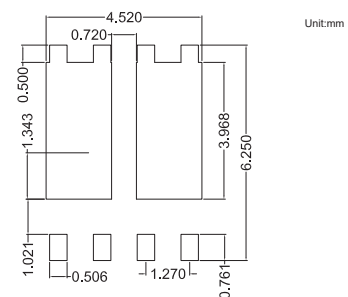
Dual N-CHANNEL MOSFET

PDFN5060-8D



DIM	DIMENSIONS				NOTE
	INCHES		MM		
	MIN	MAX	MIN	MAX	
D	0.203	0.218	5.15	5.55	
D2	0.197	0.213	5.00	5.40	
E	0.234	0.250	5.95	6.35	
E2	0.223	0.238	5.66	6.06	
A	0.039	0.047	1.00	1.20	
A1	0.010		0.254		BSC
A2	0.000	0.004	0.00	0.10	
D1	0.059	0.075	1.50	1.90	
E1	0.139	0.154	3.52	3.92	
L1	0.022	0.030	0.56	0.76	
L2	0.019		0.50		BSC
b	0.012	0.020	0.31	0.51	
e	0.050		1.27		BSC

Suggested Solder Pad Layout



Electrical Characteristics @ 25°C (Unless Otherwise Specified)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Static Characteristics						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS}=0V, I_D=250\mu A$	60			V
Gate-Source Leakage Current	I_{GSS}	$V_{DS}=0V, V_{GS}=\pm 20V$			± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS}=60V, V_{GS}=0V$			1	μA
Gate-Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=250\mu A$	1.3	1.8	2.4	V
Drain-Source On-Resistance	$R_{DS(on)}$	$V_{GS}=10V, I_D=20A$		8.1	12	m Ω
		$V_{GS}=4.5V, I_D=15A$		11.3	16	
Gate Resistance	R_g	$f=1MHz, \text{Open Drain}$		1.7		Ω
Diode Characteristics						
Continuous Body Diode Current	I_S				52	A
Diode Forward Voltage	V_{SD}	$V_{GS}=0V, I_S=20A$			1.2	V
Reverse Recovery Time	t_{rr}	$I_F=20A, di_F/dt=100A/\mu s$		17.7		ns
Reverse Recovery Charge	Q_{rr}			9		nC
Dynamic Characteristics						
Input Capacitance	C_{iss}	$V_{DS}=30V, V_{GS}=0V, f=1MHz$		910		pF
Output Capacitance	C_{oss}			316		
Reverse Transfer Capacitance	C_{rss}			17		
Total Gate Charge	Q_g	$V_{DS}=30V, V_{GS}=10V, I_D=20A$		15.8		nC
Gate-Source Charge	Q_{gs}			3.1		
Gate-Drain Charge	Q_{gd}			3.5		
Turn-On Delay Time	$t_{d(on)}$	$V_{DD}=30V, V_{GS}=10V, I_{DS}=20A, R_G=3\Omega$		8		ns
Turn-On Rise Time	t_r			44.6		
Turn-Off Delay Time	$t_{d(off)}$			19.8		
Turn-Off Fall Time	t_f			6.4		

Curve Characteristics

Fig. 1 - Typical Output Characteristics

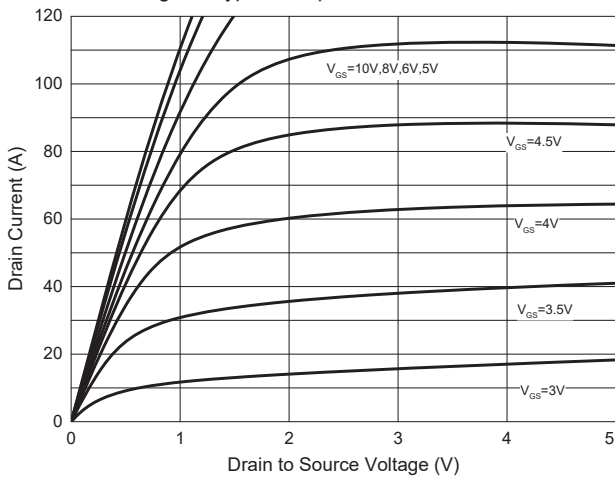


Fig. 2 - Transfer Characteristics

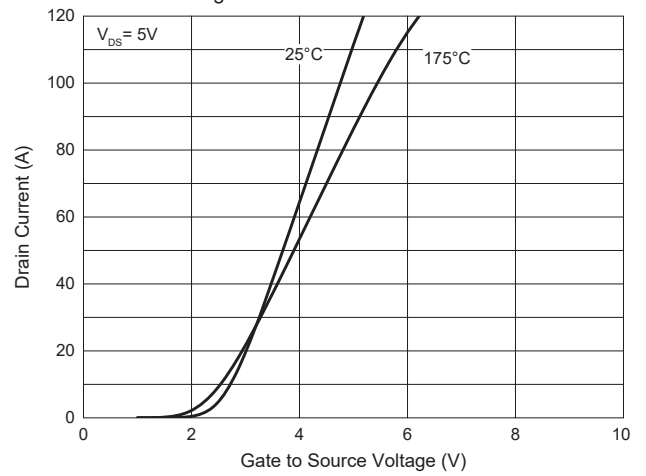


Fig. 3 - $R_{DS(ON)} - V_{GS}$

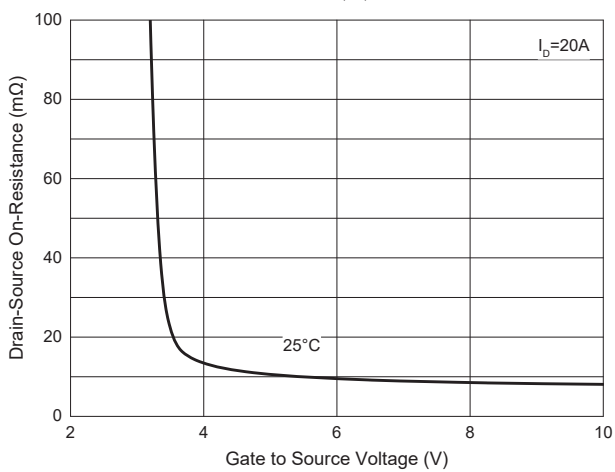


Fig. 4 - $R_{DS(ON)} - I_D$

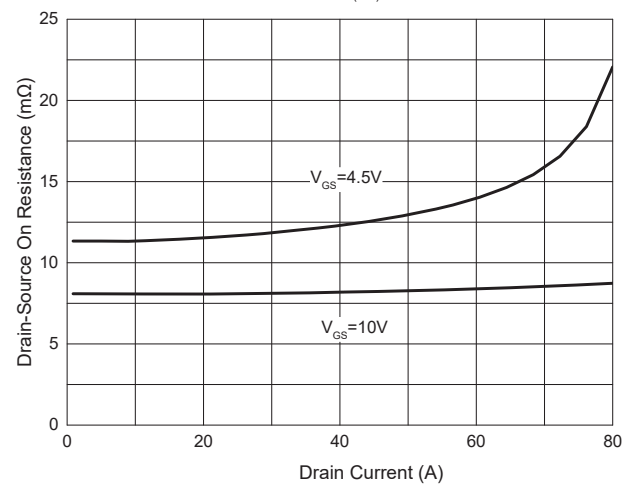


Fig. 5 - Capacitance Characteristics

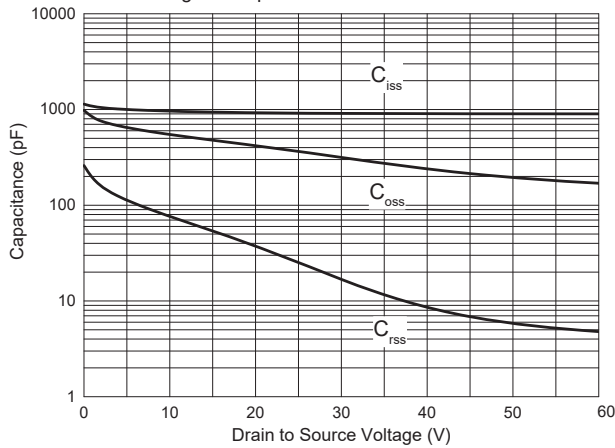
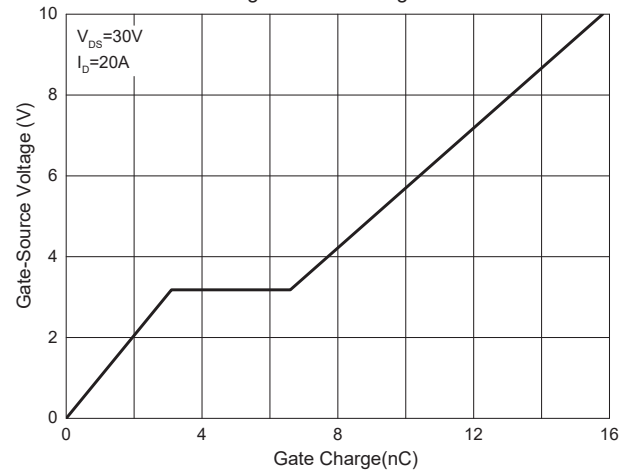


Fig. 6 - Gate Charge



Curve Characteristics

Fig. 7 - Normalized Threshold Voltage

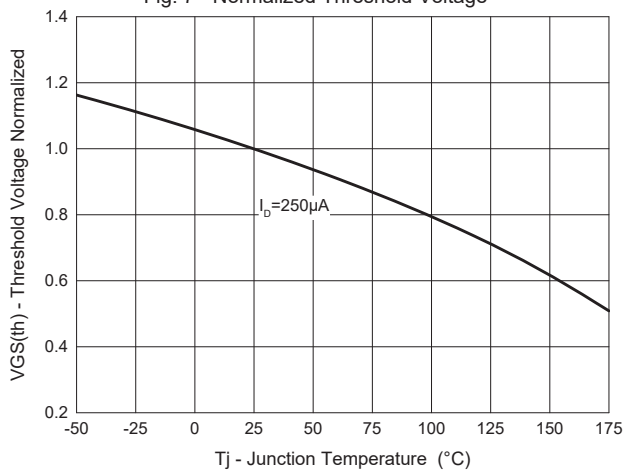


Fig. 8 - Normalized On Resistance Characteristics

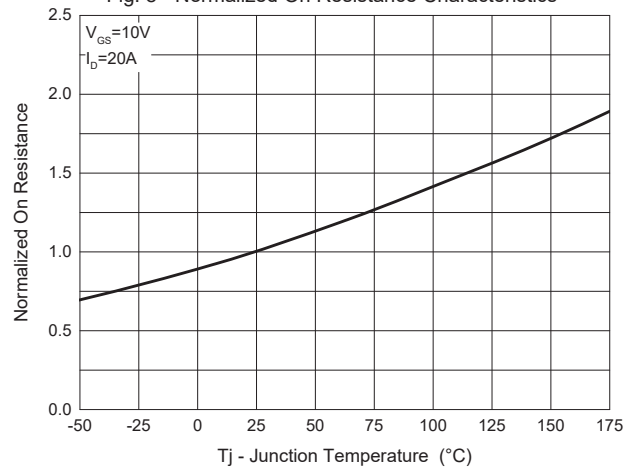


Fig. 9 - $I_s - V_{SD}$

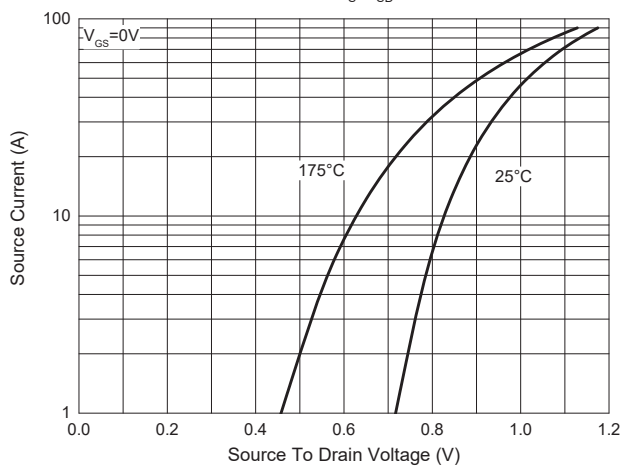


Fig. 10 - Drain Current

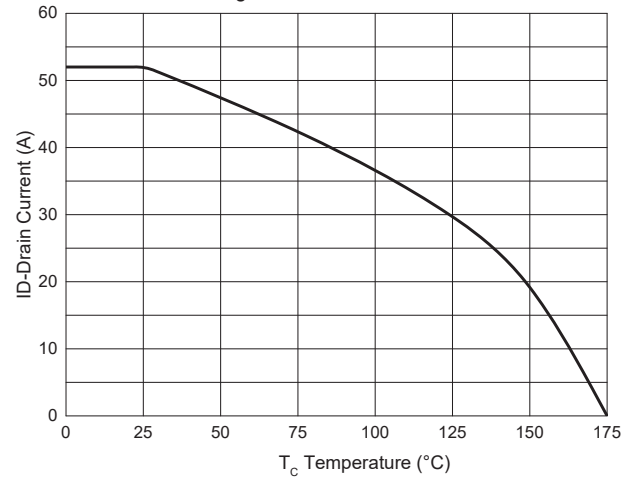
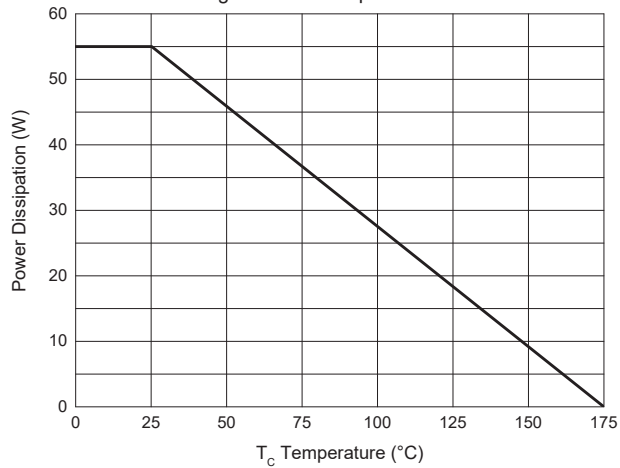


Fig. 11 - PD Dissipation



Curve Characteristics

Fig. 12 - Safe Operation Area

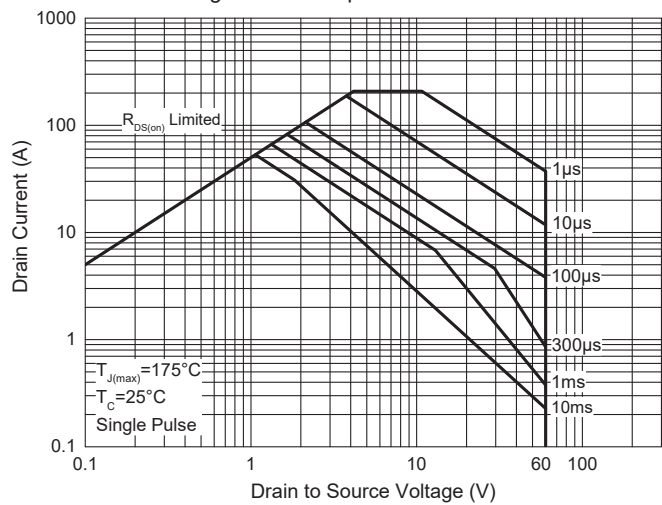
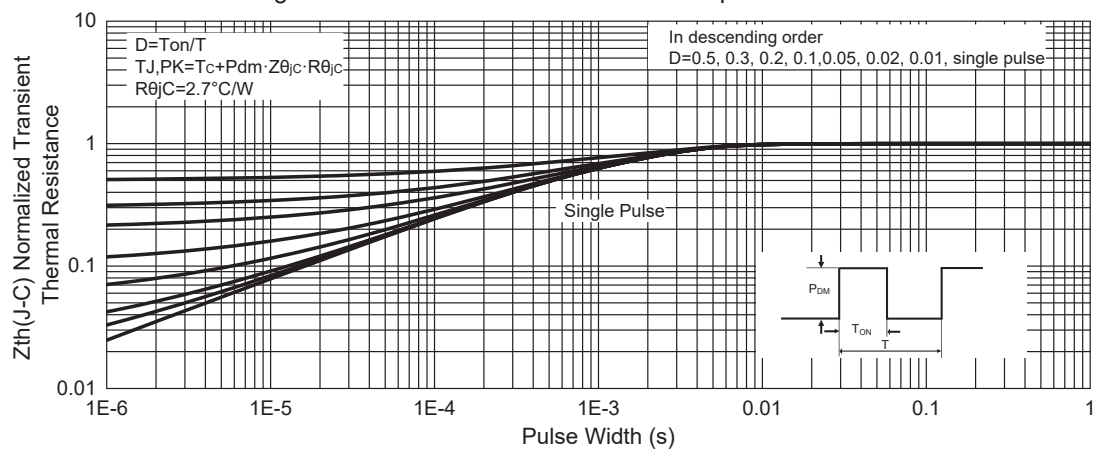


Fig. 13 - Normalized Transient Thermal Impedance



Ordering Information

Device	Packing
Part Number-TP	Tape&Reel: 5Kpcs/Reel

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