

Features

- ESD Protected Up To 2KV (HBM)
- Trench LV MOSFET Technology
- Operated At Low Logic Level Gate Drive
- Moisture Sensitivity Level 1
- Halogen Free. "Green" Device (Note1)
- Epoxy Meets UL 94 V-0 Flammability Rating
- Lead Free Finish/RoHS Compliant ("P" Suffix Designates RoHS Compliant. See Ordering Information)

Maximum Ratings

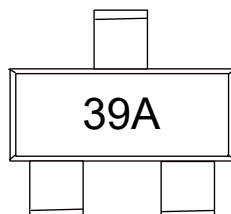
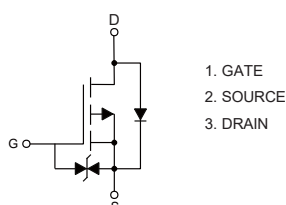
- Operating Junction Temperature Range: -55°C to +150°C
- Storage Temperature Range: -55°C to +150°C
- Thermal Resistance: 290°C/W Junction to Ambient (Note2)

Parameter		Symbol	Rating	Unit
Drain-Source Voltage		V_{DS}	-20	V
Gate-Source Voltage		V_{GS}	±12	V
Continuous Drain Current	$T_A=25^\circ\text{C}$	I_D	-0.6	A
	$T_A=100^\circ\text{C}$		-0.38	
Pulsed Drain Current (Note3)		I_{DM}	-2.4	A
Total Power Dissipation (Note4)		P_D	0.4	W

Note:

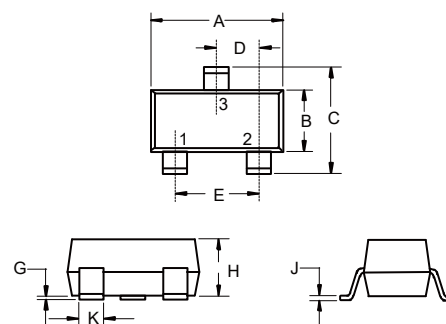
1. Halogen free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
2. The value of $R_{\theta JA}$ is measured with the device mounted on 1in² FR-4 board with 2oz. Copper, in a still air environment with $T_A=25^\circ\text{C}$.
3. Repetitive rating; pulse width limited by max. junction temperature.
4. P_D is based on max. junction temperature, using junction-ambient thermal resistance.

Internal Structure and Marking Code



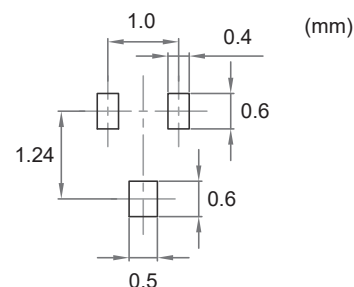
P-Channel MOSFET

SOT-523



DIMENSIONS					
DIM	INCHES		MM		NOTE
	MIN	MAX	MIN	MAX	
A	0.059	0.067	1.50	1.70	
B	0.030	0.033	0.75	0.85	
C	0.057	0.069	1.45	1.75	
D	0.020		0.50		TYP.
E	0.035	0.043	0.90	1.10	
G	0.000	0.004	0.00	0.10	
H	0.024	0.031	0.60	0.80	
J	0.004	0.008	0.10	0.20	
K	0.006	0.014	0.15	0.35	

Suggested Solder Pad Layout



ELECTRICAL CHARACTERISTICS (Ta=25°C unless otherwise specified)

Parameter	Symbol	Test conditions	Min	Typ	Max	Unit
Static Characteristics						
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} =0V, I _D =-250μA	-20			V
Gate-Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =-250μA	-0.35	-0.64	-1.1	V
Gate-Body Leakage Current	I _{GSS}	V _{GS} =±10V, V _{DS} =0V			±10	μA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =-20V, V _{GS} =0V			-1	μA
Drain-Source On-Resistance	R _{DS(on)}	V _{GS} =-4.5V, I _D =-500mA		0.62	0.85	Ω
		V _{GS} =-2.5V, I _D =-300mA		0.89	1.2	
		V _{GS} =-1.8V, I _D =-200mA		1.35	2	
Forward Transconductance	g _{FS}	V _{DS} =-5V, I _D =-600mA		1		S
Gate Resistance	R _g	f=1 MHz, Open drain		31		Ω
Diode Characteristics						
Continuous Body Diode Current	I _S				-0.6	A
Diode Forward Voltage	V _{SD}	V _{GS} =0V, I _S =-500mA			-1.2	V
Reverse Recovery Time	t _{rr}	I _F =-0.3A, dI _F /dt=100A/μs		8.6		ns
Reverse Recovery Charge	Q _{rr}			2.5		nC
Dynamic Characteristics						
Input Capacitance	C _{iss}	V _{DS} =-16V, V _{GS} =0V, f=1MHz		35		pF
Output Capacitance	C _{oss}			10.4		
Reverse Transfer Capacitance	C _{rss}			5.7		
Total Gate Charge	Q _g	V _{DS} =-10V, V _{GS} =-4.5V, I _D =-1A		1.20		nC
Gate-Source Charge	Q _{gs}			0.36		
Gate-Drain Charge	Q _{gd}			0.21		
Turn-On Delay Time	t _{d(on)}	V _{DD} =-10V, V _{GS} =-4.5V, R _G =10Ω, I _D =-0.5A		6.1		ns
Turn-On Rise Time	t _r			5.5		
Turn-Off Delay Time	t _{d(off)}			10.7		
Turn-Off Fall Time	t _f			5.6		

Curve Characteristics

Fig.1 - Typical Output Characteristics

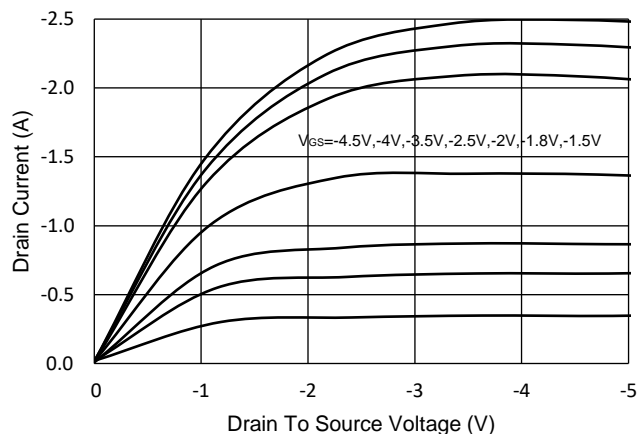


Fig.2 - Transfer Characteristic

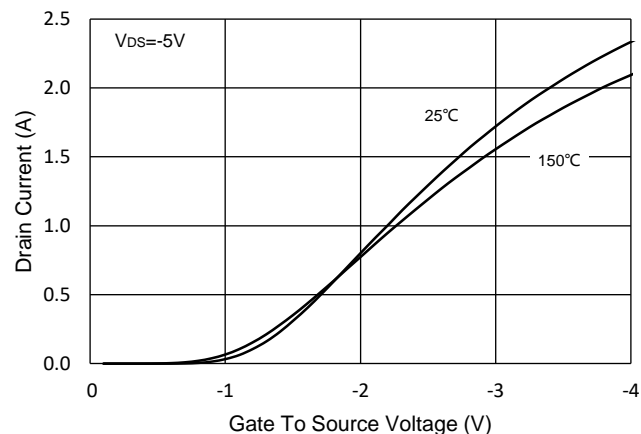


Fig.3 - $R_{DS(ON)}$ - V_{GS}

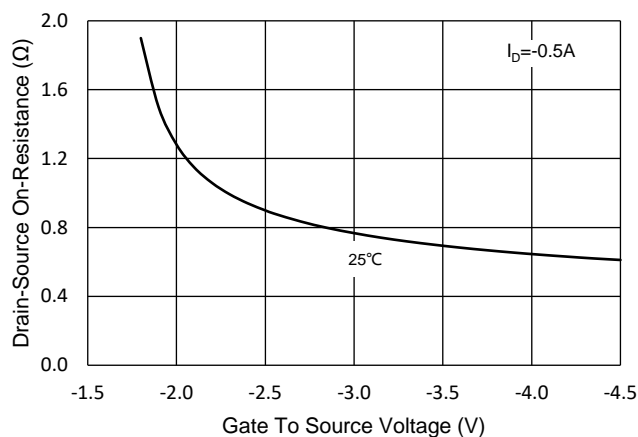


Fig.4 - $R_{DS(ON)}$ - I_D

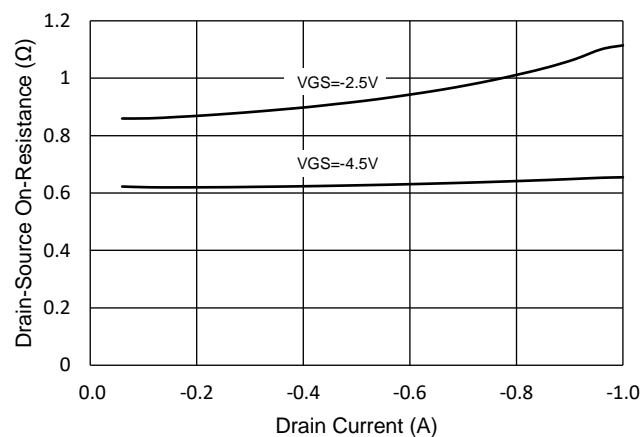


Fig.5 - Capacitance Characteristics

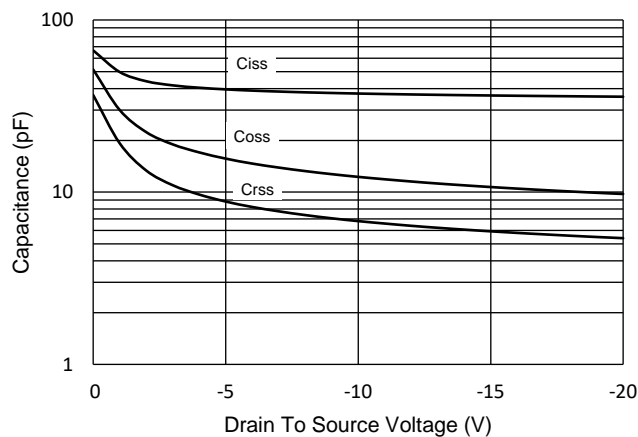
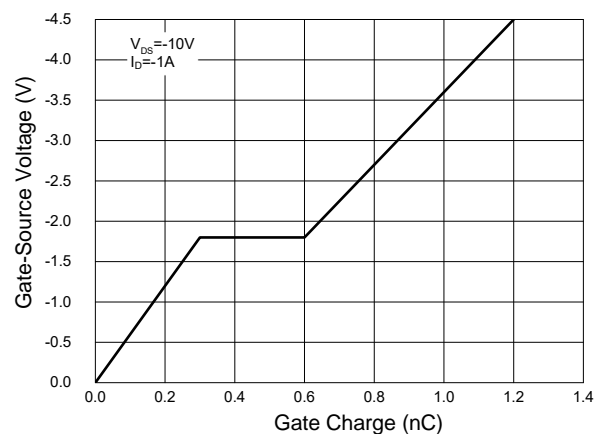


Fig.6 - Gate Charge



Curve Characteristics

Fig.7 - Normalized Threshold Voltage

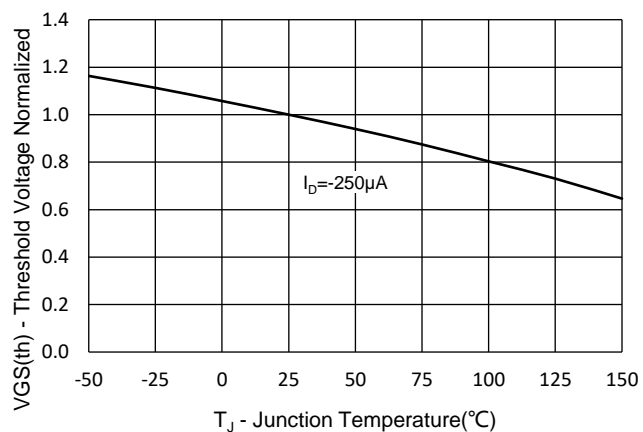


Fig.8 - Normalized On Resistance Characteristics

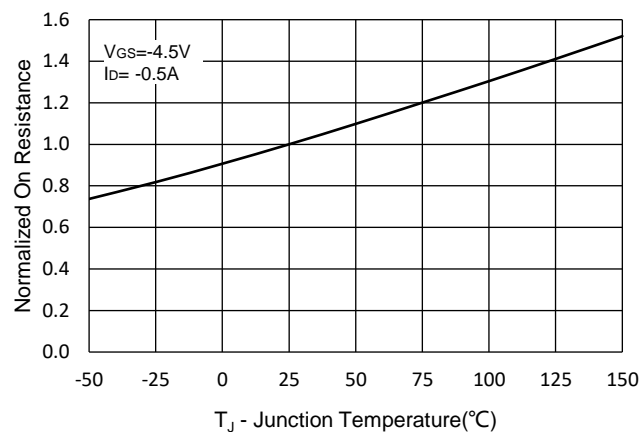


Fig.9 - $I_S - V_{SD}$

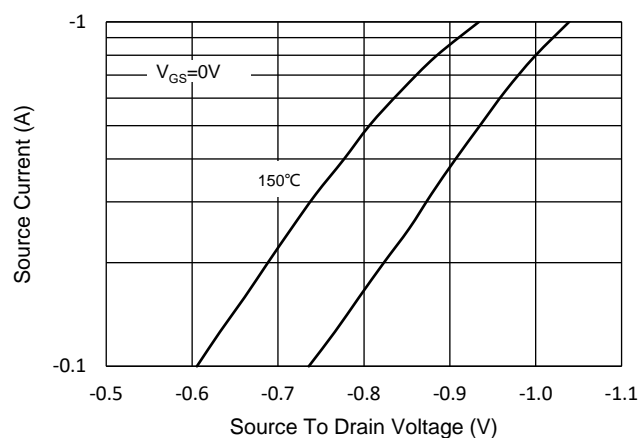


Fig.10 - Drain Current

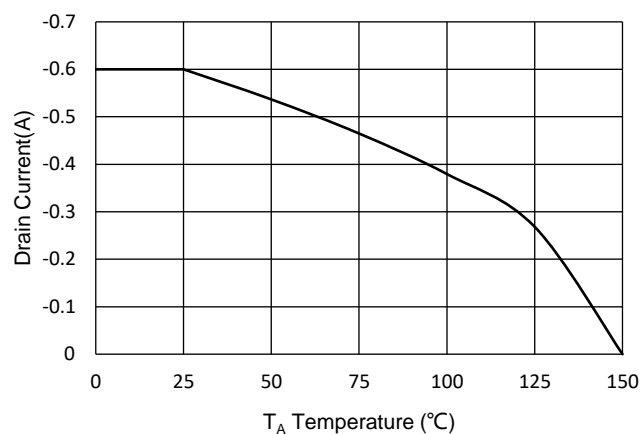
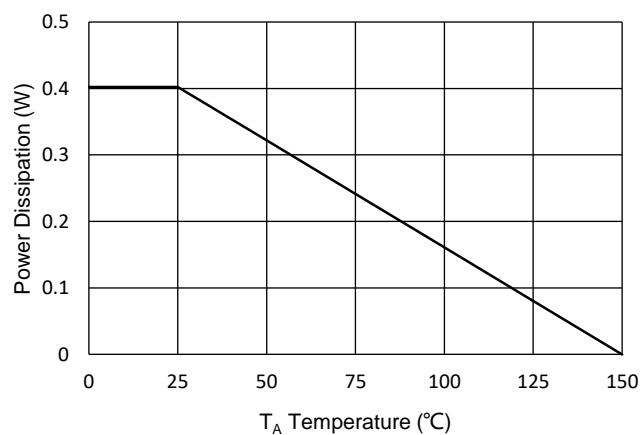


Fig.11 - PD Dissipation



Curve Characteristics

Fig.12 - Safe Operation Area

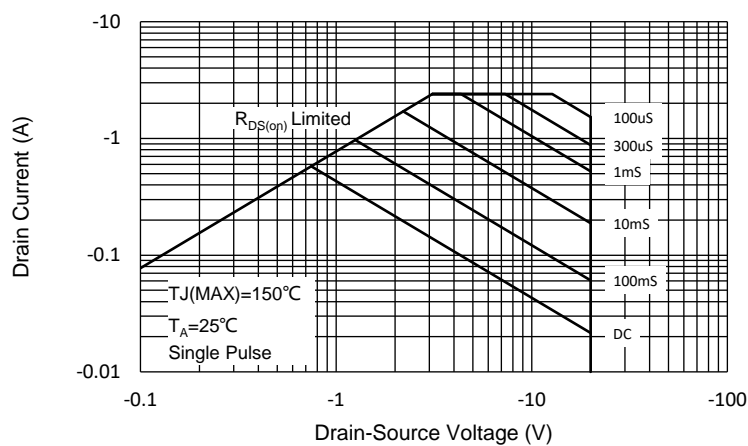
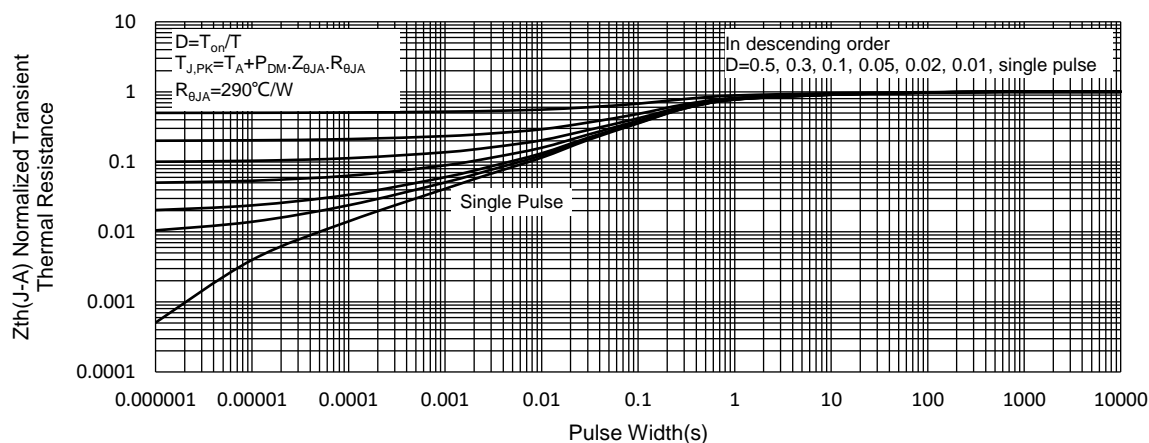


Fig.13 - Normalized Transient Thermal Impedance



Ordering Information

Device	Packing
Part Number-TP	Tape&Reel:3Kpcs/Reel

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