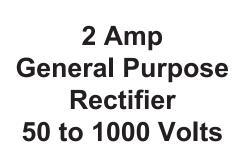


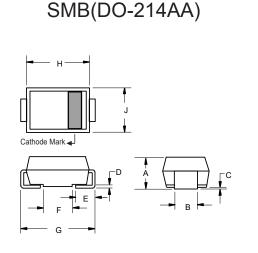
### Features

- AEC-Q101 Qualified
- Halogen Free. "Green" Device (Note 1)
- Glass Passivated Chip Junction
- High Forward Surge Capability
- Epoxy Meets UL 94 V-0 Flammability Rating
- Moisture Sensitivity Level 1
- Lead Free Finish/RoHS Compliant (Note 2)("P" Suffix Designates RoHS Compliant. See Ordering Information)

# Maximum Ratings @ 25°C (Unless Otherwise Specified)

Demonster	0	Value							Unit		
Parameter	Symbol	S2A HE3-L	S2B HE3-L	S2D HE3-L	S2G HE3-L	S2J HE3-L	S2K HE3-L	S2M HE3-L	Unit		
Peak Repetitive Reverse Voltage	$V_{RRM}$										
Working Peak Reverse Voltage	$V_{RWM}$	50	100	200	400	600	800	1000	V		
DC Blocking Voltage	$V_{R}$										
RMS Reverse Voltage	$V_{\text{RMS}}$	35	70	140	280	420	560	700	V		
Average Rectified Forward Current @ $T_L=105^{\circ}C$	I <sub>F(AV)</sub>				2				A		
Non-Repetitive Peak Surge Current @ 8.3ms Half Sine Wave	I				50				А		
Non-Repetitive Peak Surge Current @ 1ms Square Wave	I <sub>FSM</sub>				100						
Current Squared Time @1ms≤t≤8.3ms	l <sup>2</sup> t	10.375						A <sup>2</sup> s			





DIMENSIONS							
DIM	INCHES		M	М	NOTE		
Divi	MIN	MAX	MIN	MAX	NOTE		
Α	0.079	0.103	2.00	2.62			
В	0.075	0.087	1.91	2.21			
С	0.002	0.008	0.05	0.20			
D	0.006	0.012	0.15	0.31			
E	0.030	0.060	0.76	1.52			
F	0.065	0.091	1.65	2.32			
G	0.200	0.220	5.08	5.59			
Н	0.160	0.191	4.06	4.85			
J	0.130	0.155	3.30	3.94			

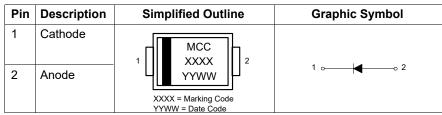
#### Part Number S2AHE3-L

Marking code

S2AHE3-L	S2A
S2BHE3-L	S2B
S2DHE3-L	S2D
S2GHE3-L	S2G
S2JHE3-L	S2J
S2KHE3-L	S2K
S2MHE3-L	S2M

Marking Code

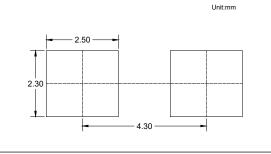
### Internal Structure



Note: 1. Halogen free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

2. High temperature solder exemption applied, see EU directive annex 7a.

#### Suggested Solder Pad Layout





# Thermal characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
TJ	Operating Junction Temperature Range		-55		150	°C
T <sub>stg</sub>	Storage Temperature Range		-55		150	°C
Rth <sub>(J-L)</sub>	Thermal Resistance from Junction to Lead	Note 1		20		°C/W
Rth <sub>(J-A)</sub>	Thermal Resistance from Junction to Ambient	Note 1		75		°C/W

Note:

1.Mounted on P.C.B. with 8.0 mm x 8.0 mm copper pad areas.

# Electrical Characteristics @ 25°C Unless Otherwise Specified

Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit
Forward Voltage	V <sub>F</sub>	I <sub>F</sub> =2A;T <sub>J</sub> =25°C			1.1	V
Reverse Current	I <sub>R</sub>	at Rated V <sub>R</sub> ;T <sub>J</sub> =25°C at Rated V <sub>R</sub> ;T <sub>J</sub> =125°C			5 100	μΑ
Junction Capacitance	CJ	V <sub>R</sub> =4V;f=1MHz;T <sub>J</sub> =25°C		18		pF





## **Curve Characteristics**

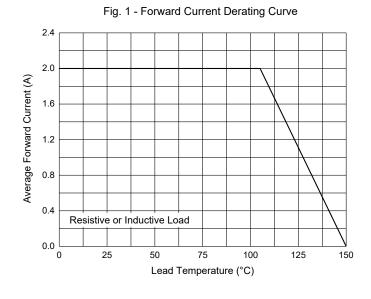


Fig. 3 - Typical Forward Characteristics

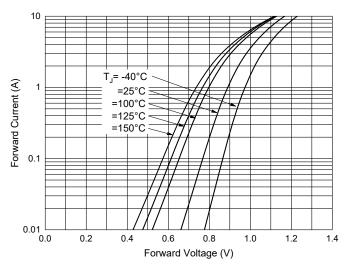
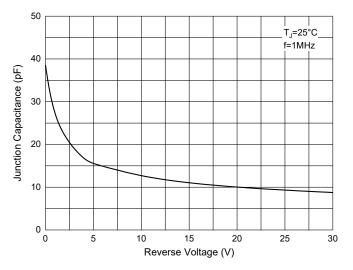


Fig. 5 - Typical Capacitance Characteristics



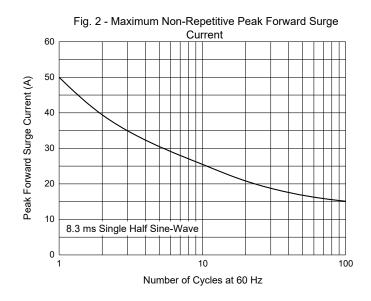
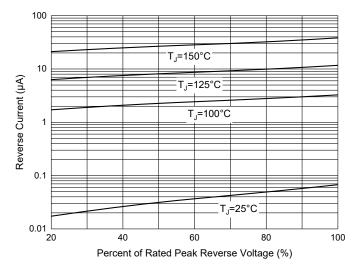


Fig. 4 - Typical Reverse Leakage Characteristics





### **Ordering Information**

Device	Packing			
S2AHE3-LTP ~ S2MHE3-LTP	Tape&Reel:3Kpcs/Reel			

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