

## Features

- Split Gate Trench MOSFET Technology
- AEC-Q101 Qualified
- Epoxy Meets UL 94 V-0 Flammability Rating
- Moisture Sensitivity Level 1
- Lead Free Finish/RoHs Compliant("P" Suffix Designates RoHs Compliant. See Ordering Information)
- Halogen Free. "Green" Device<sup>(Note1)</sup>

## Maximum Ratings

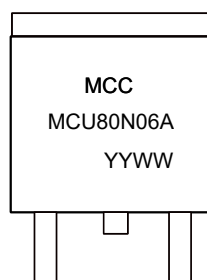
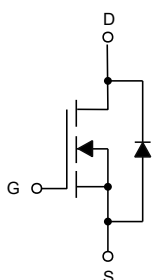
- Operating Junction Temperature Range : -55°C to +150°C
- Storage Temperature Range: -55°C to +150°C
- Thermal Resistance: 50°C/W Junction to Ambient (Steady-State)<sup>(Note2)</sup>
- Thermal Resistance: 1.6°C/W Junction to Case (Steady-State)

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	$V_{DS}$	60	V
Gate-Source Voltage	$V_{GS}$	±20	V
Continuous Drain Current	$I_D$	$T_C=25^\circ\text{C}$	80
		$T_C=100^\circ\text{C}$	51
Pulsed Drain Current <sup>(Note3)</sup>	$I_{DM}$	240	A
Total Power Dissipation <sup>(Note4)</sup>	$P_D$	78	W
Single Pulse Avalanche Energy <sup>(Note5)</sup>	$E_{AS}$	144	mJ

Note:

1. Halogen free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
2. The value of  $R_{\theta JA}$  is measured with the device mounted on 1in<sup>2</sup> FR-4 board with 2oz. Copper, in a still air environment with  $T_A=25^\circ\text{C}$ . The Power dissipation  $P_{DSM}$  is based on  $R_{\theta JA} \leq 10\text{s}$  and the maximum allowed junction temperature of 150°C. The value in any given application depends on the user's specific board design.
3. Repetitive rating; pulse width limited by max. junction temperature.
4.  $P_D$  is based on max. junction temperature, using junction-case thermal resistance.
5.  $T_J=25^\circ\text{C}$ ,  $V_{DD}=40\text{V}$ ,  $V_{GS}=10\text{V}$ ,  $L=0.5\text{mH}$

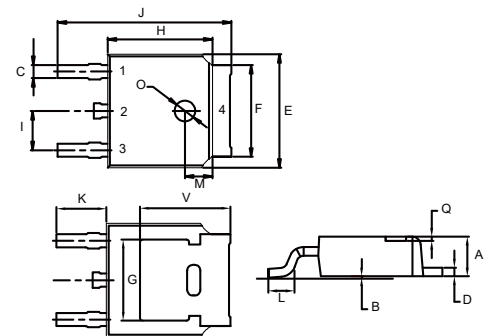
## Internal Structure and Marking Code



4 codes in total  
YY is the year  
ww is the week

# N-CHANNEL MOSFET

## DPAK(TO-252)



1. Gate
- 2,4. Drain
3. Source

### DIMENSIONS

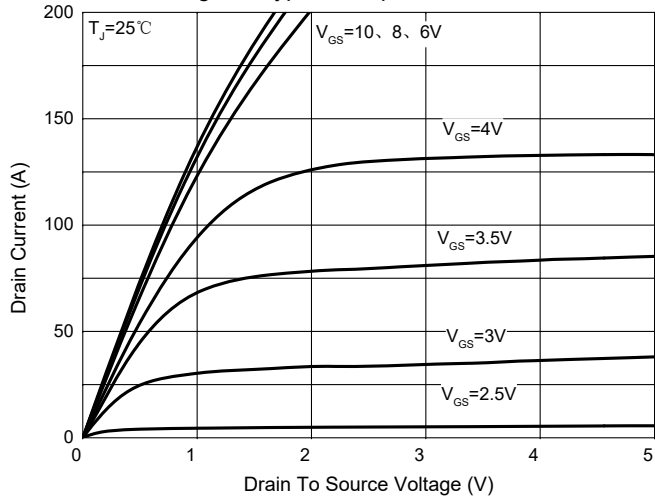
DIM	INCHES		MM		NOTE
	MIN	MAX	MIN	MAX	
A	0.087	0.094	2.20	2.40	
B	0.000	0.005	0.00	0.13	
C	0.026	0.034	0.66	0.86	
D	0.018	0.023	0.46	0.58	
E	0.256	0.264	6.50	6.70	
F	0.201	0.215	5.10	5.46	
G	0.190		4.83		TYP.
H	0.236	0.244	6.00	6.20	
I	0.086	0.094	2.18	2.39	
J	0.386	0.409	9.80	10.40	
K	0.114		2.90		TYP.
L	0.055	0.067	1.40	1.70	
M	0.063		1.60		TYP.
O	0.043	0.051	1.10	1.30	
Q	0.000	0.012	0.00	0.30	
V	0.211		5.35		TYP.

**Electrical Characteristics @ 25°C (Unless Otherwise Specified)**

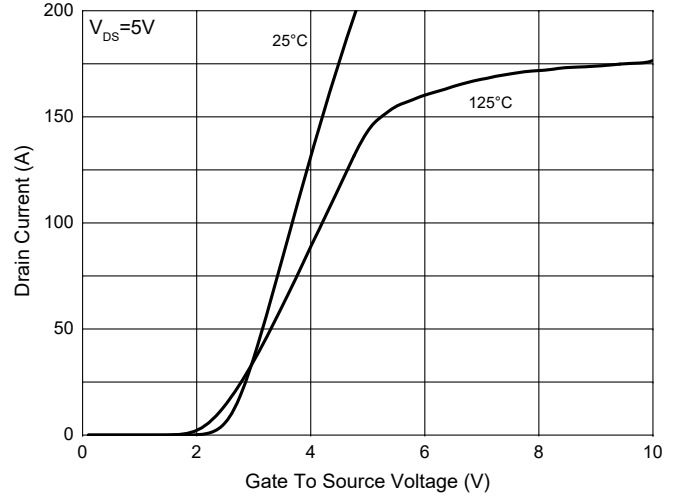
Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
<b>Static Characteristics</b>						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS}=0V, I_D=250\mu A$	60			V
Gate-Source Leakage Current	$I_{GSS}$	$V_{DS}=0V, V_{GS}=\pm 20V$			$\pm 100$	nA
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS}=60V, V_{GS}=0V$			1	$\mu A$
Gate-Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=250\mu A$	1.2	1.7	2.5	V
Drain-Source On-Resistance	$R_{DS(on)}$	$V_{GS}=10V, I_D=20A$		5.5	7.5	m $\Omega$
		$V_{GS}=4.5V, I_D=10A$		6.5	10	
Gate Resistance	$R_g$	F=1 MHz, Open drain		1.5		$\Omega$
<b>Diode Characteristics</b>						
Diode Forward Voltage	$V_{SD}$	$V_{GS}=0V, I_S=20A$			1.3	V
Continuous Body Diode Current	$I_S$				80	A
Reverse Recovery Time	$t_{rr}$	$I_F=20A, di/dt=500A/\mu s$		30		ns
Reverse Recovery Charge	$Q_{rr}$			18		nC
<b>Dynamic Characteristics</b>						
Input Capacitance	$C_{iss}$	$V_{DS}=25V, V_{GS}=0V, f=1MHz$		2100		pF
Output Capacitance	$C_{oss}$			670		
Reverse Transfer Capacitance	$C_{riss}$			30		
Total Gate Charge	$Q_g$	$V_{DS}=30V, V_{GS}=10V, I_D=20A$		31		nC
Gate-Source Charge	$Q_{gs}$			6		
Gate-Drain Charge	$Q_{gd}$			5		
Turn-On Delay Time	$t_{d(on)}$	$V_{GS}=10V, V_{DS}=30V, I_D=20A, R_{GEN}=3\Omega$		10		ns
Turn-On Rise Time	$t_r$			34		
Turn-Off Delay Time	$t_{d(off)}$			26.2		
Turn-Off Fall Time	$t_f$			45		

**Curve Characteristics**

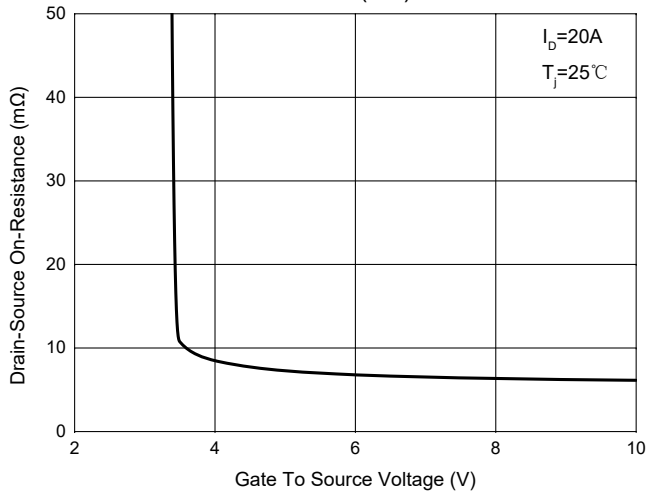
**Fig. 1 - Typical Output Characteristics**



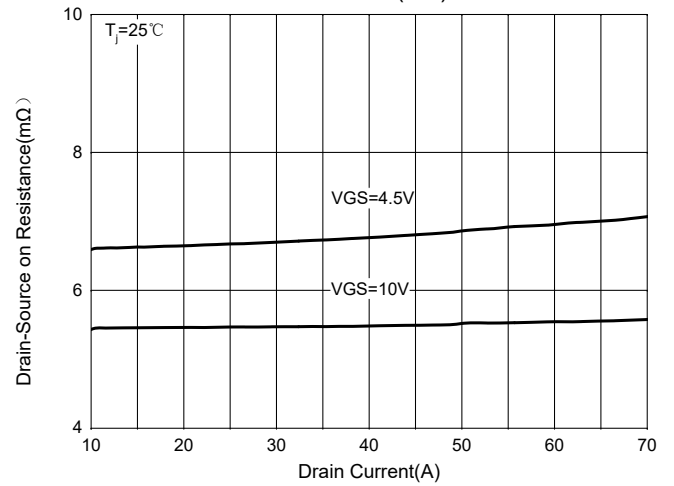
**Fig. 2 - Transfer Characteristics**



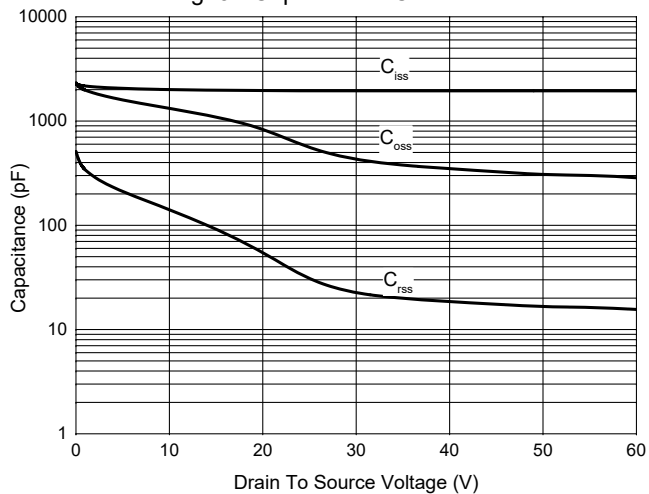
**Fig. 3 -  $R_{DS(ON)}$  -  $V_{GS}$**



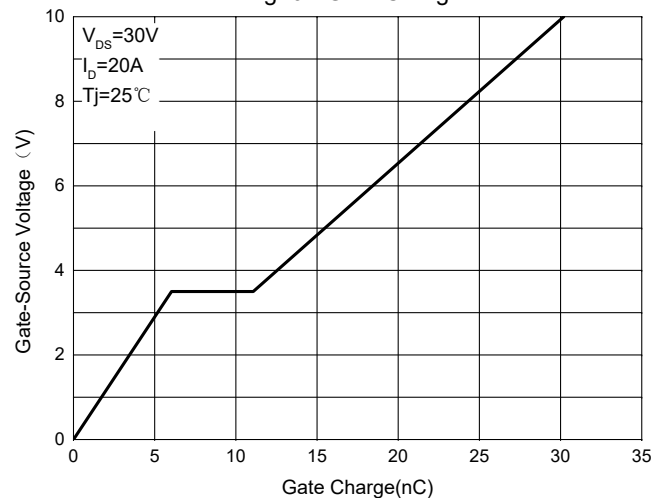
**Fig. 4 -  $R_{DS(ON)}$  -  $I_D$**



**Fig. 5 - Capacitance Characteristics**



**Fig. 6 - Gate Charge**



Curve Characteristics

Fig. 7 - Normalized Threshold voltage

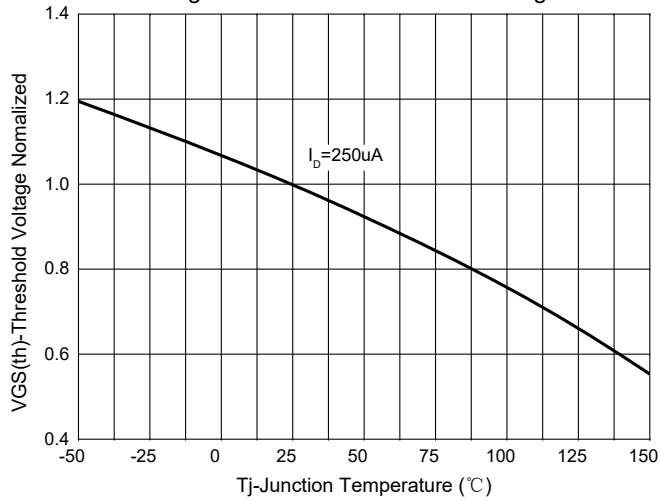


Fig.8-Normalized On Resistance Characteristics

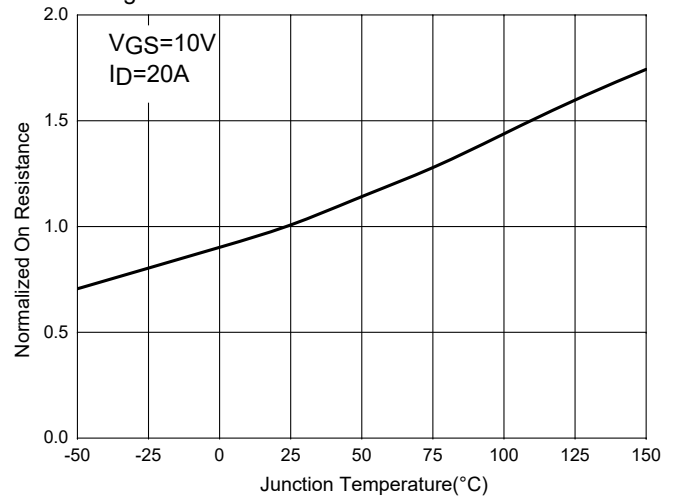


Fig. 9 - I<sub>S</sub>—V<sub>SD</sub>

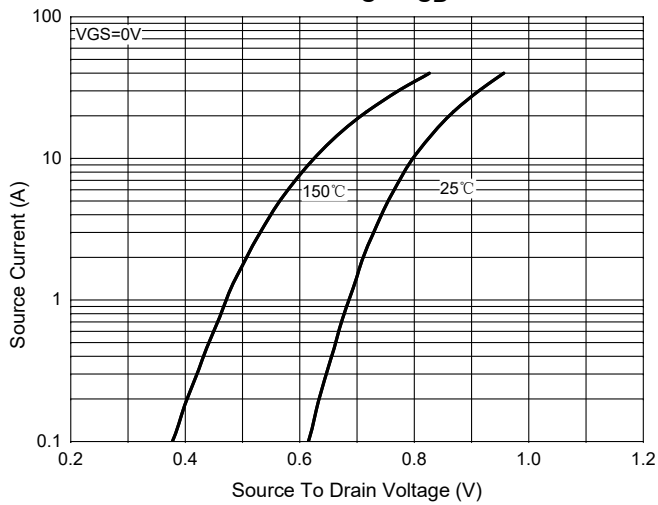


Fig. 10 - Current dissipation

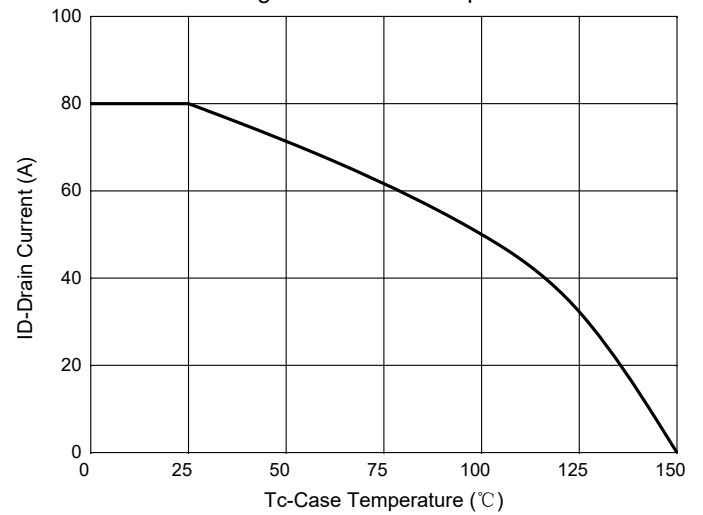
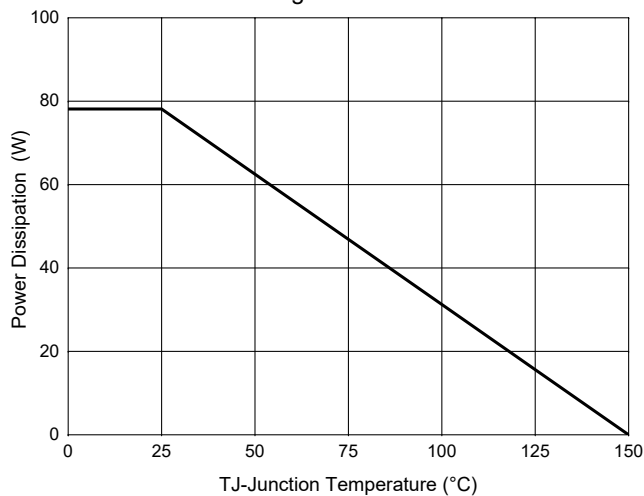


Fig.11-PD-TJ



Curve Characteristics

Fig. 12 - Safe Operation Area

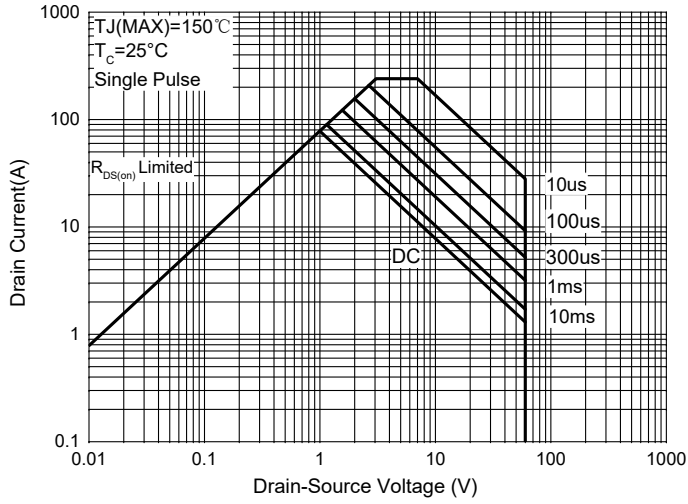
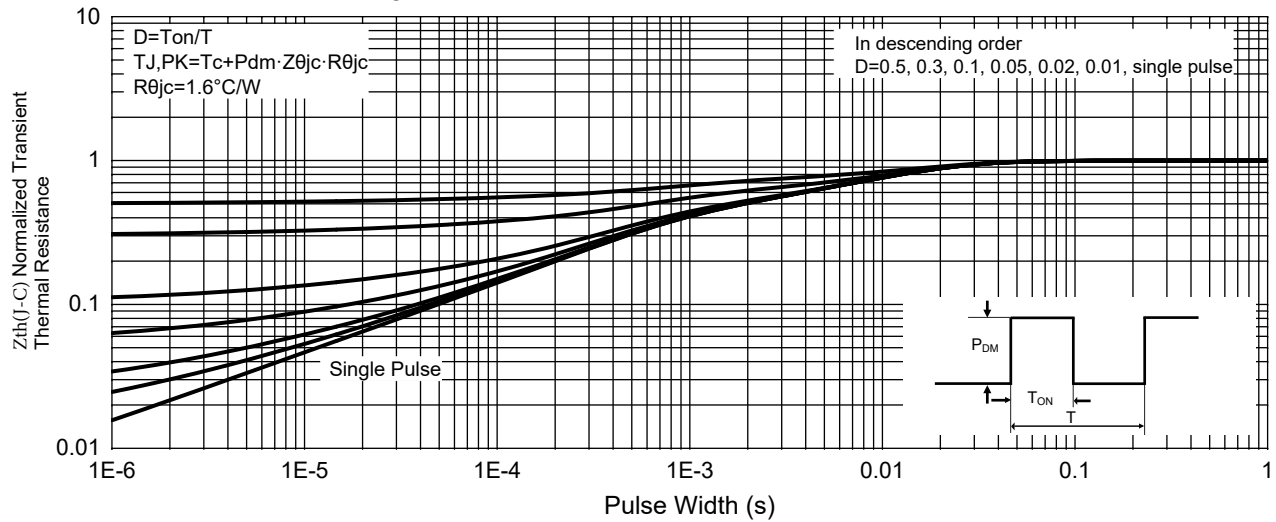


Fig. 13 - Normalized Transient Thermal Impedance



## Ordering Information

Device	Packing
Part Number-TP	Tape&Reel: 2.5Kpcs/Reel

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