

## Features

- Trench Power LV MOSFET Technology
- Low  $R_{DS(on)}$
- Low Gate Charge
- Moisture Sensitivity Level 1
- Halogen Free. "Green" Device<sup>(Note 1)</sup>
- Epoxy Meets UL 94 V-0 Flammability Rating
- Lead Free Finish/RoHS Compliant ("P" Suffix Designates RoHS Compliant. See Ordering Information)

## N-CHANNEL MOSFET

## Maximum Ratings

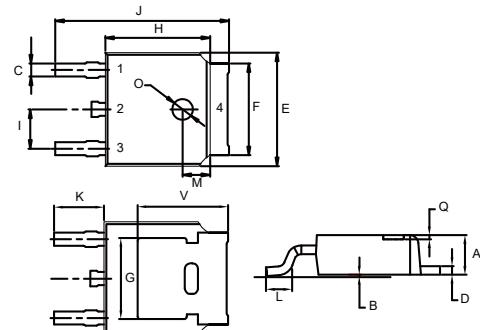
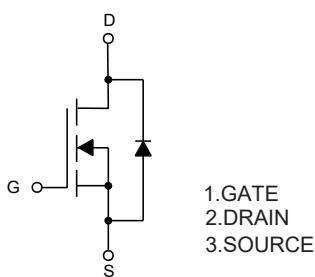
- Operating Junction Temperature Range: -55°C to +150°C
- Storage Temperature Range: -55°C to +150°C
- Thermal Resistance: 50°C/W Junction to Ambient<sup>(Note 2)</sup>
- Thermal Resistance: 2.15°C/W Junction to Case

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	$V_{DS}$	40	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	V
Continuous Drain Current $T_C=25^\circ\text{C}$	$I_D$	60	A
$T_C=100^\circ\text{C}$		38	
Pulsed Drain Current <sup>(Note 3)</sup>	$I_{DM}$	240	A
Total Power Dissipation <sup>(Note 4)</sup>	$P_D$	70	W
Single Pulsed Avalanche Energy <sup>(Note 5)</sup>	$E_{AS}$	100	mJ

Note:

1. Halogen free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
2. The value of  $R_{\theta JA}$  is measured with the device mounted on 1in<sup>2</sup> FR-4 board with 2oz. Copper, in a still air environment with  $T_A = 25^\circ\text{C}$ .
3. Repetitive rating; pulse width limited by max. junction temperature.
4.  $P_D$  is based on max. junction temperature, using junction-case thermal resistance.
5.  $T_J=25^\circ\text{C}$ ,  $V_{DD}=38\text{V}$ ,  $R_G=25\Omega$ ,  $L=0.5\text{mH}$ .

## Internal Structure and Marking Code



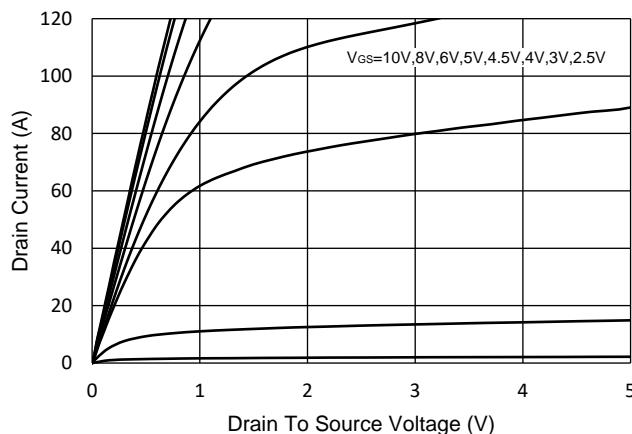
DIM	DIMENSIONS				NOTE
	INCHES		MM		
	MIN	MAX	MIN	MAX	
A	0.087	0.094	2.20	2.40	
B	0.000	0.005	0.00	0.13	
C	0.026	0.034	0.66	0.86	
D	0.018	0.023	0.46	0.58	
E	0.256	0.264	6.50	6.70	
F	0.201	0.215	5.10	5.46	
G	0.190		4.83		TYP.
H	0.236	0.244	6.00	6.20	
I	0.086	0.094	2.18	2.39	
J	0.386	0.409	9.80	10.40	
K	0.114		2.90		TYP.
L	0.055	0.067	1.40	1.70	
M	0.063		1.60		TYP.
O	0.043	0.051	1.10	1.30	
Q	0.000	0.012	0.00	0.30	
V	0.211		5.35		TYP.

**ELECTRICAL CHARACTERISTICS (Ta=25°C unless otherwise specified)**

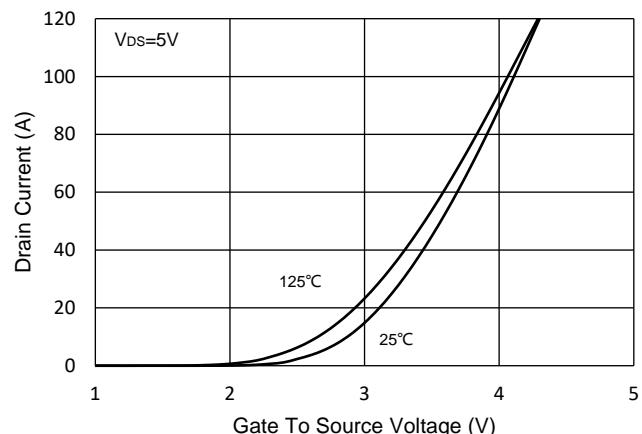
Parameter	Symbol	Test conditions	Min	Typ	Max	Unit
<b>Static Characteristics</b>						
Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =250μA	40			V
Gate-Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250μA	0.9	1.3	2.0	V
Gate-Body Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =± 20V, V <sub>DS</sub> =0V			±100	nA
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =40V, V <sub>GS</sub> =0V			1	μA
Drain-Source On-Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =20A		5.4	7	mΩ
		V <sub>GS</sub> =4.5V, I <sub>D</sub> =20A		6.8	9.6	
Forward Transconductance	g <sub>FS</sub>	V <sub>DS</sub> =25V, I <sub>D</sub> =20A	24			S
Gate Resistance	R <sub>g</sub>	f=1MHz, Open drain		2		Ω
<b>Diode Characteristics</b>						
Continuous Body Diode Current	I <sub>S</sub>				60	A
Diode Forward Voltage	V <sub>SD</sub>	V <sub>GS</sub> =0V, I <sub>S</sub> =20A			1.2	V
Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> =20A, dI <sub>F</sub> /dt=100A/μs		23.5		ns
Reverse Recovery Charge	Q <sub>rr</sub>			16		nC
<b>Dynamic Characteristics</b>						
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> =20V, V <sub>GS</sub> =0V, f=1MHz		1990		pF
Output Capacitance	C <sub>oss</sub>			224		
Reverse Transfer Capacitance	C <sub>rss</sub>			203		
Total Gate Charge	Q <sub>g</sub>	V <sub>DS</sub> =20V, V <sub>GS</sub> =10V, I <sub>D</sub> =20A		46		nC
Gate-Source Charge	Q <sub>gs</sub>			5.6		
Gate-Drain Charge	Q <sub>gd</sub>			12		
Turn-On Delay Time	t <sub>d(on)</sub>	V <sub>DD</sub> =20V, V <sub>GS</sub> =10V, R <sub>GEN</sub> =3Ω, I <sub>D</sub> =20A		8.8		ns
Turn-On Rise Time	t <sub>r</sub>			12.5		
Turn-Off Delay Time	t <sub>d(off)</sub>			32		
Turn-Off Fall Time	t <sub>f</sub>			11		

## Curve Characteristics

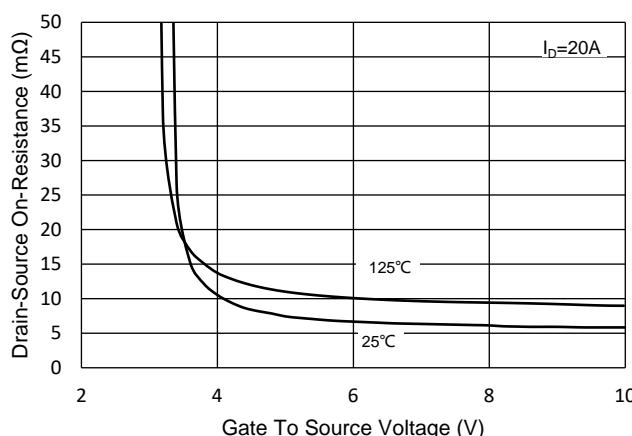
**Fig. 1 - Typical Output Characteristics**



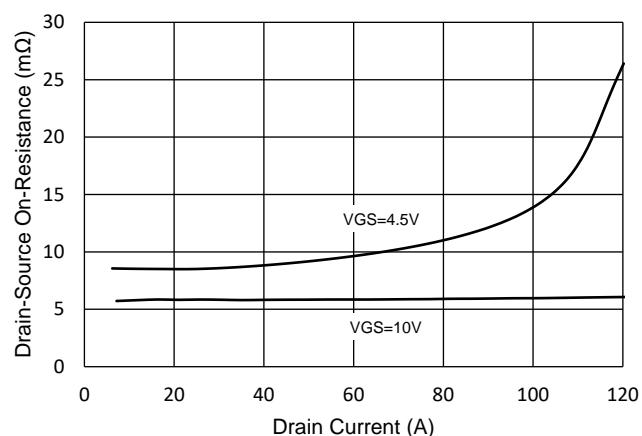
**Fig.2 - Transfer Characteristic**



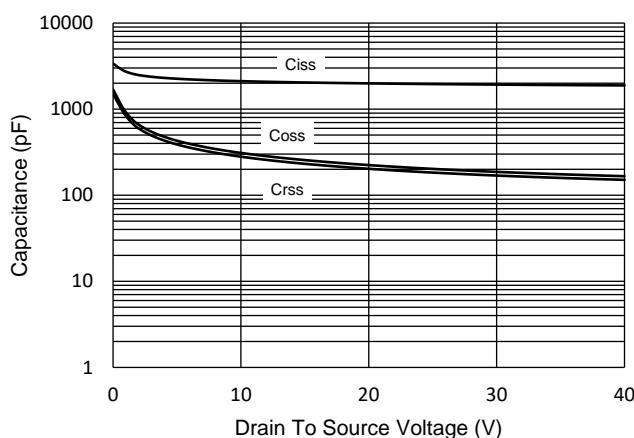
**Fig.3 -  $R_{DS(ON)}$  -  $V_{GS}$**



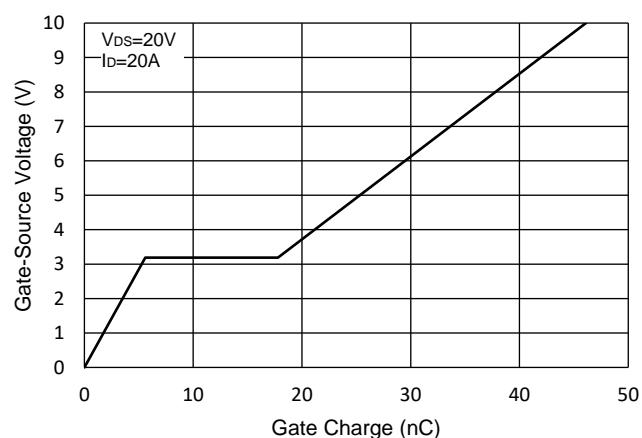
**Fig.4 -  $R_{DS(ON)}$  -  $I_D$**



**Fig.5 - Capacitance Characteristics**

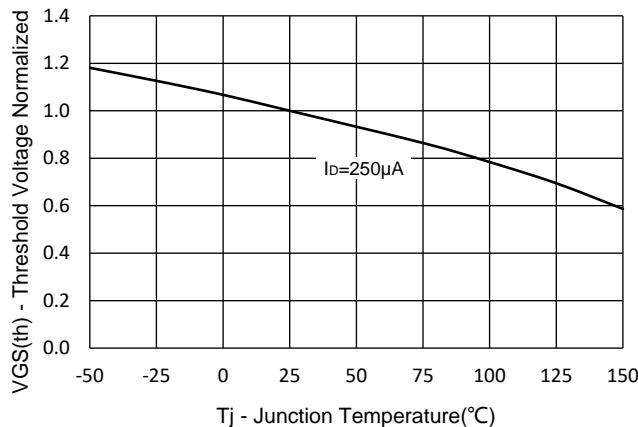


**Fig.6 - Gate Charge**

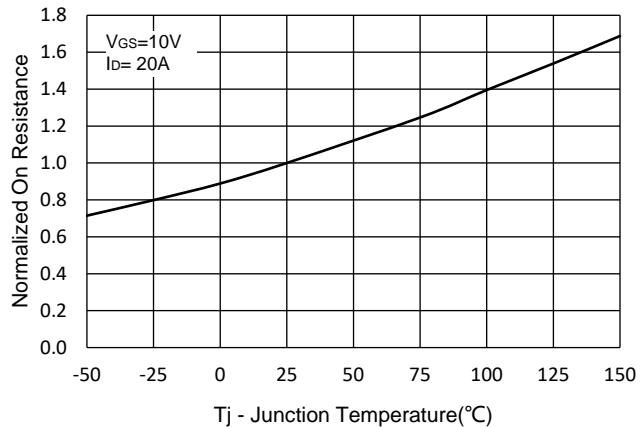


## Curve Characteristics

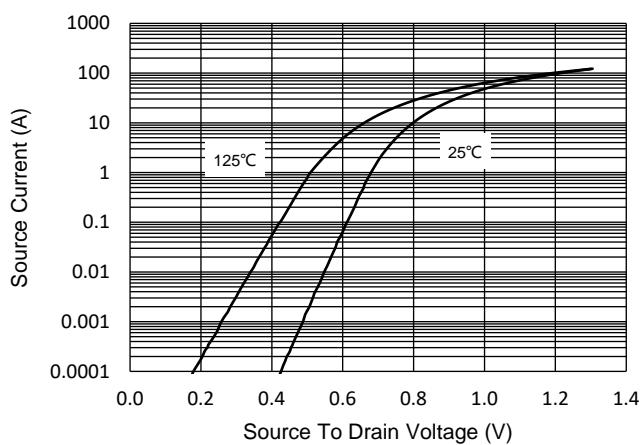
**Fig.7 - Normalized Threshold Voltage**



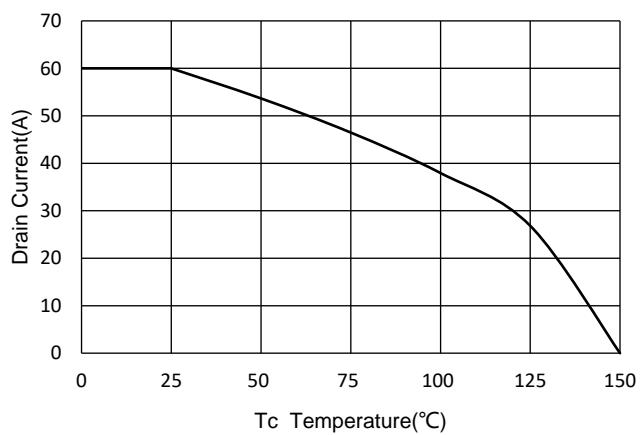
**Fig.8 - Normalized On Resistance Characteristics**



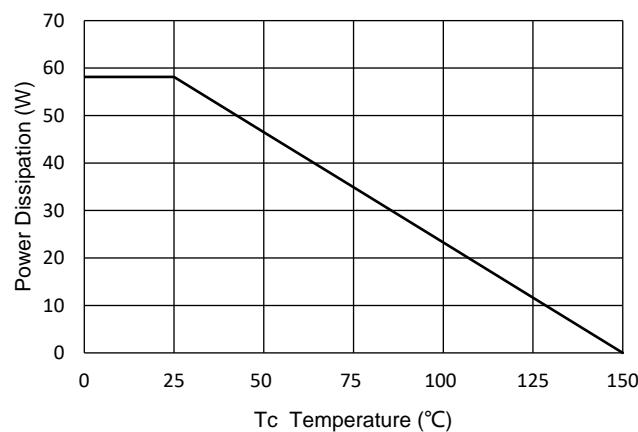
**Fig.9 -  $I_S$  -  $V_{SD}$**



**Fig.10 - Drain Current**



**Fig.11 - PD Dissipation**



## Curve Characteristics

Fig.12 - Safe Operation Area

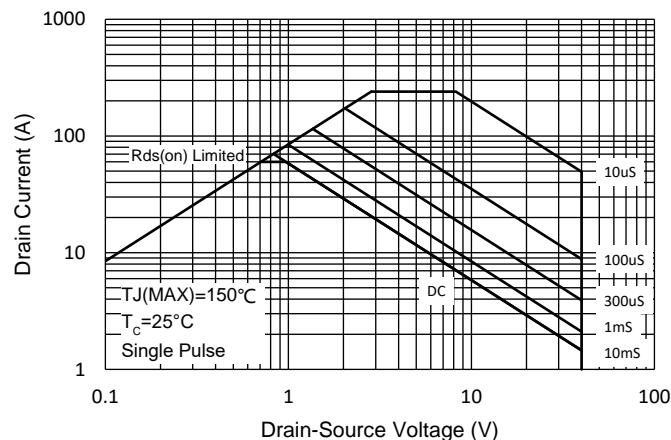
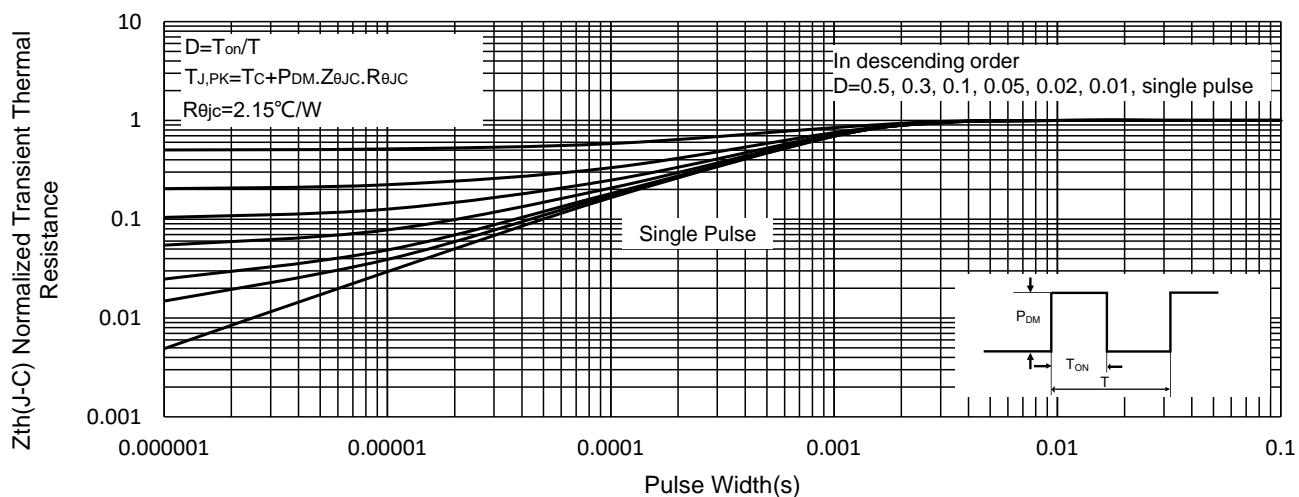


Fig.13 - Normalized Transient Thermal Impedance



## Ordering Information

Device	Packing
Part Number-TP	Tape&Reel:2.5Kpcs/Reel

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