

Features

- High Density Cell Design for Ultra Low R_{DS(ON)}
- · Fully Characterized Avalanche Voltage and Current
- Good Stability and Uniformity With High E_{AS}
- Epoxy Meets UL 94 V-0 Flammability Rating
- Moisture Sensitivity Level 1
- Halogen Free Available Upon Request By Adding Suffix "-HF"
- Lead Free Finish/RoHS Compliant ("P" Suffix Designates RoHS Compliant. See Ordering Information)

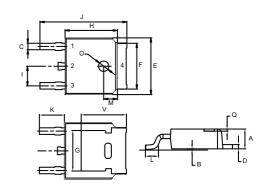
P-Channel MOSFET

Maximum Ratings

- Operating Junction Temperature Range: -55°C to +150°C
- Storage Temperature Range: -55°C to +150°C
- Thermal Resistance: 1.92°C/W Junction to Case

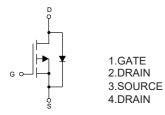
Parameter	Symbol	Rating	Unit
Drain -Source Voltage	V _{DS}	-40	V
Gate -Source Volltage	V _{GS}	±20	V
Drain Current-Continuous	I _D	-50	Α
Drain Current-Pulsed	I _{DM}	-115	Α
Single Pulsed Avalanche Energy ^(Note1)	E _{AS}	840	mJ
Power Dissipation	P _D	65	W

DPAK



DIMENSIONS						
	INC		MM			
DIM	INCHES				NOTE	
	MIN	MAX	MIN	MAX		
Α	0.087	0.094	2.20	2.40		
В	0.000	0.005	0.00	0.13		
С	0.026	0.034	0.66	0.86		
D	0.018	0.023	0.46	0.58		
Е	0.256	0.264	6.50	6.70		
F	0.201	0.215	5.10	5.46		
G	0.190		4.83		TYP.	
Н	0.236	0.244	6.00	6.20		
ı	0.086	0.094	2.18	2.39		
J	0.386	0.409	9.80	10.40		
K	0.114		2.90		TYP.	
L	0.055	0.067	1.40	1.70		
M	0.063		1.60		TYP.	
0	0.043	0.051	1.10	1.30		
Q	0.000	0.012	0.00	0.30		
V	0.211		5.35		TYP.	

Internal Structure





ELECTRICAL CHARACTERISTICS (Ta=25°C unless otherwise specified)

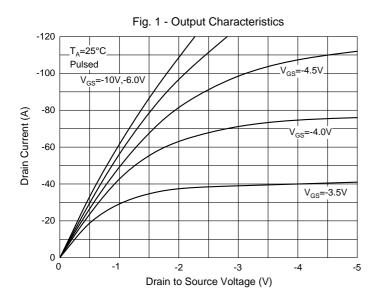
Parameter	Symbol	Test conditions	Min	Тур	Max	Unit
Static Characteristics	l					
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} =0V, I _D =-250μA	-40			V
Gate-Threshold Voltage ^(Note2)	V _{GS(th)}	V _{DS} =V _{GS} , I _D =-250μA	-1.2	-1.9	-2.5	V
Gate-Body Leakage Current	I _{GSS}	V _{GS} =± 20V, V _{DS} =0V			±100	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =-40V, V _{GS} =0V			-1	μΑ
Drain-Source On-Resistance ^(Note2)	R _{DS(on)}	V _{GS} =-10V, I _D =-14A		9	13	mΩ
Forward Transconductance(Note2)	g FS	V _{DS} =-10V, I _D =-20A		50		S
Dynamic Characteristics(Note3)			·			
Input Capacitance	C _{iss}			5020		
Output Capacitance	C _{oss}	V _{DS} =-20V,V _{GS} =0V, f=1MHz		551		pF
Reverse Transfer Capacitance	C _{rss}			374		
Switching Characteristics(Notes)	3)					
Total Gate Charge	Q _g			77		
Gate-Source Charge	Q_{gs}	V _{DS} =-20V,V _{GS} =-10V,I _D =-14A		19		nC
Gate-Drain Charge	Q_{gd}			21		
Turn-on Delay Time	t _{d(on)}			9.4		
Turn-on Rise Time	t _r	V_{DD} =-20V, V_{GS} =-10V, R_{L} =1 Ω ,		20		·- C
Turn-off Delay Time	t _{d(off)}	$R_{G}=3\Omega$		55		nS
Turn-off Fall Time	t _f			30		
Drain-Source Diode Characteristics			- 1	ı		
Diode Forward Voltage ^(Note 2)	V _{SD}	V _{GS} =0V,I _S =-10A			-1.2	V
Diode Forward Current (Note 4)	I _S				-50	Α
Reverse Recovery Time	t _{rr}	T _{.I} =25°C,I _F =-10A,		49		nS
Reverse Recovery Time	Q _{rr}	di/dt=-100a/us ^(Note2)		47		nC
Reverse Recovery Time	t _{on}	Intrinsic Turn-on Time is Negligible (Turn-on is Dominated by LS+LD)				

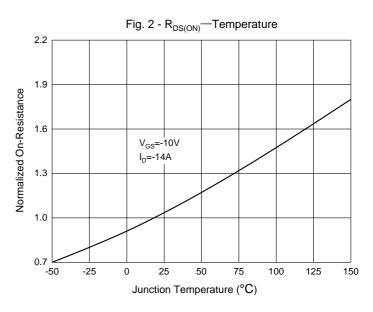
Notes:

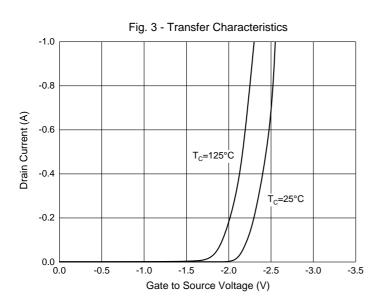
- 1. E_{AS} Condition: V_{DD} =-20V,L=1mH, R_{G} =25 Ω , V_{G} =-10V, I_{AS} =41A, Starting T_{J} = 25 $^{\circ}$ C
- Pulse Test : Pulse Width≤300µs, Duty Cycle ≤2%.
- 3. Guaranteed by Design, Not Subject to Production.
- 4. Surface Mounted on FR4 Board, t≤10 sec.

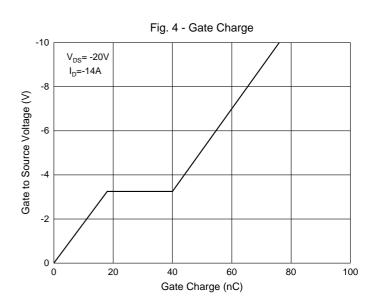


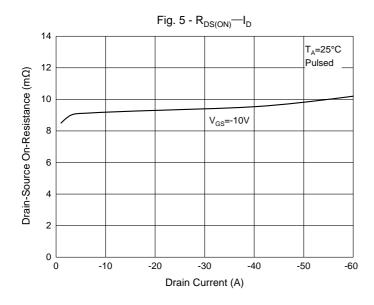
Curve Characteristics

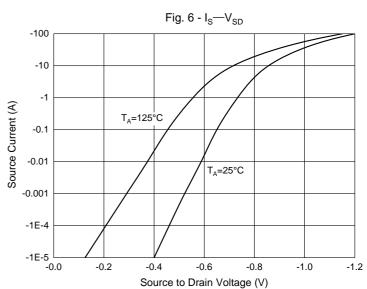














Ordering Information

Device	Packing
Part Number-TP	Tape&Reel:2.5Kpcs/Reel

Note: Adding "-HF" Suffix For Halogen Free, eg. Part Number-TP-HF

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