



**Micro Commercial Components** 

Micro Commercial Components 20736 Marilla Street Chatsworth CA 91311

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## **MCCD2005**

#### **Features**

- Advanced trench MOSFET process technology
- Ultra low on-resistance with low gate charge
- Halogen free available upon request by adding suffix "-HF"
- Epoxy meets UL 94 V-0 flammability rating
- Moisture Sensitivity Level 1
- Marking:2005

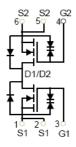
#### Maximum Ratings @ 25°C Unless Otherwise Specified

Symbol	Parameter	Rating	Unit	
$V_{DS}$	Drain-source Voltage	20	V	
I <sub>D</sub>	Drain Current-Continuous	8	Α	
I <sub>DM</sub>	Pulsed Drain Current (note1)	30	Α	
$V_{GS}$	Gate-source Voltage	± 12	V	
$R_{\thetaJA}$	Thermal Resistance Junction to Ambient	125	°C/W	
TJ	Operating Junction Temperature	-55 to +150	$^{\circ}\!\mathbb{C}$	
T <sub>STG</sub>	Storage Temperature	-55 to +150	$^{\circ}\!\mathbb{C}$	

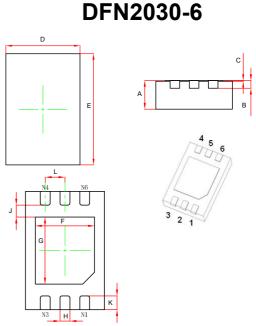
#### Notes:

1. Repetitive Rating: Pulse width limited by junction temperature.

#### **Equivalent Circuit**



## **Dual N-Channel Power MOSFET**



Dimensions						
D.114	INCHES		ММ			
DIM	MIN	MAX	MIN	MAX	NOTE	
Α	0.028	.032	0.700	0.800		
В	0.008REF.		0.203REF.			
С	0000	0.002	0.000	0.050		
D	0.077	0.081	1.950	2.050	ĺ	
E	0.116	0.120	2.950	3.050		
F	0.055	0.063	1.400	1.600		
G	0.063	0.071	1.600	1.800		
Н	0.008	0.012	0.200	0.300		
J	0.008		0.200			
K	0.018	0.026	0.300	0.400		
L	0.020TYP.		0.50	OTYP.		



#### **ELECTRICAL CHARACTERISTICS(T<sub>a</sub>=25℃ unless otherwise specified)**

Parameter	Symbol	Test Condition	Min	Тур	Max	Unit
STATIC PARAMETERS						
Drain-source breakdown voltage	V (BR) DSS	V <sub>G</sub> S = 0V, I <sub>D</sub> =250µA	20			V
Zero gate voltage drain current	IDSS	V <sub>DS</sub> =16V,V <sub>GS</sub> = 0V			10	μΑ
Gate-body leakage current	Igss	V <sub>GS</sub> =±10V, V <sub>DS</sub> = 0V			±10	μΑ
Gate threshold voltage (note 1)	V <sub>GS</sub> (th)	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250µA	0.5		1	V
		V <sub>GS</sub> =10V, I <sub>D</sub> =8A			13	mΩ
		V <sub>G</sub> S =4.5V, I <sub>D</sub> =5A			14	mΩ
Drain-source on-resistance (note 1)	R <sub>DS(on)</sub>	V <sub>GS</sub> =3.8V, I <sub>D</sub> =5A			15.5	mΩ
		Vgs =2.5V, ID =4A			19	mΩ
		V <sub>G</sub> S =1.8V, I <sub>D</sub> =3A			27	mΩ
Forward tranconductance (note 1)	<b>g</b> FS	V <sub>DS</sub> =5V, I <sub>D</sub> =8A		36		S
Diode forward voltage(note 1)	V <sub>SD</sub>	I <sub>S</sub> =1A, V <sub>GS</sub> = 0V			1	V
<b>DYNAMIC PARAMETERS</b> (note 2)						
Input Capacitance	C <sub>iss</sub>			1800		pF
Output Capacitance	C <sub>oss</sub>	V <sub>DS</sub> =10V,V <sub>GS</sub> =0V,f =1MHz		230		pF
Reverse Transfer Capacitance	C <sub>rss</sub>			200		pF
Total gate charge	Qg			17.9		nC
Gate-source charge	Q <sub>gs</sub>	V <sub>DS</sub> =10V,V <sub>GS</sub> =4.5V,I <sub>D</sub> =8A		1.5		nC
Gate-drain charge	Q <sub>gd</sub>			4.7		nC
SWITCHING PARAMETERS(note 2)						
Turn-on delay time	td(on)			2.5		ns
Turn-on rise time	tr	V <sub>GS</sub> =10V,V <sub>DD</sub> =10V,		7.2		ns
Turn-off delay time	td(off)	$R_L$ =1.2 $\Omega$ , $R_{GEN}$ =3 $\Omega$		49		ns
Turn-off fall time	tf			10.8		ns

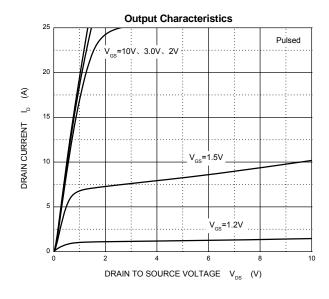
#### Notes:

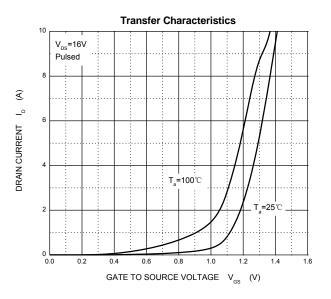
- 1. Pulse Test : Pulse width≤300µs, duty cycle≤0.5%.
- 2. Guaranteed by design, not subject to production testing.

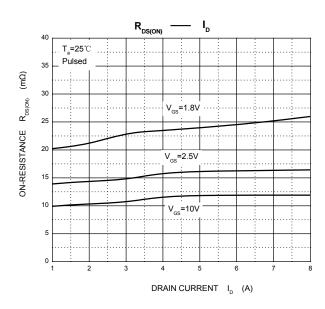


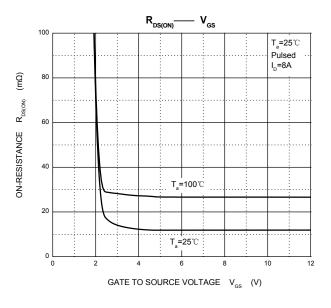
### **Typical Characteristics**

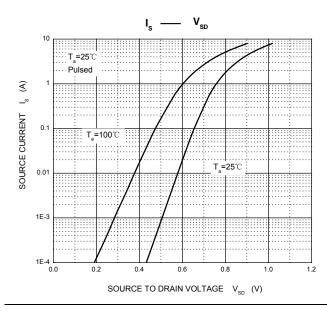
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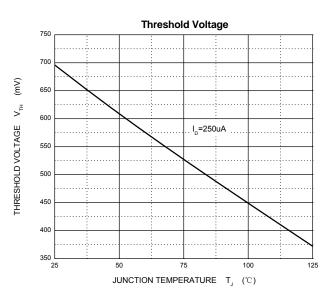














#### Ordering Information:

Device	Packing
Part Number-TP	Tape&Reel:3Kpcs/Reel

Note: Adding "-HF" suffix for halogen free, eg. Part Number-TP-HF

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