

Features

- Split Gate Trench MOSFET Technology
- · Excellent Package for Heat Dissipation
- High Density Cell Design for Low R_{DS(ON)}
- · Moisture Sensitivity Level 1
- Halogen Free. "Green" Device (Note1)
- Epoxy Meets UL 94 V-0 Flammability Rating
- Lead Free Finish/RoHS Compliant ("P" Suffix Designates RoHS Compliant. See Ordering Information)

Maximum Ratings

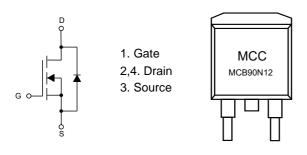
- Operating Junction Temperature Range : -55°C to +150°C
- Storage Temperature Range: -55°C to +150°C
- Thermal Resistance: 15°C/W Junction to Ambient(t≤10S)^(Note 2)
- Thermal Resistance: 60°C/W Junction to Ambient(Steady-State)^(Note 2)
- Thermal Resistance: 0.48°C/W Junction to Case

Parameter	Symbol	Rating	Unit	
Drain-Source Voltage	V _{DS}	120	V	
Gate-Source Volltage	V _{GS}	±20	V	
Continuous Drain Current	I _D	90	Α	
Pulsed Drain Current (Note 3)	I _{DM}	360	Α	
Total Power Dissipation ^(Note 4)	P _D	260	W	
Single Pulsed Avalanche Energy ^(Note 5)	E _{AS}	306	mJ	

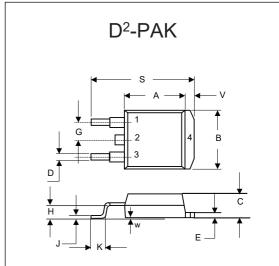
Note:

- 1. Halogen free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 2. The value of $R_{\theta JA}$ is measured with the device mounted on 1in^2 FR-4 board with 2oz. Copper, in a still air environment with T_A =25°C.
- 3. Repetitive rating; pulse width limited by max. junction temperature.
- 4. P_D is based on max. junction temperature, using junction-case thermal resistance.
- 5. T_J =25°C, V_{DD} =50V, V_{GS} =10V, R_G =25 Ω , L=0.5mH.

Internal Structure and Marking Code



N-CHANNEL MOSFET



DIMENSIONS					
DIM	INCHES		MM		NOTE
	MIN	MAX	MIN	MAX	NOTE
Α	0.331	0.370	8.40	9.40	
В	0.378	0.417	9.60	10.60	
С	0.165	0.189	4.20	4.80	
D	0.027	0.037	0.68	0.94	
E	0.045	0.055	1.14	1.40	
G	0.10		2.54		TYP.
Н	0.096	0.134	2.43	3.40	
J	0.011	0.025	0.28	0.64	
K	0.071	0.131	1.80	3.32	
S	0.575	0.625	14.60	15.87	
V	0.042	0.058	1.07	1.47	
W	0.000	0.010	0.00	0.25	

Suggested Solder Pad Layout

10.26

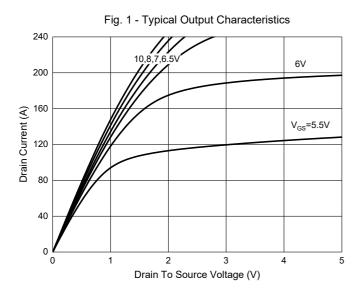


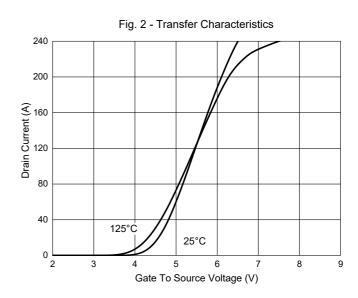
Electrical Characteristics @ 25°C (Unless Otherwise Specified)

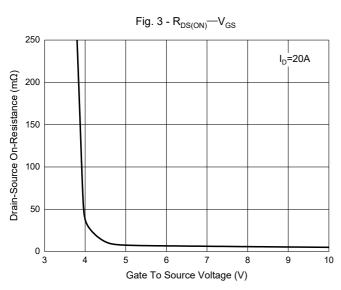
Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit	
Static Characteristics					1		
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} =0V, I _D =250μA	120			V	
Gate-Source Leakage Current	I _{GSS}	V _{DS} =0V, V _{GS} =±20V			±100	nA	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =120V, V _{GS} =0V			1	μA	
Gate-Threshold Voltage	V _{GS(th)}	$V_{DS}=V_{GS}$, $I_{D}=250\mu A$	2	3.2	4	V	
Drain-Source On-Resistance	R _{DS(on)}	V _{GS} =10V, I _D =20A		6.2	7.2	mΩ	
Gate Resistance	R_G	f=1MHz, Open drain		0.9		Ω	
Diode Characteristics							
Continuous Body Diode Current	Is				90	Α	
Diode Forward Voltage	V _{SD}	V _{GS} =0V, I _S =20A			1.3	V	
Reverse Recovery Time	t _{rr}	1 = 20 A d1 /d+= 100 A/u o		84		ns	
Reverse Recovery Charge	Q _{rr}	I _F =20A, dI _F /dt=100A/μs		191		nC	
Dynamic Characteristics	-		,				
Input Capacitance	C _{iss}			4514			
Output Capacitance	C _{oss}	V_{DS} =50V, V_{GS} =0V,f=1MHz		1110		pF	
Reverse Transfer Capacitance	C _{rss}			34			
Total Gate Charge	Q_g			65			
Gate-Source Charge	Q _{gs}	V_{DS} =50V, V_{GS} =10V, I_{D} =20A		25.9		nC	
Gate-Drain Charge	Q_{gd}			8.9			
Turn-On Delay Time	t _{d(on)}			21.6			
Turn-On Rise Time	t _r	V _{DS} =50V, V _{GEN} =10V,		39.9		no	
Turn-Off Delay Time	t _{d(off)}	$R_G=2.2\Omega$, $I_{DS}=20A$		35		ns	
Turn-Off Fall Time	t _f			40.9			

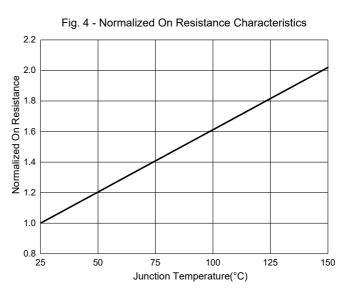


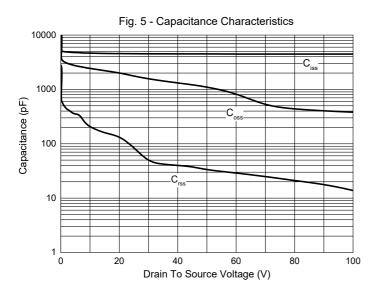
Curve Characteristics

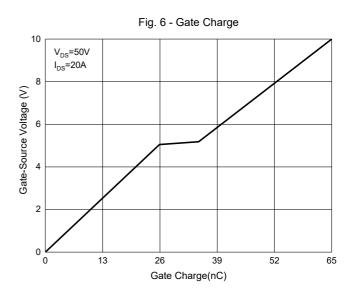






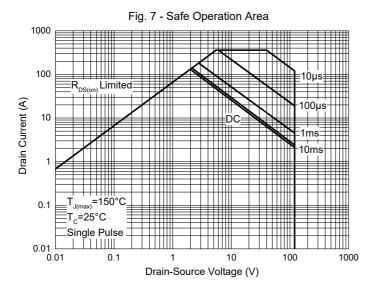








Curve Characteristics





Ordering Information

Device	Packing	
Part Number-TP	Tape&Reel: 800pcs/Reel	

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