

### **Features**

- Low On-resistance and Low Conduction Loss
- Super Junction technology for High Voltage Application
- Soft Switching with Fast Reverse Recovery Diode
- Ultra Low Gate Charge Cause Lower Driving Requirement
- Moisture Sensitivity Level 1
- Epoxy Meets UL 94 V-0 Flammability Rating
- Halogen Free."Green "Device(Note 1)
- Lead Free Finish/RoHS Compliant. "P" Suffix Designates RoHS Compliant. See Ordering Information

# **Maximum Ratings**

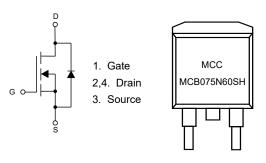
- Operating Junction Temperature Range: -55°C to +150°C
- Storage Temperature Range: -55°C to +150°C
- Thermal Resistance Junction to Ambient, Max(Note 2): 60°C/W
- Thermal Resistance Junction to Case, Max: 1.18°C/W

Parameter	Symbol	Value	Unit		
Drain-Source Voltage		V <sub>DS</sub>	600	V	
Gate-Source Volltage		V <sub>GS</sub>	±30	V	
Continuous Drain Current	T <sub>C</sub> =25°C		23	А	
	T <sub>C</sub> =100°C	- I <sub>D</sub>	14.5		
Pulsed Drain Current <sup>(Note 3)</sup>		I <sub>DM</sub>	92	Α	
Total Power Dissipation, T <sub>C</sub> =25°C		P <sub>D</sub>	106	W	
Single Avalanche Energy <sup>(Note 4)</sup>		E <sub>AS</sub>	132	mJ	

#### Note:

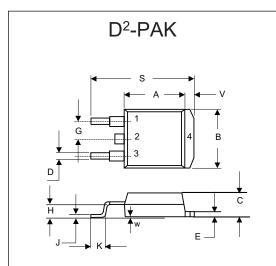
- 1. Halogen free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 2. Device mounted on 1 in 2 FR-4 board with 2oz. single-sided Copper, in a still air environment with TA=25 °C.
- 3. Repetitive rating; pulse width limited by max. junction temperature.
- 4. Starting T<sub>J</sub>=25°C, V<sub>DD</sub>=50V,I<sub>AS</sub>=23A.

# **Internal Structure and Marking Code**



Device Code: MCB075N60SH

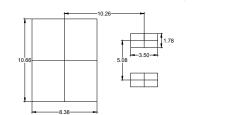
# **N-CHANNEL Super-Junction Power MOSFET**



DIMENSIONS					
DIM	INCHES		MM		NOTE
	MIN	MAX	MIN	MAX	NOTE
Α	0.331	0.370	8.40	9.40	
В	0.378	0.417	9.60	10.60	
С	0.165	0.189	4.20	4.80	
D	0.027	0.037	0.68	0.94	
E	0.045	0.055	1.14	1.40	
G	0.	10	2.	54	TYP.
Н	0.096	0.134	2.43	3.40	
J	0.011	0.025	0.28	0.64	
K	0.071	0.131	1.80	3.32	
S	0.575	0.625	14.60	15.87	
V	0.042	0.058	1.07	1.47	
W	0.000	0.010	0.00	0.25	

### Suggested Solder Pad Layout

Unit:mm



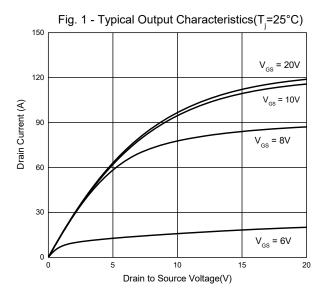


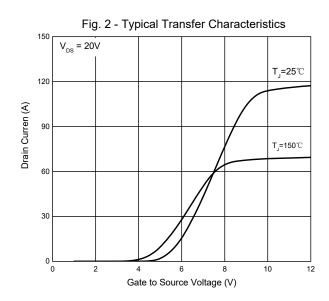
# **Electrical Characteristics** ( $T_J = 25\,^{\circ}$ C unless otherwise specified)

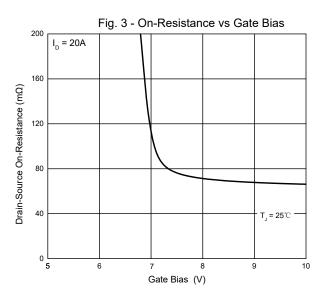
Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit	
Static Characteristics							
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	V <sub>GS</sub> =0V, I <sub>D</sub> =1mA	600			V	
Gate-Source Leakage Current	$I_{GSS}$	V <sub>DS</sub> =0V, V <sub>GS</sub> =±30V			±100	nA	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =600V, V <sub>GS</sub> =0V			10	μΑ	
Gate-Threshold Voltage	$V_{GS(th)}$	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =2.8mA	3	4	5	V	
Drain-Source On-Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =20A		65	78	mΩ	
Gate Resistance	$R_{g}$	f=1MHz, open drain		1		Ω	
Diode Characteristics			1		1	I	
Diode Forward Voltage	V <sub>SD</sub>	V <sub>GS</sub> =0V, I <sub>S</sub> =20A		0.9	1.2	V	
Reverse Recovery Time	t <sub>rr</sub>			115		ns	
Reverse Recovery Charge	Q <sub>rr</sub>	V <sub>R</sub> =400V, I <sub>F</sub> =20A dI <sub>F</sub> /dt=100A/µs		723		nC	
Peak Reverse Recovery Current	I <sub>rrm</sub>			11		Α	
Dynamic Characteristics							
Input Capacitance	C <sub>iss</sub>			3202			
Output Capacitance	C <sub>oss</sub>	$V_{DS}$ =100V, $V_{GS}$ =0V, f=1MHz		135		_	
Output capacitance - energy related	C <sub>o(er)</sub>	V <sub>DS</sub> =0 to 400V, V <sub>GS</sub> =0V		132		pF	
Output capacitance - time related	C <sub>o(tr)</sub>			857			
Total Gate Charge	Q <sub>g</sub>			81			
Gate-Source Charge	$Q_{gs}$	V <sub>DS</sub> =400V, V <sub>GS</sub> =10V, I <sub>D</sub> =20A		21		nC	
Gate-Drain Charge	$Q_{gd}$			41			
Turn-On Delay Time	$t_{d(on)}$			66			
Turn-On Rise Time	t <sub>r</sub>	V <sub>DD</sub> =400V, V <sub>GS</sub> =10V		20			
Turn-Off Delay Time	$t_{\text{d(off)}}$	$R_G=5.6\Omega$ , $I_D=20A$		57		ns	
Turn-Off Fall Time	t <sub>f</sub>	-		15			

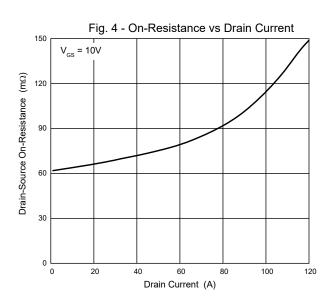


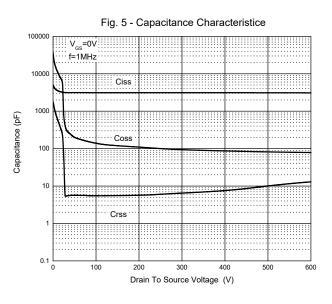
# **Typical Characteristics** ( $T_J$ =25 $^{\circ}$ C unless otherwise specified)

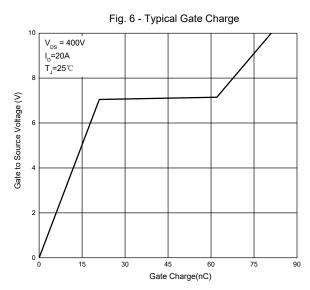






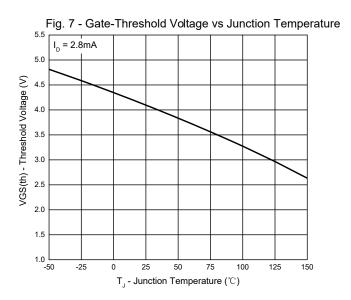


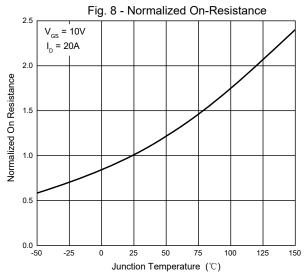


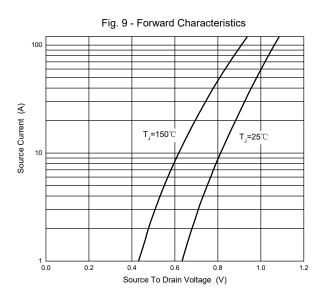


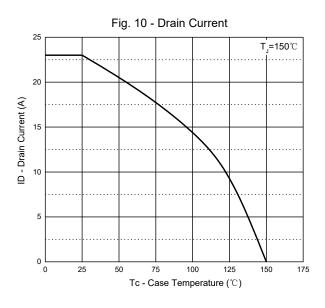


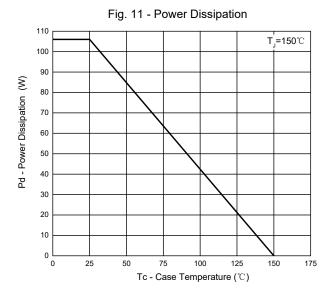
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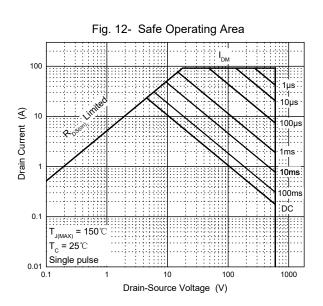














Typical Characteristics (T<sub>J</sub>=25 ℃ unless otherwise specified)

Fig.13 - Normalized Transient Thermal Impedance, Junction-Case

In descending order D = 0.5, 0.2, 0.1, 0.05, 0.02, 0.01, Single Pulse Normalizd Transient Thermal Resistance Zth(J-C) D (duty cycle) =  $T_{ON}/T$   $T_{J,PK} = T_C + P_{DM} \times Z_{th,JC}$   $R_{th,JC} = 1.18^{\circ}C/W$ 0.01 1E-3 L 1E-6 0.01 Rectangular Pulse Width (s)

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### **Ordering Information**

Device	Packing
Part Number-TP	Tape&Reel: 800pcs/Reel

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