

Features

- AEC-Q101 Qualified
- Split Gate Trench Mosfet Technology
- Dual-Side Cooling Package With Excellent Heat Dissipation
- Moisture Sensitivity Level 1
- Halogen Free, "Green" Device ^(Note 1)
- Epoxy Meets UL 94 V-0 Flammability Rating
- Lead Free Finish/RoHS Compliant ("P" Suffix Designates RoHS Compliant. See Ordering Information)

N-CHANNEL MOSFET

Maximum Ratings

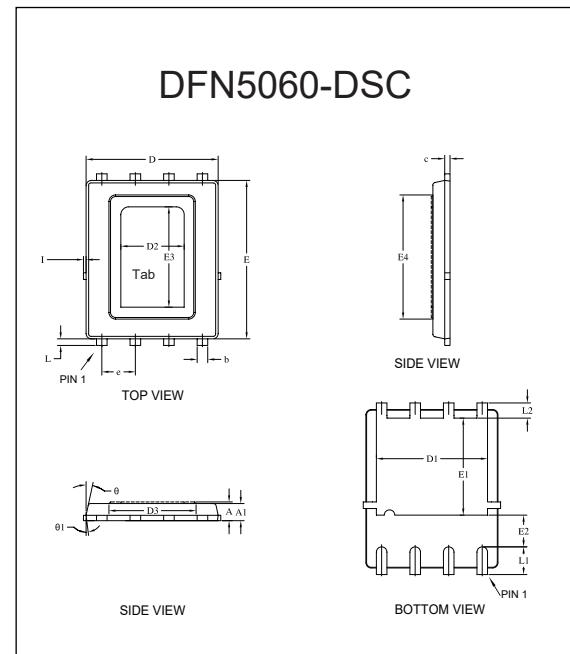
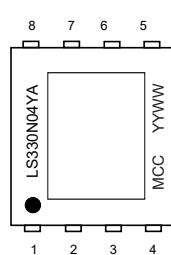
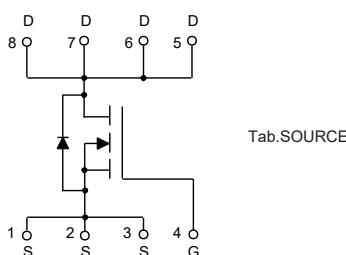
- Operating Junction Temperature Range : -55°C to +175°C
- Storage Temperature Range: -55°C to +175°C
- Thermal Resistance: 50°C/W Junction to Ambient ^(Note 2)
- Thermal Resistance: 0.57°C/W Junction to Case(Bottom)
- Thermal Resistance: 0.68°C/W Junction to Case (Top)

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	V _{DS}	40	V
Gate-Source Voltage	V _{GS}	±20	V
Continuous Drain Current T _C =25°C	I _D	330	A
T _C =100°C	I _D	233	A
Pulsed Drain Current ^(Note 3)	I _{DM}	1320	A
Total Power Dissipation ^(Note 4)	P _D	263	W
Avalance Energy ^(Note 5)	E _{AS}	1350	mJ

Note:

1. Halogen free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
2. The value of R_{θJA} is measured with the device mounted on 1in² FR-4 board with 2oz. Copper, in a still air environment with T_A =25°C.
3. Repetitive rating; pulse width limited by max. junction temperature.
4. P_D is based on max. junction temperature, using junction-case (Bottom) thermal resistance.
5. T_j=25°C, V_{DD}=30V, V_{GS}=10V, R_G=25Ω, L=12mH.

Internal Structure and Marking Code



Electrical Characteristics @ 25°C (Unless Otherwise Specified)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Static Characteristics						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS}=0V, I_D=1mA$	40			V
Gate-Source Leakage Current	I_{GSS}	$V_{DS}=0V, V_{GS}=\pm 20V$			± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS}=32V, V_{GS}=0V$			1	μA
Gate-Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=250\mu A$	2.0	2.6	4.0	V
Drain-Source On-Resistance	$R_{DS(on)}$	$V_{GS}=10V, I_D=75A$		0.83	1.1	$m\Omega$
		$V_{GS}=6V, I_D=20A$		1.3	2.0	
Gate Resistance	R_g	f=1 MHz, Open drain		1.5		Ω
Diode Characteristics						
Continuous Body Diode Current	I_S				330	A
Diode Forward Voltage	V_{SD}	$V_{GS}=0V, I_S=20A$			1.3	V
Reverse Recovery Time	t_{rr}	$I_F=165A, dI_F/dt=100A/\mu s$		68		ns
Reverse Recovery Charge	Q_{rr}			77		nC
Dynamic Characteristics						
Input Capacitance	C_{iss}	$V_{DS}=25V, V_{GS}=0V, f=1MHz$		7349		pF
Output Capacitance	C_{oss}			2350		
Reverse Transfer Capacitance	C_{rss}			165		
Total Gate Charge	Q_g	$V_{DS}=20V, V_{GS}=10V, I_D=165A$		115.9		nC
Gate-Source Charge	Q_{gs}			34.4		
Gate-Drain Charge	Q_{gd}			33.1		
Turn-On Delay Time	$t_{d(on)}$	$V_{DD}=20V, V_{GS}=10V, I_{DS}=165A, R_{GEN}=6\Omega$		33		ns
Turn-On Rise Time	t_r			78		
Turn-Off Delay Time	$t_{d(off)}$			76		
Turn-Off Fall Time	t_f			75		

Curve Characteristics

Fig.1 - Typical Output Characteristics

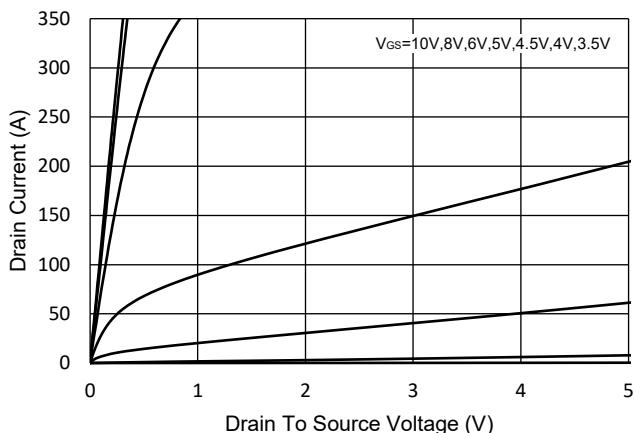


Fig.2 - Transfer Characteristic

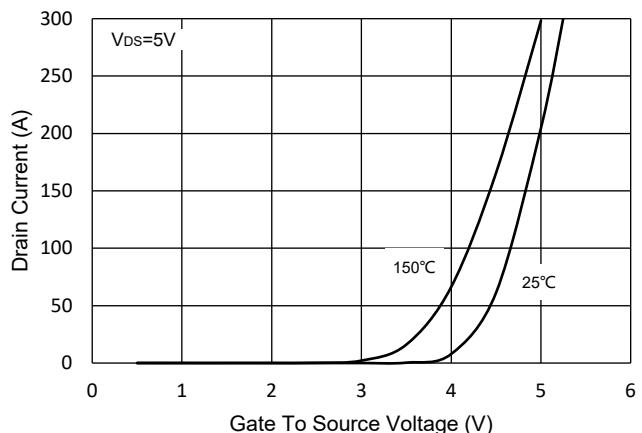


Fig.3 - $R_{DS(ON)}$ - V_{GS}

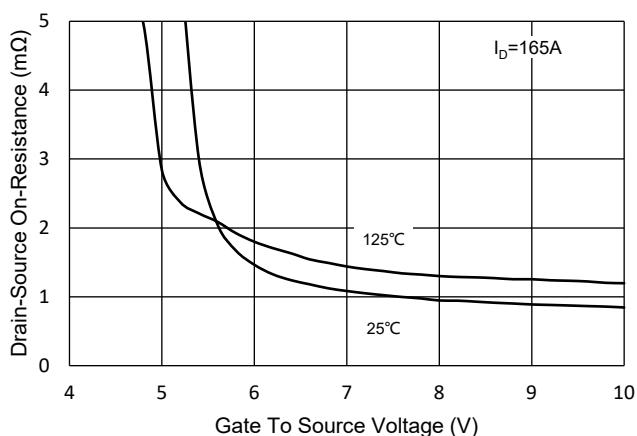


Fig.4 - $R_{DS(ON)}$ - I_D

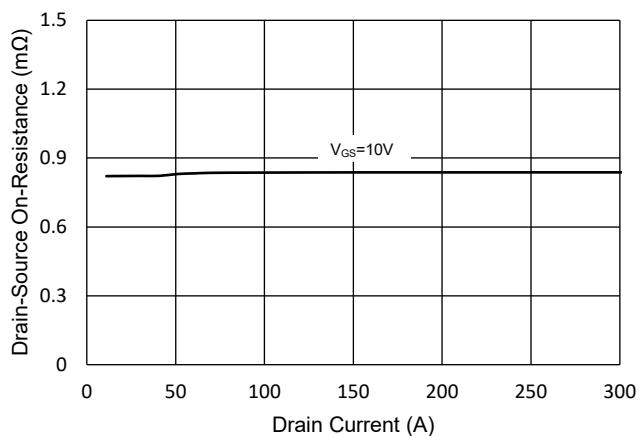


Fig.5 - Capacitance Characteristics

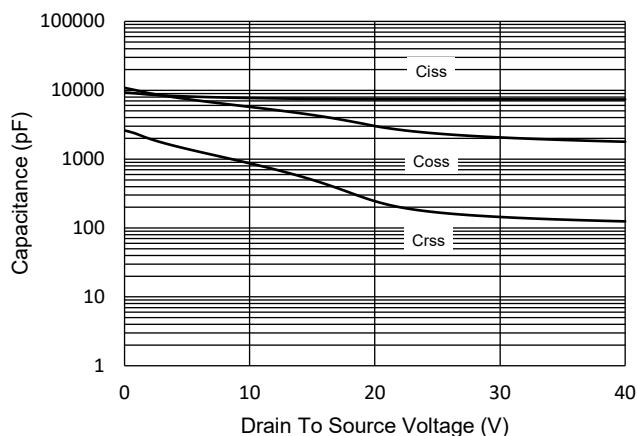
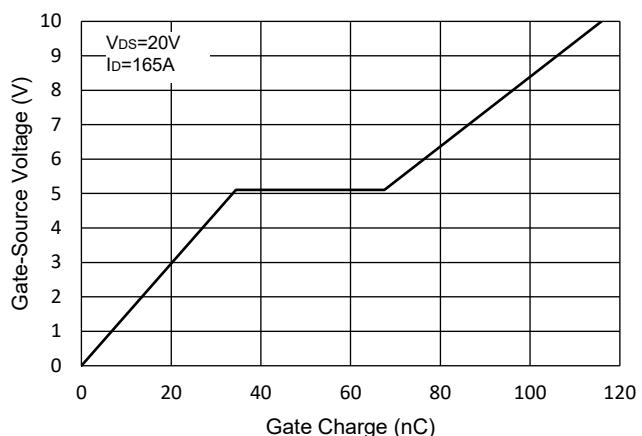
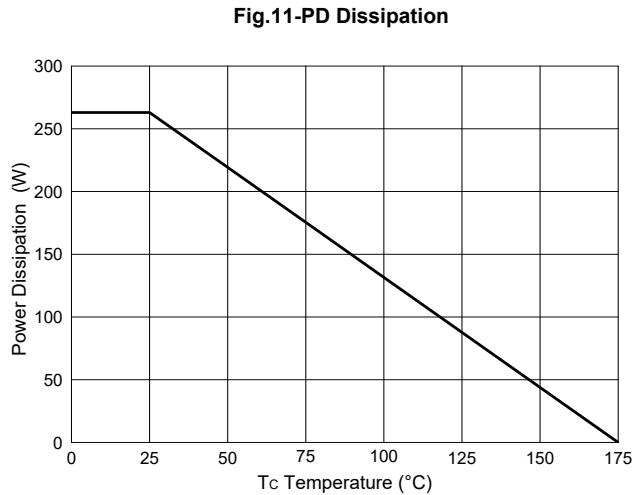
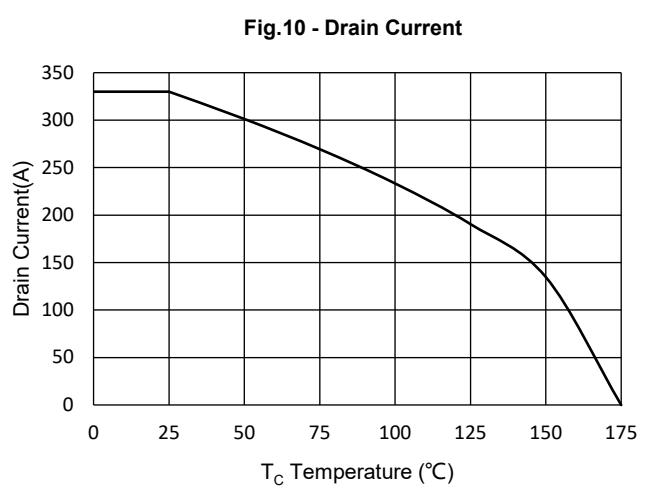
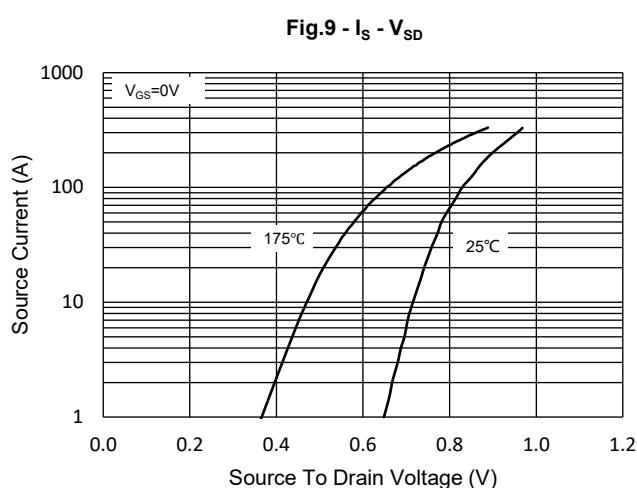
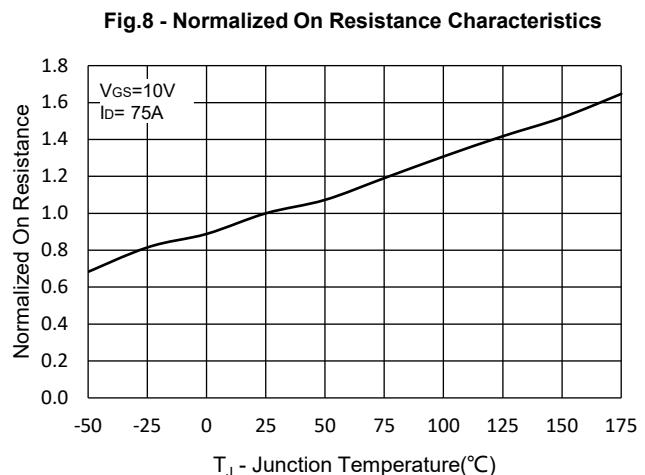
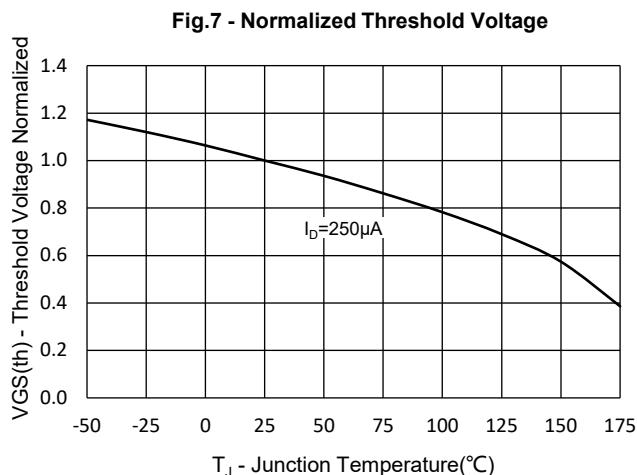


Fig.6 - Gate Charge



Curve Characteristics



Curve Characteristics

Fig.12 - Safe Operation Area

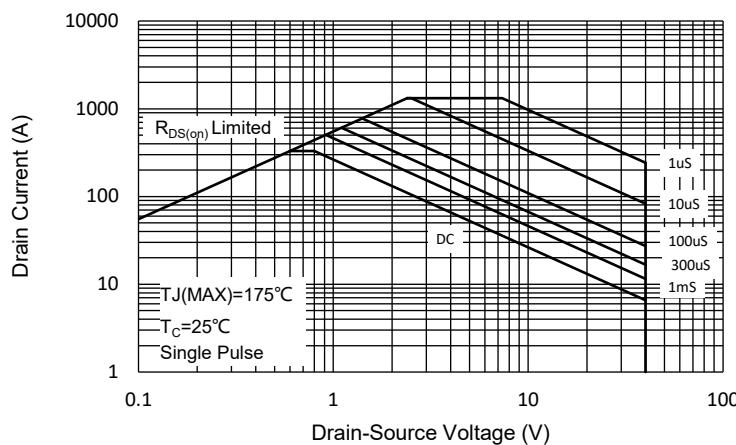


Fig.13 - Normalized Transient Thermal Impedance (Bottom)

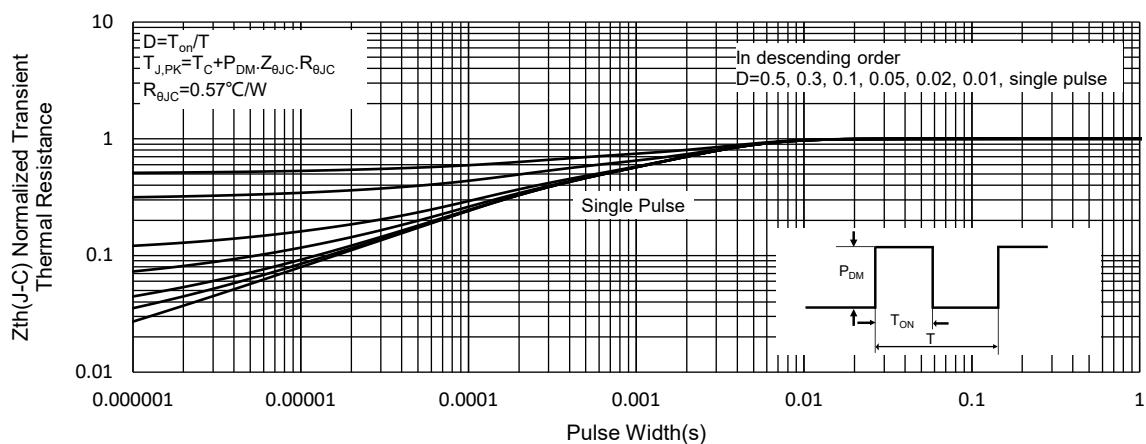
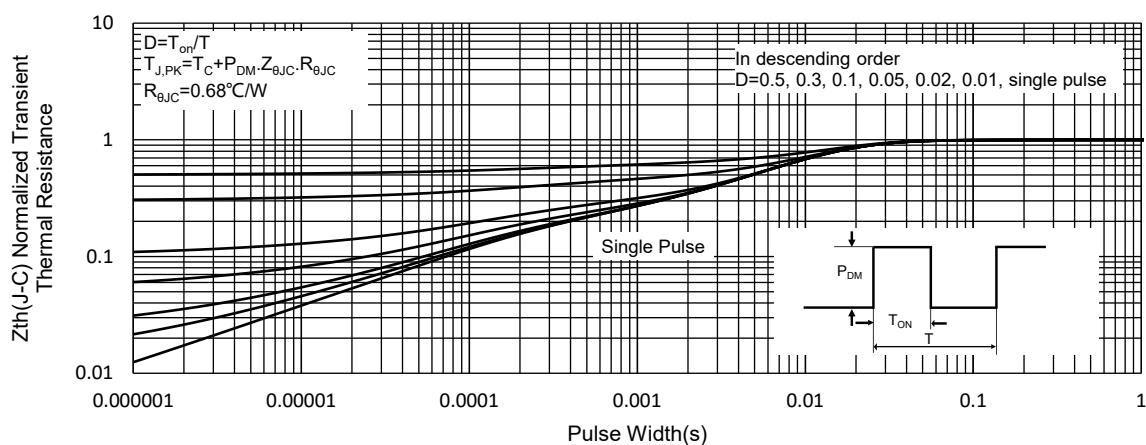


Fig.14 - Normalized Transient Thermal Impedance (Top)



Ordering Information

Device	Packing
Part Number-TP	Tape&Reel: 5Kpcs/Reel

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