

## Features

- Split Gate Trench Mosfet Technology
- Moisture Sensitivity Level 1
- Halogen Free. "Green" Device (Note 1)
- Epoxy Meets UL 94 V-0 Flammability Rating
- Lead Free Finish/RoHS Compliant (Note2)("P" Suffix Designates RoHS Compliant. See Ordering Information)

## Maximum Ratings

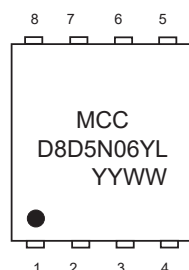
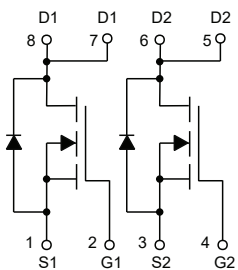
- Operating Junction Temperature Range : -55°C to +150°C
- Storage Temperature Range: -55°C to +150°C
- Thermal Resistance: 60°C/W Junction to Ambient (Note 3)
- Thermal Resistance: 2.2°C/W Junction to Case

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	$V_{DS}$	60	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	V
Continuous Drain Current	$I_D$	$T_C=25^\circ\text{C}$	50
		$T_C=100^\circ\text{C}$	31
Pulsed Drain Current (Note 4)	$I_{DM}$	200	A
Total Power Dissipation (Note 5)	$P_D$	56	W
Avalanche Energy (Note 6)	$E_{AS}$	88	mJ

Note:

1. Halogen free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
2. High Temperature Solder Exemption Applied, see EU Directive Annex 7(a)-I.
3. The value of  $R_{\theta JA}$  is measured with the device mounted on 1in<sup>2</sup> FR-4 board with 2oz. Copper, in a still air environment with  $T_A=25^\circ\text{C}$ .
4. Repetitive rating; pulse width limited by max. junction temperature.
5.  $P_D$  is based on max. junction temperature, using junction-case thermal resistance.
6.  $T_J=25^\circ\text{C}$ ,  $V_{DD}=40\text{V}$ ,  $V_{GS}=10\text{V}$ ,  $R_G=25\Omega$ ,  $L=0.1\text{mH}$ .

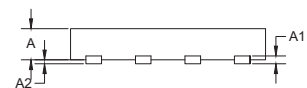
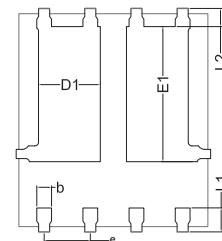
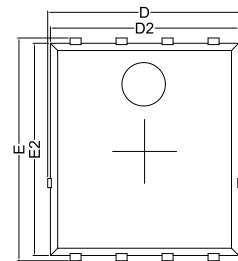
## Internal Structure and Marking Code



4 codes in total  
YY is the year  
WW is the week

# Dual N-CHANNEL MOSFET

## PDFN5060-8D



DIM	DIMENSIONS				NOTE
	INCHES		MM		
	MIN	MAX	MIN	MAX	
D	0.203	0.218	5.15	5.55	
D2	0.197	0.213	5.00	5.40	
E	0.234	0.250	5.95	6.35	
E2	0.223	0.238	5.66	6.06	
A	0.039	0.047	1.00	1.20	
A1	0.010		0.254		BSC
A2	0.000	0.004	0.00	0.10	
D1	0.059	0.075	1.50	1.90	
E1	0.139	0.154	3.52	3.92	
L1	0.022	0.030	0.56	0.76	
L2	0.019		0.50		BSC
b	0.012	0.020	0.31	0.51	
e	0.050		1.27		BSC

**Electrical Characteristics @ 25°C (Unless Otherwise Noted)**

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
<b>Static Characteristics</b>						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS}=0V, I_D=250\mu A$	60			V
Gate-Source Leakage Current	$I_{GSS}$	$V_{GS}=\pm 20V, V_{DS}=0V$			$\pm 100$	nA
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS}=60V, V_{GS}=0V$			1	$\mu A$
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=250\mu A$	1.0	1.8	2.5	V
Drain-Source On-Resistance	$R_{DS(on)}$	$V_{GS}=10V, I_D=20A$		6.5	8.5	m $\Omega$
		$V_{GS}=4.5V, I_D=10A$		8	11	
Gate resistance	$R_G$	f=1MHz, Open Drain		1.5		$\Omega$
<b>Diode Characteristics</b>						
Continuous Body Diode Current	$I_S$				50	A
Body Diode Voltage	$V_{SD}$	$I_S=20A, V_{GS}=0V$			1.2	V
Reverse Recovery Charge	$Q_{rr}$	$I_F=25A, di/dt=100A/\mu s$		38		nC
Reverse Recovery Time	$t_{rr}$			38		ns
<b>Dynamic Characteristics</b>						
Input Capacitance	$C_{iss}$	$V_{DS}=30V, V_{GS}=0V, f=1MHz$		1930		pF
Output Capacitance	$C_{oss}$			400		
Reverse Transfer Capacitance	$C_{rss}$			6		
Total Gate Charge	$Q_g$	$V_{DS}=30V, V_{GS}=10V, I_D=25A$		30.4		nC
Gate-Source Charge	$Q_{gs}$			6.6		
Gate-Drain Charge	$Q_{gd}$			5		
Turn-On Delay Time	$t_{d(on)}$	$V_{DD}=30V, I_D=25A,$ $V_{GS}=10V, R_G=2.2\Omega$		13.7		ns
Turn-On Rise Time	$t_r$			62		
Turn-Off Delay Time	$t_{d(off)}$			25		
Turn-Off Fall Time	$t_f$			4.4		

**Curve Characteristics**

Fig. 1 - Typical Output Characteristics

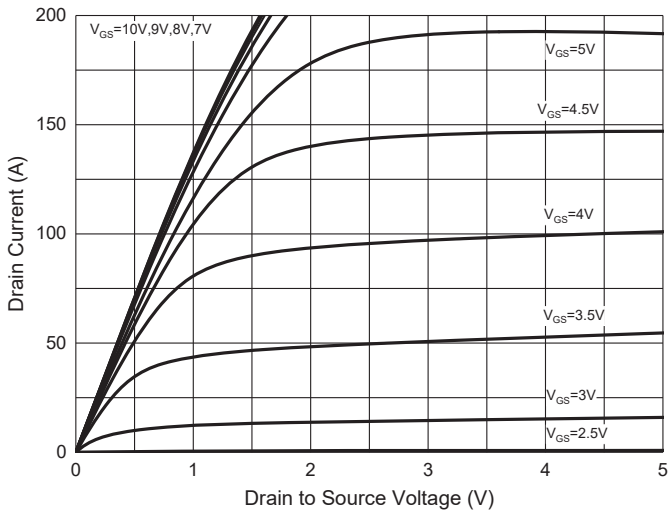


Fig. 2 - Transfer Characteristics

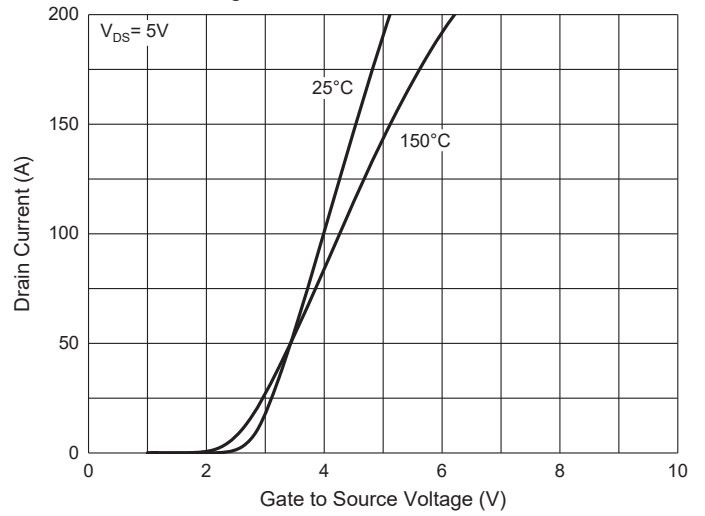


Fig. 3 -  $R_{DS(ON)}-V_{GS}$

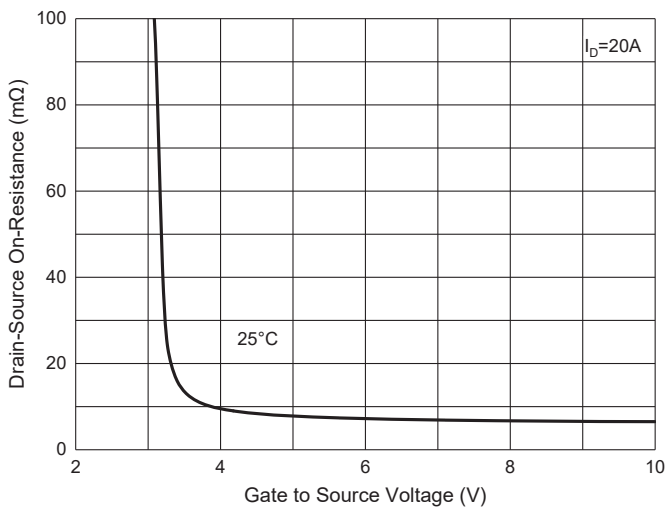


Fig. 4 -  $R_{DS(ON)}-I_D$

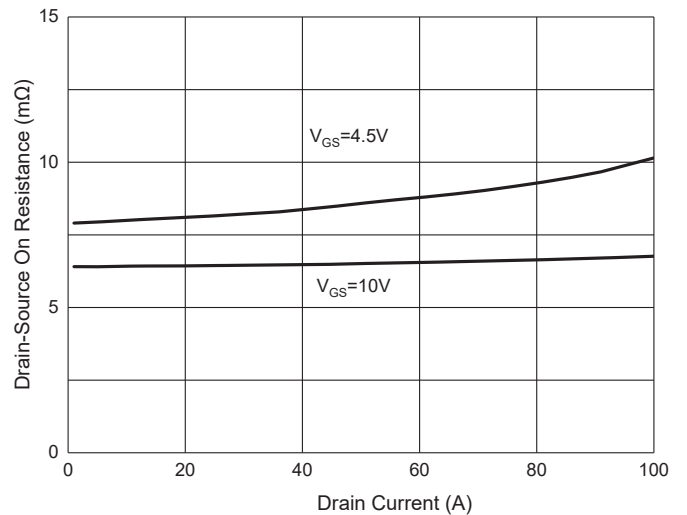


Fig. 5 - Capacitance Characteristics

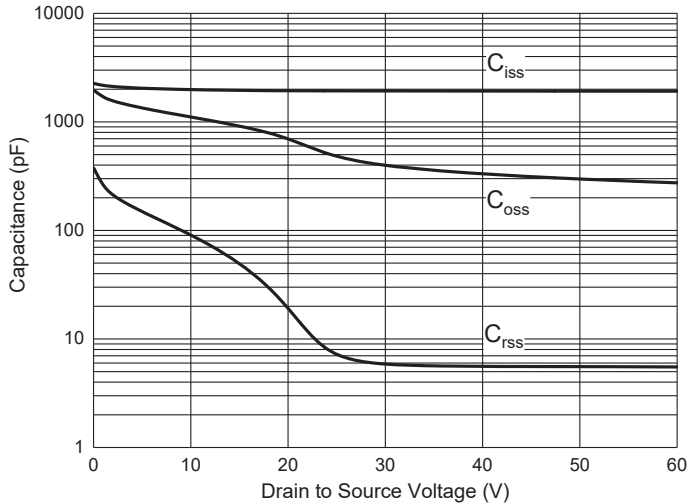
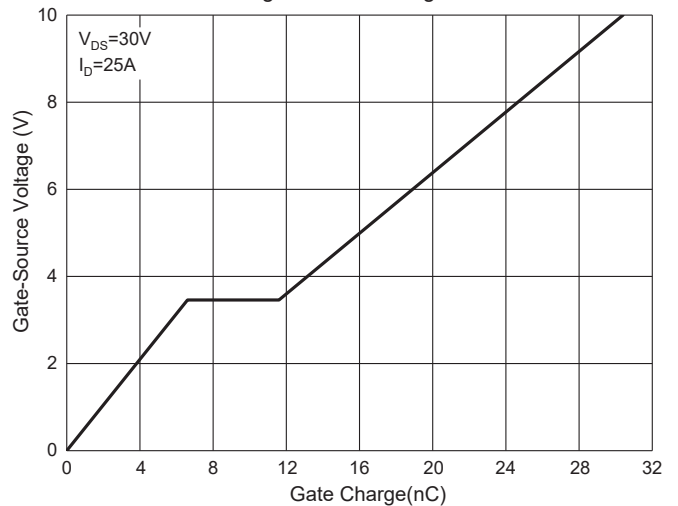


Fig. 6 - Gate Charge



**Curve Characteristics**

Fig. 7 - Normalized Threshold Voltage

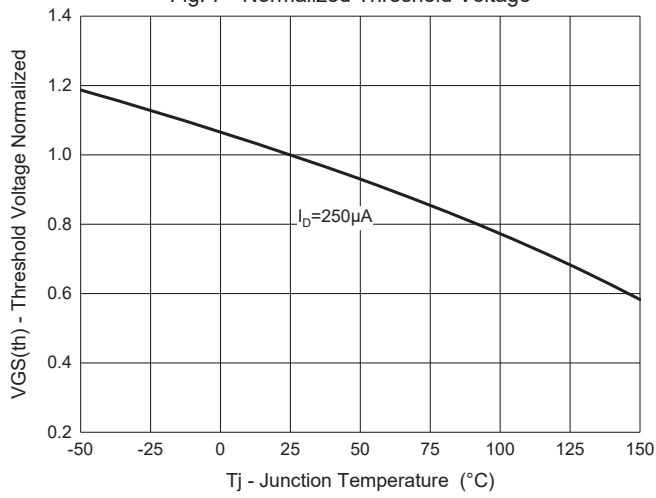


Fig. 8 - Normalized On Resistance Characteristics

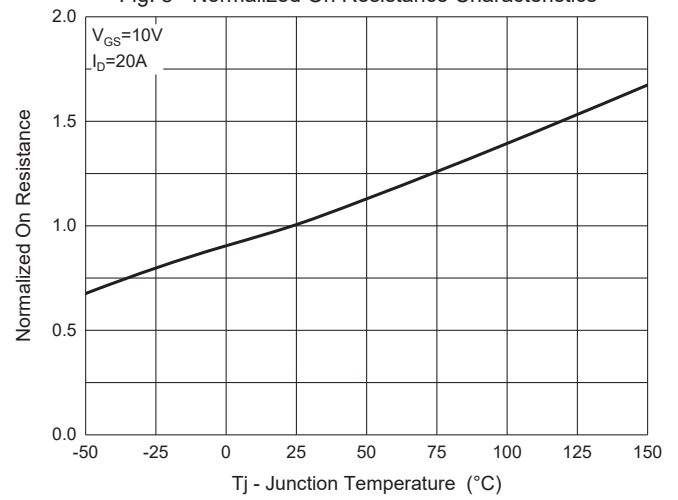


Fig. 9 - I<sub>S</sub>-V<sub>SD</sub>

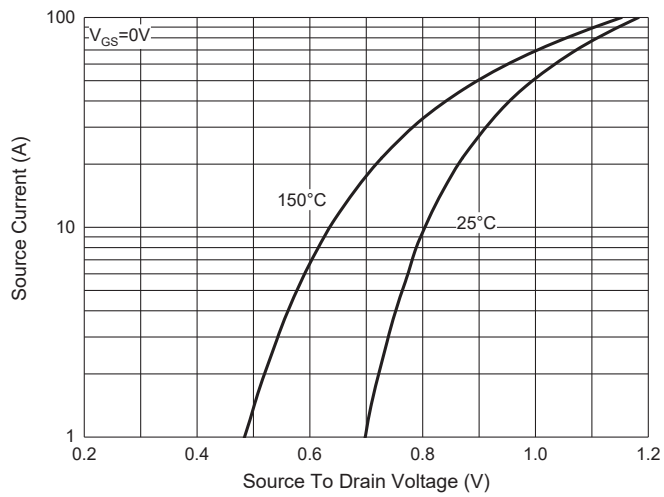


Fig. 10 - Drain Current

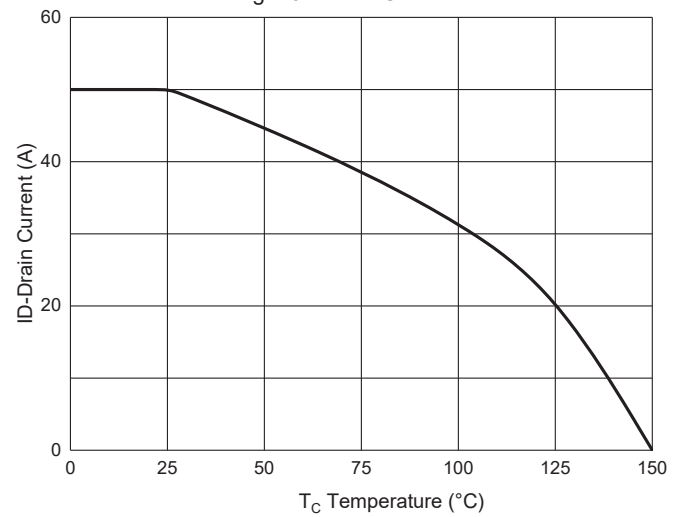
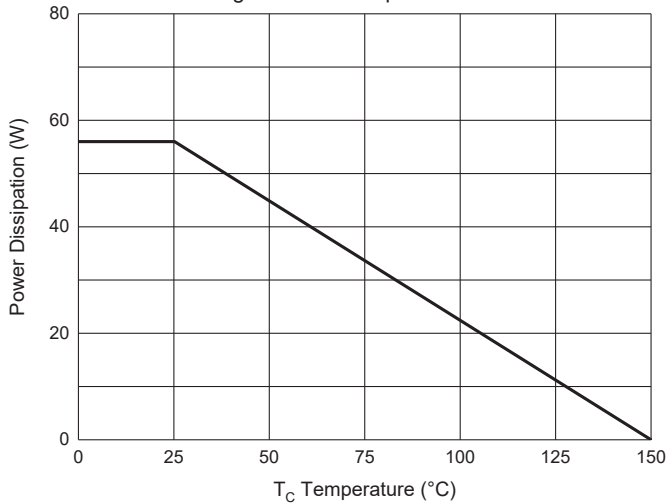


Fig. 11 - PD Dissipation



Curve Characteristics

Fig. 12 - Safe Operation Area

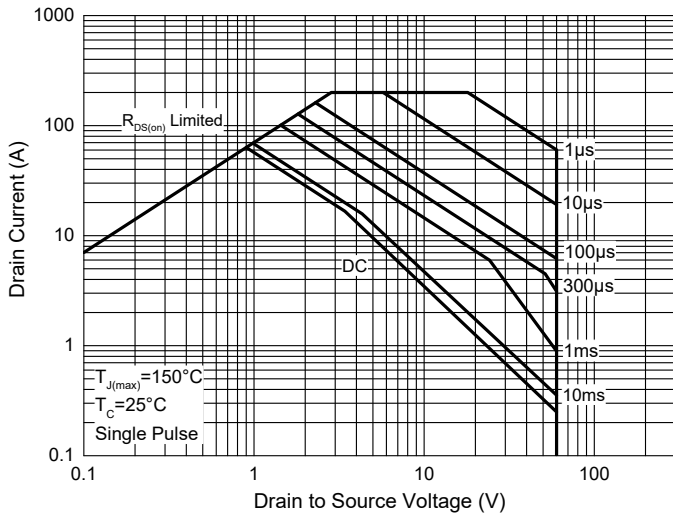
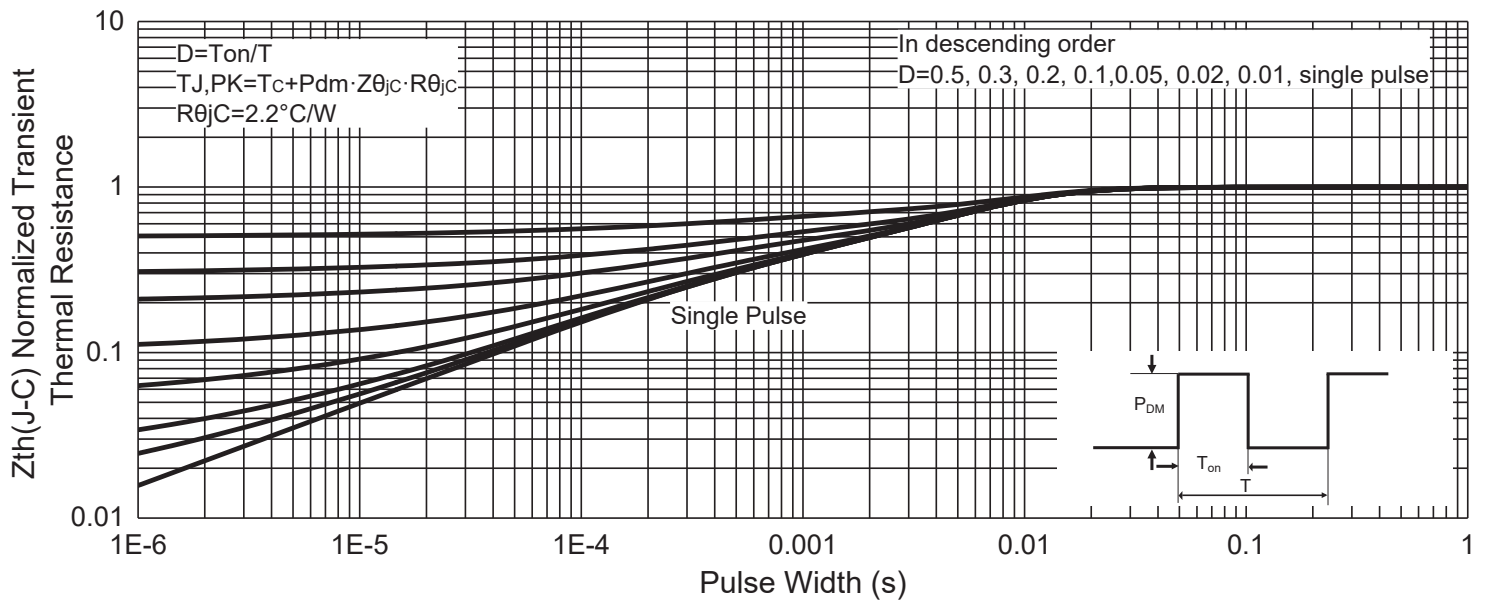


Fig. 13 - Normalized Transient Thermal Impedance



## Ordering Information

Device	Packing
Part Number-TP	Tape&Reel: 5Kpcs/Reel

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