

Features

- Split Gate Trench MOSFET technology
- AEC-Q101 Qualified
- Excellent Package for Heat Dissipation
- High Density Cell Design for Low $R_{DS(on)}$
- Halogen Free. "Green" Device (Note 1)
- Epoxy Meets UL 94 V-0 Flammability Rating
- Lead Free Finish/RoHS Compliant ("P" Suffix Designates RoHS Compliant. See Ordering Information)
- Moisture Sensitivity Level 1

Maximum Ratings

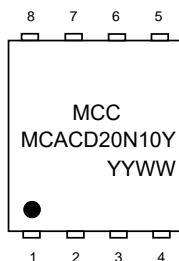
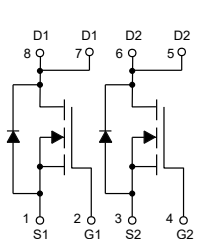
- Operating Junction Temperature Range : -55°C to +150°C
- Storage Temperature Range: -55°C to +150°C
- Thermal Resistance: 50°C/W Junction to Ambient (Note 2)
- Thermal Resistance: 2.5°C/W Junction to Case

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	V_{DS}	100	V
Gate-Source Voltage	V_{GS}	±20	V
Continuous Drain Current	I_D	$T_c=25^\circ\text{C}$	20
		$T_c=70^\circ\text{C}$	13
Pulsed Drain Current (Note 3)	I_{DM}	80	A
Total Power Dissipation (Note 4)	P_D	50	W
Single Pulsed Avalanche Energy (Note 5)	E_{AS}	81	mJ

Note:

1. Halogen free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
2. The value of $R_{\theta JA}$ is measured with the device mounted on 1in² FR-4 board with 2oz. Copper, in a still air environment with $T_A=25^\circ\text{C}$. The Power dissipation P_{DSM} is based on $R_{\theta JA} \leq 10\text{s}$ and the maximum allowed junction temperature of 150°C. The value in any given application depends on the user's specific board design.
3. Repetitive rating; pulse width limited by max. junction temperature.
4. P_D is based on max. junction temperature, using junction-Case thermal resistance.
5. $T_j=25^\circ\text{C}$, $V_{DD}=50\text{V}$, $V_{GS}=10\text{V}$, $L=2\text{mH}$.

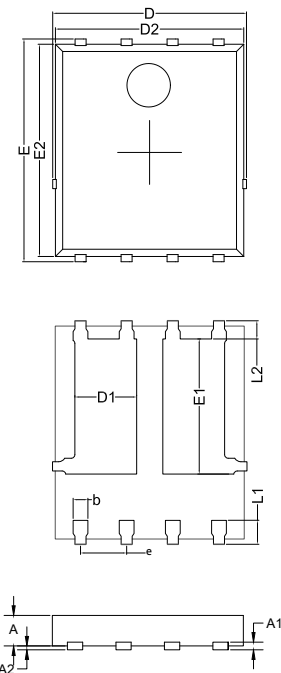
Internal Structure and Marking Code



4 codes in total
YY is the year
WW is the week

Dual N-CHANNEL MOSFET

PDFN5060-8D



DIM	INCHES		MM		NOTE
	MIN	MAX	MIN	MAX	
D	0.203	0.218	5.15	5.55	
D2	0.197	0.213	5.00	5.40	
E	0.234	0.250	5.95	6.35	
E2	0.223	0.238	5.66	6.06	
A	0.039	0.047	1.00	1.20	
A1	0.010		0.254		BSC
A2	0.000	0.004	0.00	0.10	
D1	0.059	0.075	1.50	1.90	
E1	0.139	0.154	3.52	3.92	
L1	0.022	0.030	0.56	0.76	
L2	0.019		0.50		BSC
b	0.012	0.020	0.31	0.51	
e	0.050		1.27		BSC

Electrical Characteristics @ 25°C (Unless Otherwise Specified)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Static Characteristics						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS}=0V, I_D=250\mu A$	100			V
Gate-Source Leakage Current	I_{GSS}	$V_{DS}=0V, V_{GS}=\pm 20V$			± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS}=100V, V_{GS}=0V$			1	μA
Gate-Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=250\mu A$	1	1.8	2.5	V
Drain-Source On-Resistance	$R_{DS(on)}$	$V_{GS}=10V, I_D=20A$		16	22	m Ω
		$V_{GS}=4.5V, I_D=10A$		18	27	
Gate Resistance	R_g	f=1MHz		1.5		Ω
Diode Characteristics						
Continuous Body Diode Current	I_S				20	A
Diode Forward Voltage	V_{SD}	$V_{GS}=0V, I_S=20A$			1.3	V
Reverse Recovery Time	t_{rr}	$I_S=20A, di/dt=100A/\mu s$		40		ns
Reverse Recovery Charge	Q_{rr}			42		nC
Dynamic Characteristics						
Input Capacitance	C_{iss}	$V_{DS}=25V, V_{GS}=0V, f=1MHz$		1240		pF
Output Capacitance	C_{oss}			740		
Reverse Transfer Capacitance	C_{riss}			25		
Total Gate Charge	Q_g	$V_{DS}=50V, V_{GS}=10V, I_D=20A$		17		nC
Gate-Source Charge	Q_{gs}			6		
Gate-Drain Charge	Q_{gd}			3		
Turn-On Delay Time	$t_{d(on)}$	$V_{DS}=50V, V_{GS}=10V, R_{GEN}=3\Omega, I_D=20A$		40		ns
Turn-On Rise Time	t_r			12		
Turn-Off Delay Time	$t_{d(off)}$			55		
Turn-Off Fall Time	t_f			16		

Curve Characteristics

Fig. 1 - Typical Output Characteristics

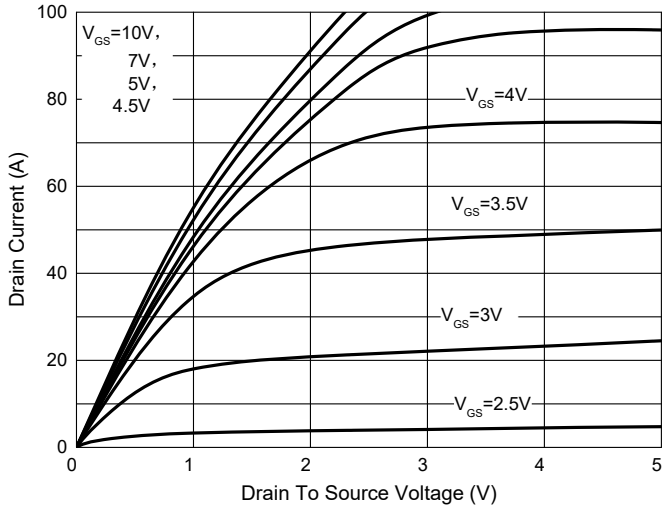


Fig. 2 - Transfer Characteristics

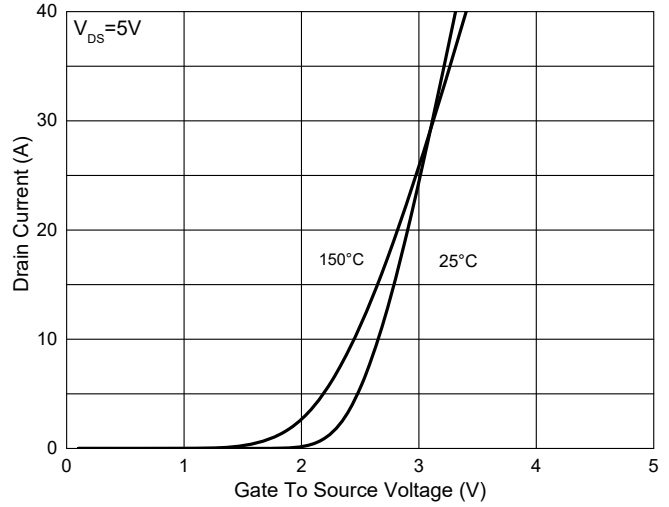


Fig. 3 - $R_{DS(ON)} - V_{GS}$

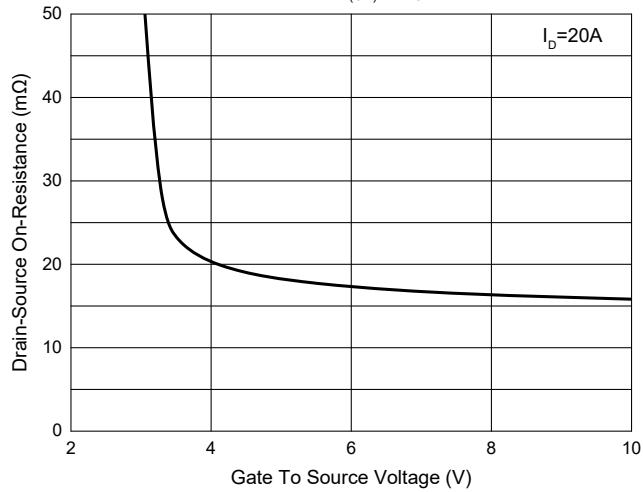


Fig. 4 - $R_{DS(ON)} - I_D$

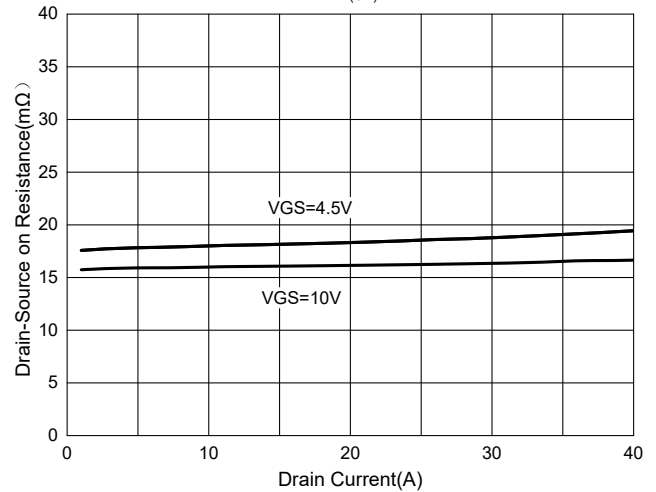


Fig. 5 - Capacitance Characteristics

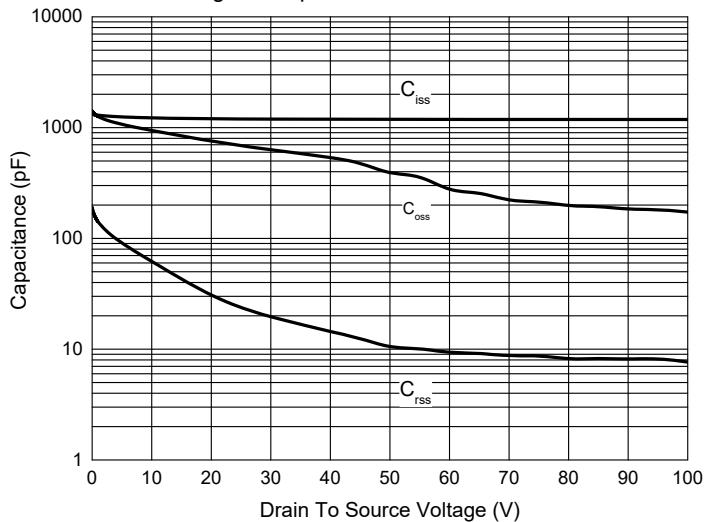
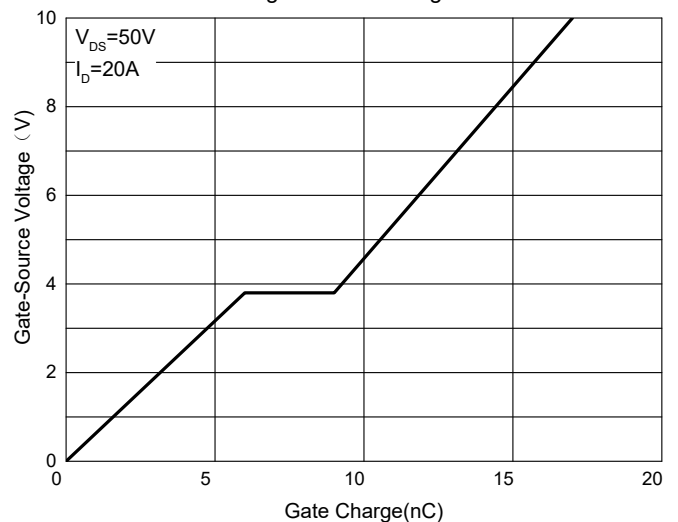


Fig. 6 - Gate Charge



Curve Characteristics

Fig. 7 - Normalized Threshold Voltage

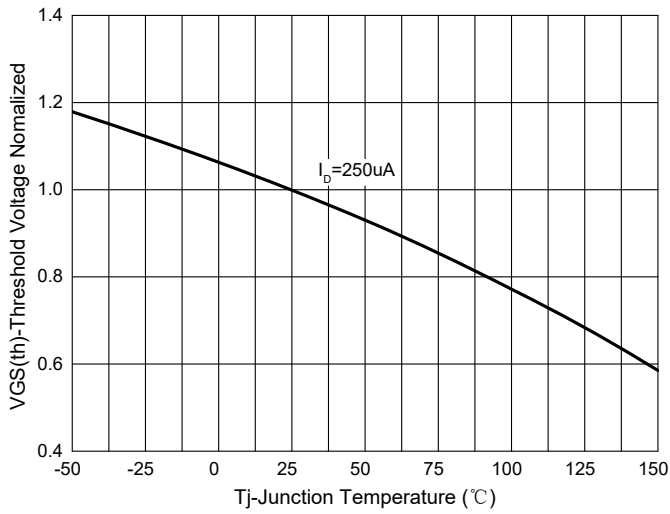


Fig.8-Normalized On Resistance Characteristics

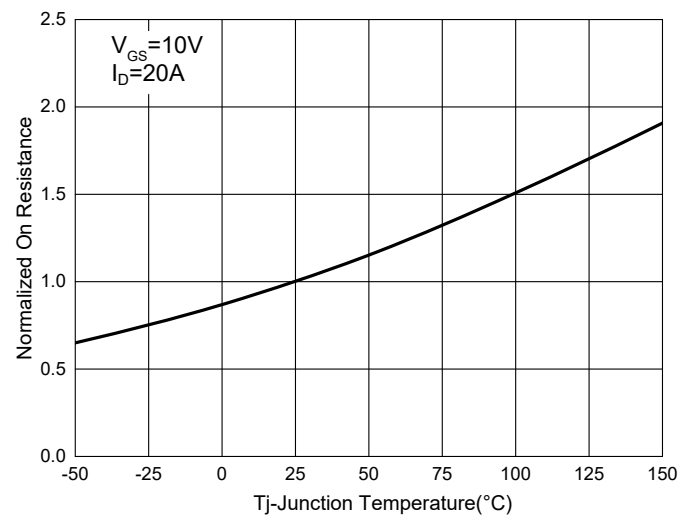


Fig.9 - $I_s - V_{SD}$

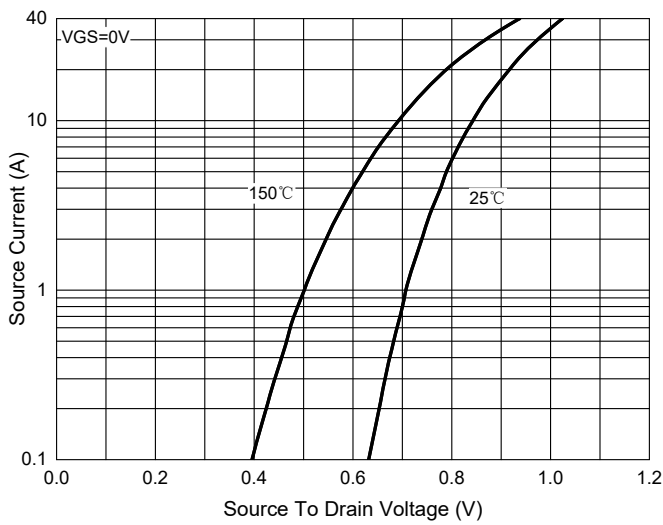


Fig. 10 - Drain Current

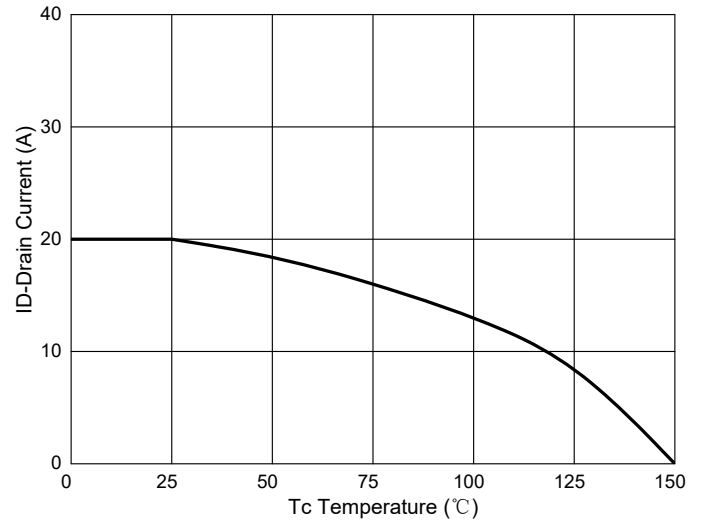
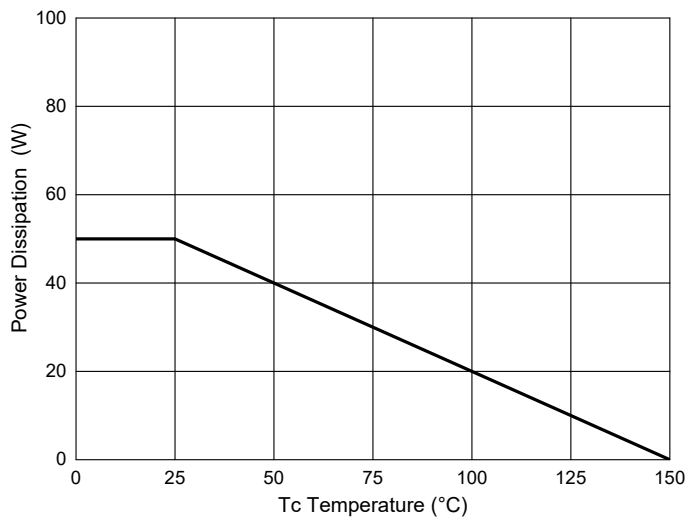


Fig.11-PD Dissipation



Curve Characteristics

Fig. 12 - Safe Operation Area

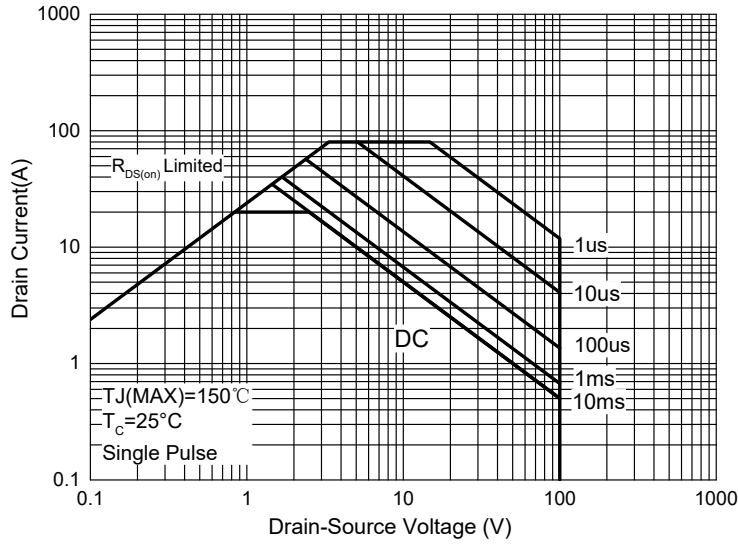
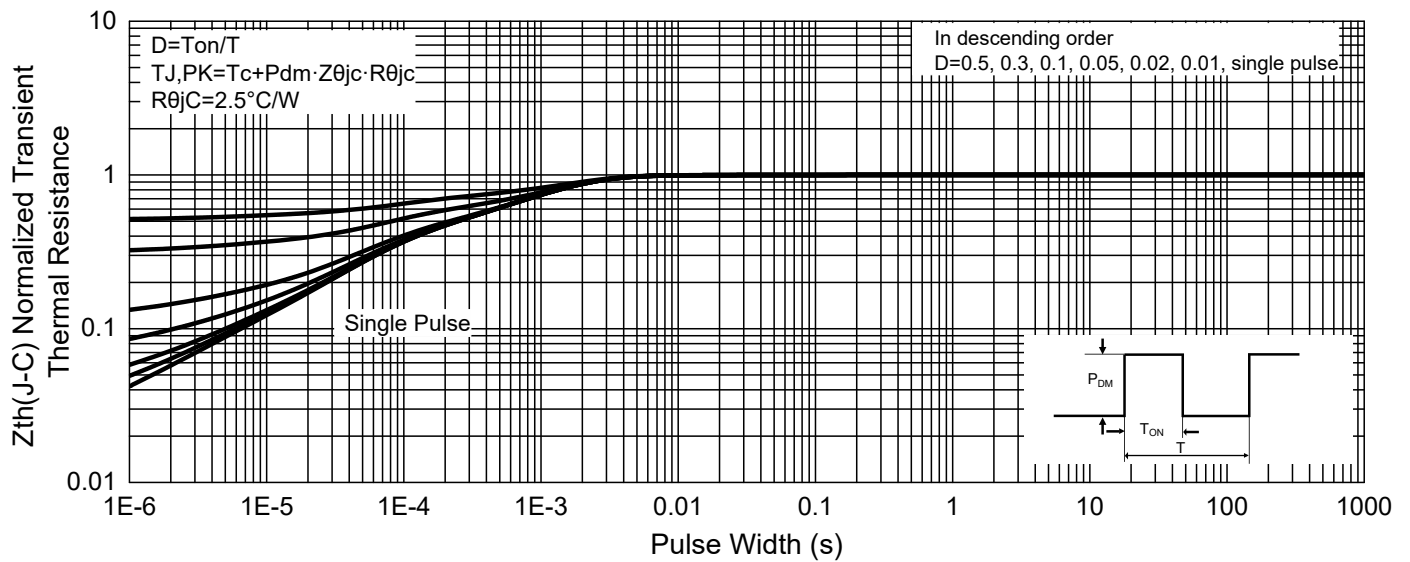


Fig. 13 - Normalized Transient Thermal Impedance



Ordering Information

Device	Packing
Part Number-TP	Tape&Reel: 5Kpcs/Reel

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