

Features

- AEC-Q101 Qualified
- Split Gate Trench MOSFET Technology
- High Density Cell Design For Low $R_{DS(on)}$
- Moisture Sensitivity Level 1
- Halogen Free."Green"Device^(Note1)
- Epoxy Meets UL 94 V-0 Flammability Rating
- Lead Free Finish/RoHS Compliant^(Note2)("P" Suffix Designates RoHS Compliant. See Ordering Information)

Maximum Ratings

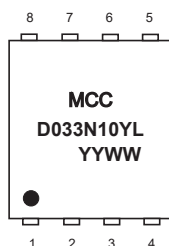
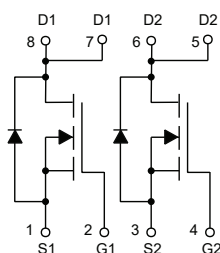
- Operating Junction Temperature Range : -55°C to +175°C
- Storage Temperature Range: -55°C to +175°C
- Thermal Resistance: 60°C/W Junction to Ambient^(Note3)
- Thermal Resistance: 3.6°C/W Junction to Case

Parameter		Symbol	Rating	Unit
Drain-Source Voltage		V _{DS}	100	V
Gate-Source Voltage		V _{GS}	±20	V
Continuous Drain Current	T _C =25°C	I _D	20	A
	T _C =100°C		14	
Pulsed Drain Current (Note4)		I _{DM}	80	A
Total Power Dissipation(Note5)		P _D	41	W
Single Pulse Avalanche Energy (Note6)		E _{AS}	20	mJ

Note:

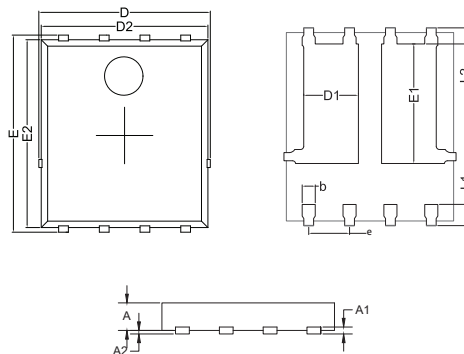
1. Halogen free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
2. High Temperature Solder Exemption Applied, see EU Directive Annex 7a.
3. The value of $R_{\theta JA}$ is measured with the device mounted on 1in² FR-4 board with 2oz. Copper, in a still air environment with $T_A = 25^\circ\text{C}$.
4. Repetitive rating; pulse width limited by max. junction temperature.
5. P_D is based on max. junction temperature, using junction-case thermal resistance.
6. $T_J = 25^\circ\text{C}$, $V_{DD} = 80\text{V}$, $V_{GS} = 10\text{V}$, $R_G = 25\Omega$, $L = 0.5\text{mH}$.

Internal Structure and Marking Code



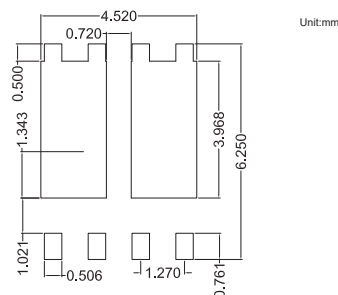
Dual N-CHANNEL MOSFET

PDFN5060-8D



DIM	INCHES		MM		NOTE
	MIN	MAX	MIN	MAX	
D	0.203	0.218	5.15	5.55	
D2	0.197	0.213	5.00	5.40	
E	0.234	0.250	5.95	6.35	
E2	0.223	0.238	5.66	6.06	
A	0.039	0.047	1.00	1.20	
A1	0.010		0.254		BSC
A2	0.000	0.004	0.00	0.10	
D1	0.059	0.075	1.50	1.90	
E1	0.139	0.154	3.52	3.92	
L1	0.022	0.030	0.56	0.76	
L2	0.019		0.50		BSC
b	0.012	0.020	0.31	0.51	
e	0.050		1.27		BSC

Suggested Solder Pad Layout



Electrical Characteristics @ 25°C (Unless Otherwise Specified)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Static Characteristics						
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} =0V, I _D =250μA	100			V
Gate-Source Leakage Current	I _{GSS}	V _{DS} =0V, V _{GS} =±20V			±100	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =100V, V _{GS} =0V			1	μA
Gate-Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =250μA	1.2	1.7	2.2	V
Drain-Source On-Resistance	R _{DS(on)}	V _{GS} =10V, I _D =20A		25	33	mΩ
		V _{GS} =4.5V, I _D =10A		32	42	
Gate Resistance	R _g	f=1MHz, Open drain		1.1		Ω
Diode Characteristics						
Continuous Body Diode Current	I _S				20	A
Diode Forward Voltage	V _{SD}	V _{GS} =0V, I _S =20A			1.2	V
Reverse Recovery Time	t _{rr}	I _F =15A, dI _F /dt=100A/μs		40.7		ns
Reverse Recovery Charge	Q _{rr}			67.5		nC
Dynamic Characteristics						
Input Capacitance	C _{iss}	V _{DS} =25V, V _{GS} =0V, f=1MHz		520		pF
Output Capacitance	C _{oss}			260		
Reverse Transfer Capacitance	C _{rss}			34		
Total Gate Charge	Q _g	V _{DS} =50V, V _{GS} =10V, I _D =15A		12.7		nC
Gate-Source Charge	Q _{gs}			2.2		
Gate-Drain Charge	Q _{gd}			5.5		
Turn-On Delay Time	t _{d(on)}	V _{DD} =50V, V _{GS} =10V, I _{DS} =15A, R _{GEN} =2.2Ω		5.3		ns
Turn-On Rise Time	t _r			36		
Turn-Off Delay Time	t _{d(off)}			13		
Turn-Off Fall Time	t _f			2.4		

Curve Characteristics

Fig. 1 - Typical Output Characteristics

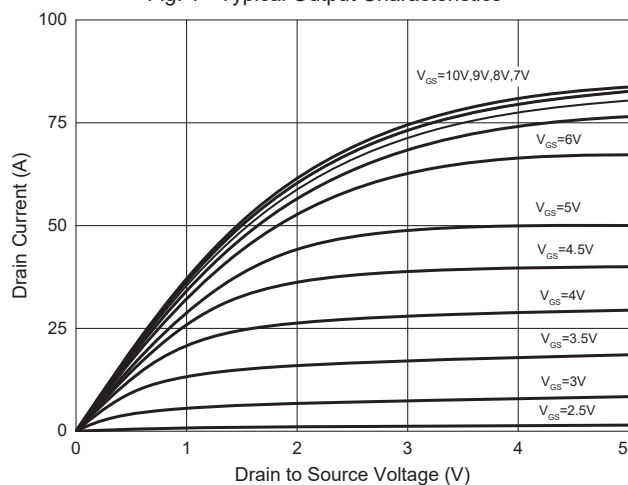


Fig. 2 - Transfer Characteristics

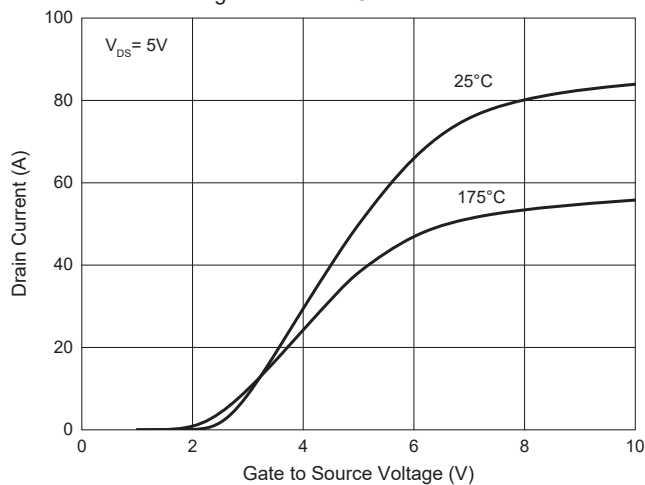


Fig. 3 - $R_{DS(ON)}-V_{GS}$

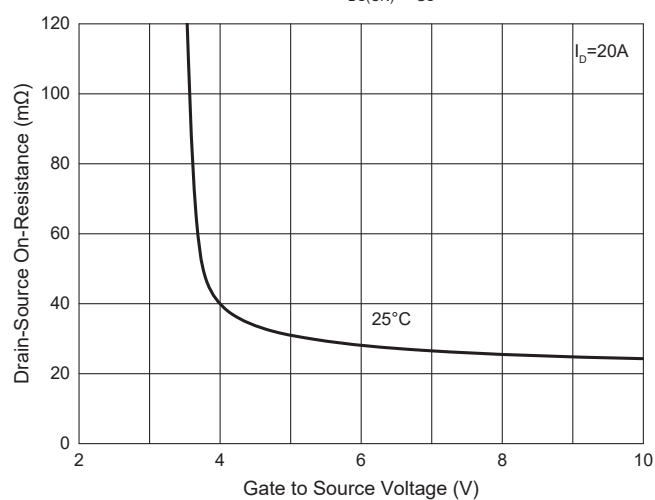


Fig. 4 - $R_{DS(ON)}-I_D$

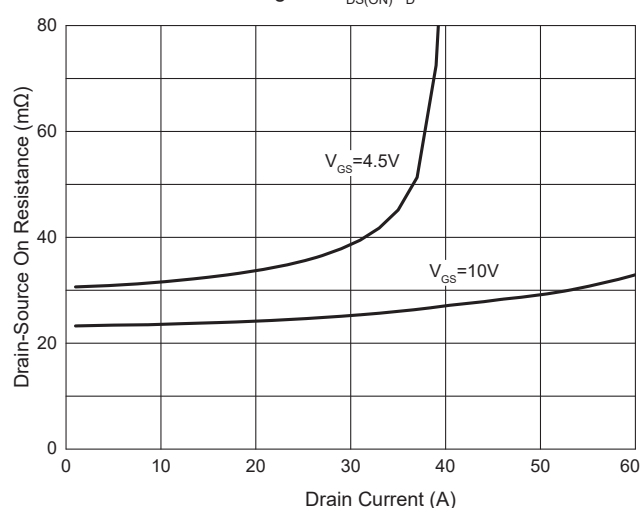


Fig. 5 - Capacitance Characteristics

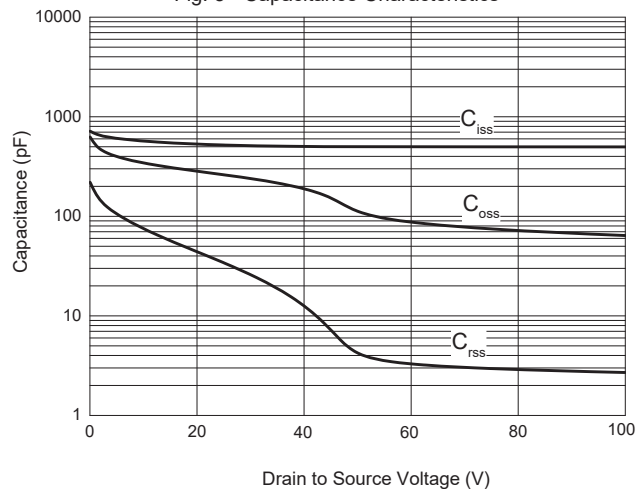
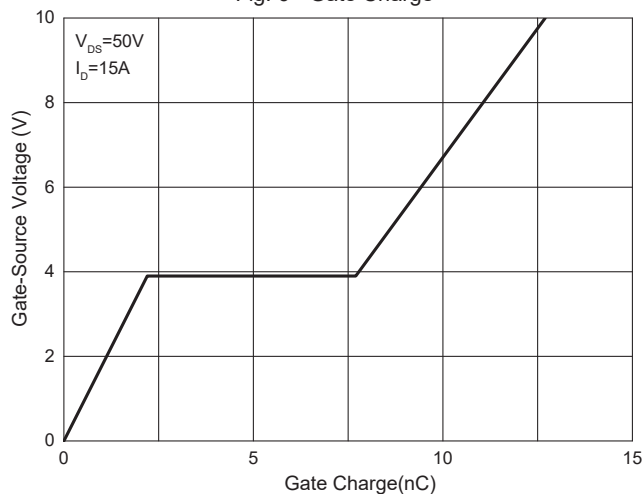


Fig. 6 - Gate Charge



Curve Characteristics

Fig. 7 - Normalized Threshold Voltage

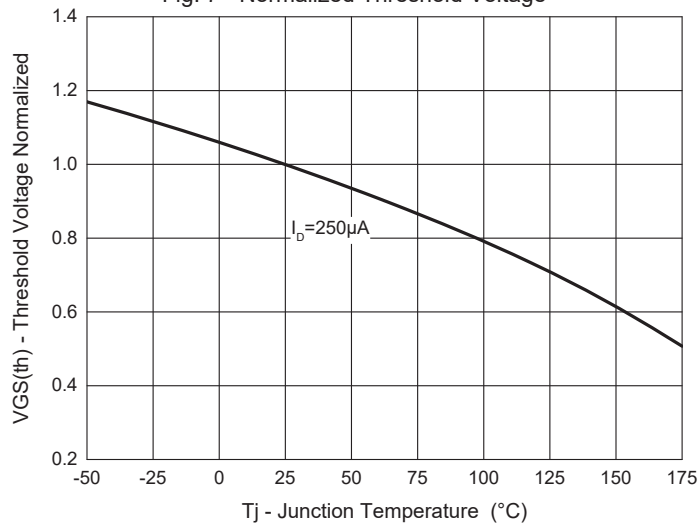


Fig. 8 - Normalized On Resistance Characteristics

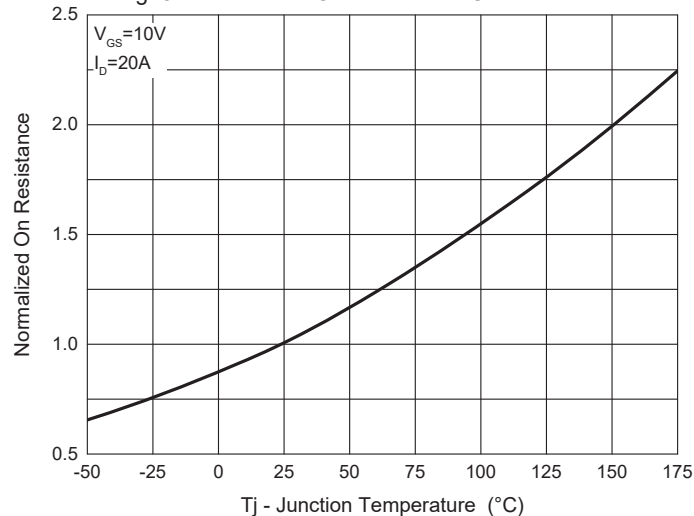


Fig. 9 - I_S - V_{SD}

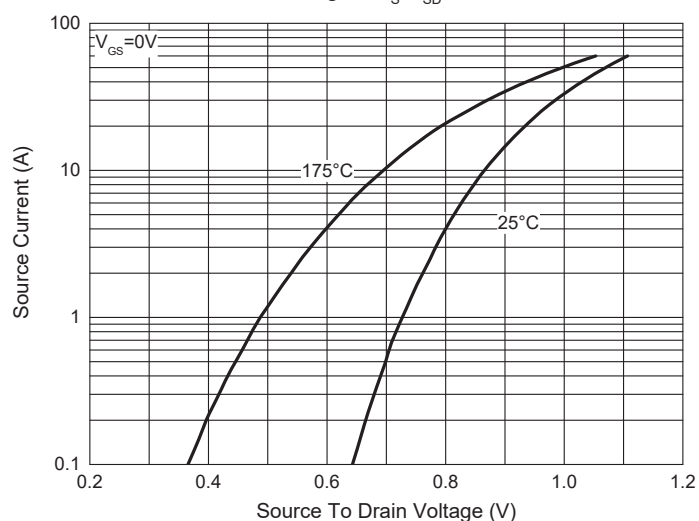


Fig. 10 - Drain Current

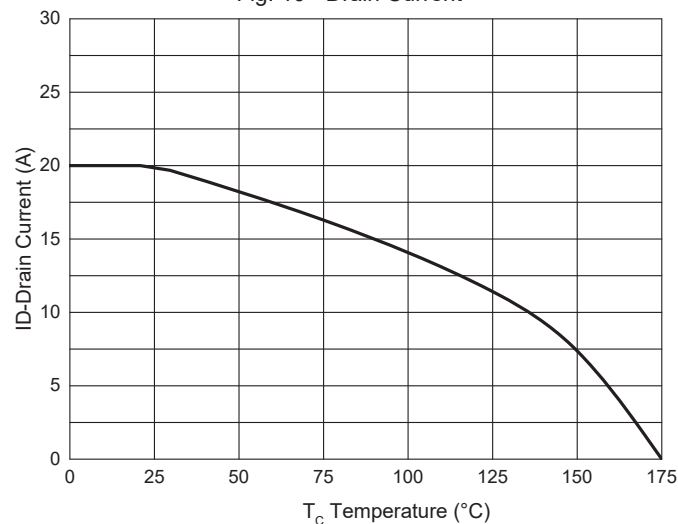
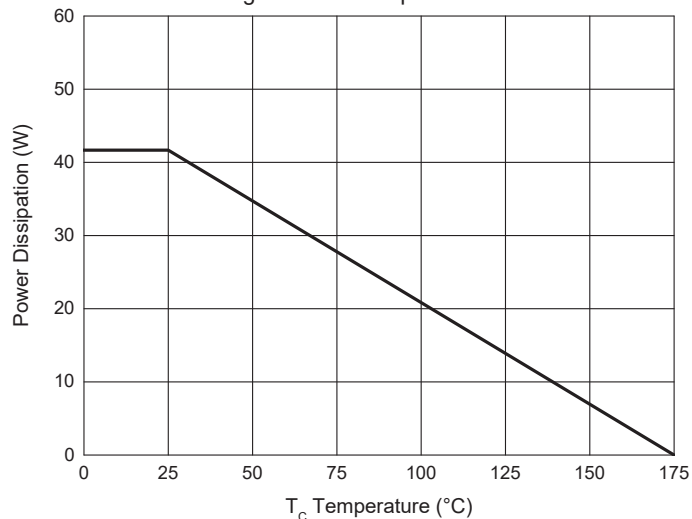


Fig. 11 - PD Dissipation



Curve Characteristics

Fig. 12 - Safe Operation Area

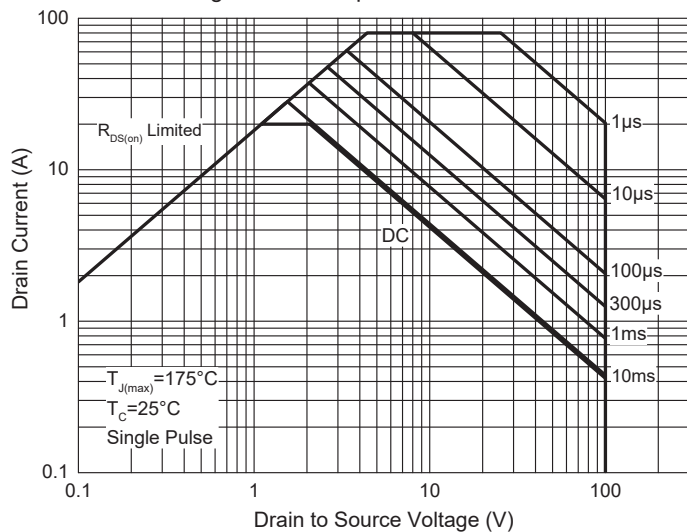
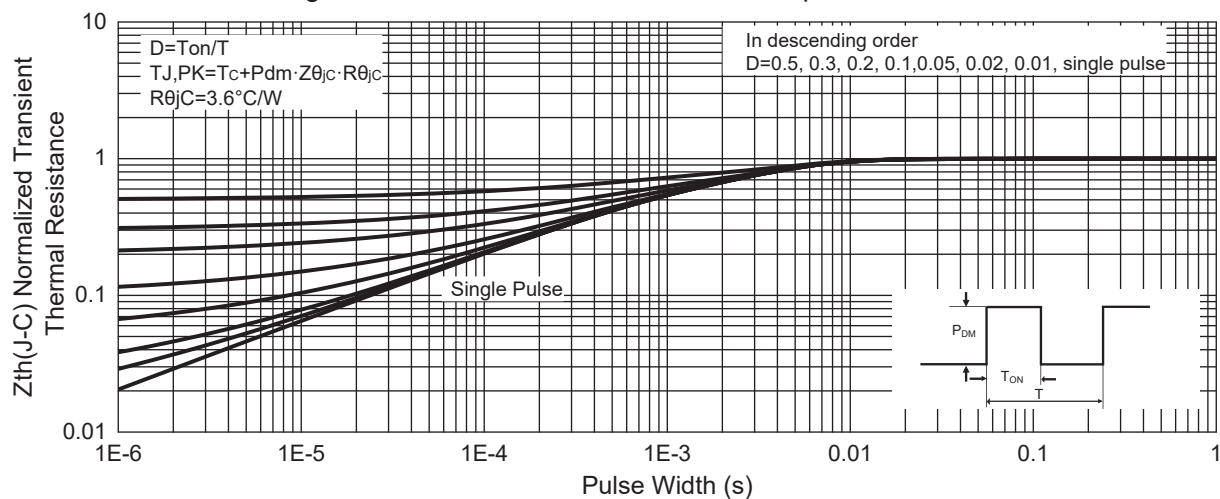


Fig. 13 - Normalized Transient Thermal Impedance



Ordering Information

Device	Packing
Part Number-TP	Tape&Reel: 5Kpcs/Reel

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