

#### **Features**

- Trench MOSFET Technology
- High Density Cell Design for Low R<sub>DS(ON)</sub>
- · Moisture Sensitivity Level 1
- Halogen Free. "Green" Device (Note 1)
- Epoxy Meets UL 94 V-0 Flammability Rating
- Lead Free Finish/RoHS Compliant ("P" Suffix Designates RoHS Compliant. See Ordering Information)

# **Maximum Ratings**

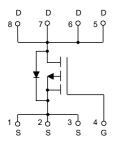
- Operating Junction Temperature Range: -55°C to +175°C
- Storage Temperature Range: -55°C to +175°C
- Thermal Resistance: 50°C/W Junction to Ambient (Note 2)
- Thermal Resistance: 1.15°C/W Junction to Case

Thermal Registance. 1.10 6,77 bandien to Gase					
Parameter		Symbol	Rating	Unit	
Drain-Source Voltage		$V_{DS}$	-60	٧	
Gate-Source Volltage		V <sub>GS</sub>	±20	V	
Continuous Drain Current	T <sub>C</sub> =25°C	- I <sub>D</sub>	-60	A	
	T <sub>C</sub> =100°C	'D	-42		
Pulsed Drain Current (Note 3)		$I_{DM}$	-240	Α	
Total Power Dissipation <sup>(Note 4)</sup>		P <sub>D</sub>	130	W	
Single Pulsed Avalanche Energy <sup>(Note 5)</sup>		E <sub>AS</sub>	225	mJ	

#### Note:

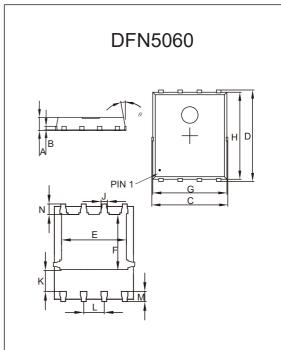
- 1. Halogen free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds. The
- 2. value of  $R_{\theta,JA}$  is measured with the device mounted on 1in<sup>2</sup> FR-4 board with 2oz. Copper, in a still air environment with  $T_A$  =25°C.
- 3. Repetitive rating; pulse width limited by max. junction temperature.
- 4. P<sub>D</sub> is based on max. junction temperature, using junction-case thermal resistance.
- 5.  $T_J$ =25°C,  $V_{DD}$ =-50V,  $V_{GS}$ =-10V, $R_G$ =25 $\Omega$ , L=0.5mH

# **Internal Structure and Marking Code**





# P-CHANNEL MOSFET



	DIMENSIONS					
DIM	INC	HES	MM		NOTE	
Dilvi	MIN	MAX	MIN	MAX	NOIL	
Α	0.031	0.047	0.80	1.20		
В	0.010		0.254		TYP.	
С	0.193	0.222	4.90	5.64		
D	0.232	0.250	5.90	6.35		
Е	0.148	0.167	3.75	4.25		
F	0.126	0.154	3.20	3.92		
G	0.189	0.213	4.80	5.40		
Н	0.222	0.239	5.65	6.06		
K	0.045	0.059	1.15	1.50		
J	0.012	0.020	0.30	0.50		
L	0.046	0.054	1.17	1.37		
М	0.012	0.028	0.30	0.71		
N	0.016	0.028	0.40	0.71		



# Electrical Characteristics @ 25°C (Unless Otherwise Specified)

Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit
Static Characteristics				1	1	I
Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =-250μA	-60			V
Gate-Source Leakage Current	I <sub>GSS</sub>	V <sub>DS</sub> =0V, V <sub>GS</sub> =±20V			±100	nA
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =-60V, V <sub>GS</sub> =0V			-1	μA
Gate-Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =-250μA	-2	-2.6	-3.5	V
Drain-Source On-Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> =-10V, I <sub>D</sub> =-20A		13	18	mΩ
Gate Resistance	R <sub>g</sub>	f=1 MHz, Open drain		2.6		Ω
Diode Characteristics						
Continuous Body Diode Current	Is				-60	Α
Diode Forward Voltage	V <sub>SD</sub>	V <sub>GS</sub> =0V, I <sub>S</sub> =-20A			-1.2	V
Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> =-20A,di/dt=100A/μs		35		ns
Reverse Recovery Charge	Q <sub>rr</sub>	- 1 <sub>F</sub> =-20Α,αι/αι=100Α/μ5		44		nC
Dynamic Characteristics						
Input Capacitance	C <sub>iss</sub>			6683		
Output Capacitance	C <sub>oss</sub>	$V_{DS}$ =-25V, $V_{GS}$ =0V,f=1MHz		365		pF
Reverse Transfer Capacitance	C <sub>rss</sub>			302		
Total Gate Charge	$Q_g$			109		
Gate-Source Charge	$Q_{gs}$	$V_{DS}$ =-30V, $V_{GS}$ =-10V, $I_{D}$ =-20A		23		nC
Gate-Drain Charge	$Q_{gd}$			24		
Turn-On Delay Time	t <sub>d(on)</sub>			21		
Turn-On Rise Time	t <sub>r</sub>	V <sub>DD</sub> =-30V, V <sub>GS</sub> =-10V,		39		ns
Turn-Off Delay Time	t <sub>d(off)</sub>	$R_G=3\Omega$ , $I_{DS}=-20A$		122		113
Turn-Off Fall Time	t <sub>f</sub>			46		



### **Curve Characteristics**

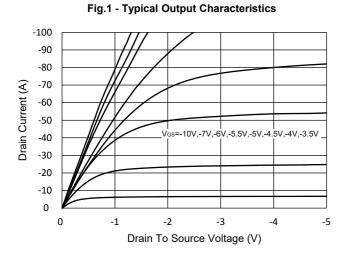


Fig.2 - Transfer Characteristic

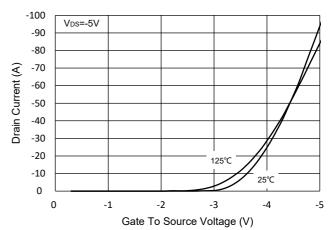


Fig.3 - R<sub>DS(ON)</sub> - V<sub>GS</sub>

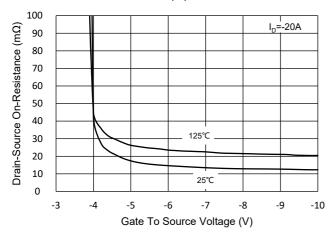


Fig. 4 -  $R_{DS(ON)}$ — $I_D$ 

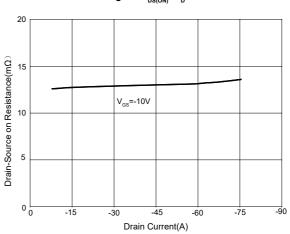


Fig.5 - Capacitance Characteristics

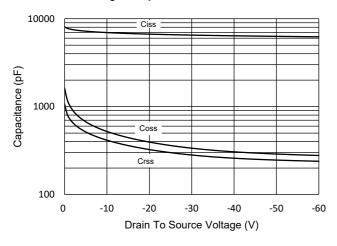
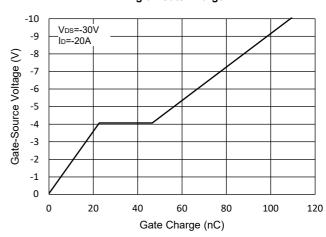
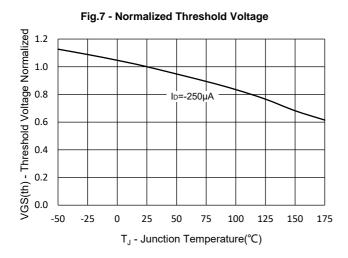


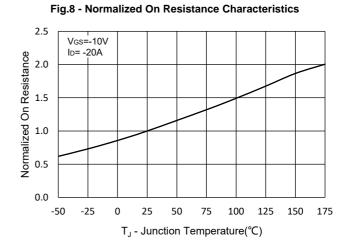
Fig.6 - Gate Charge

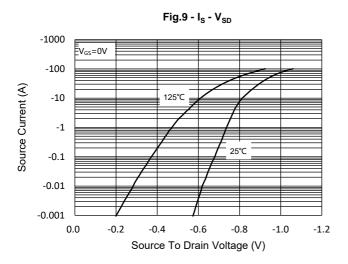


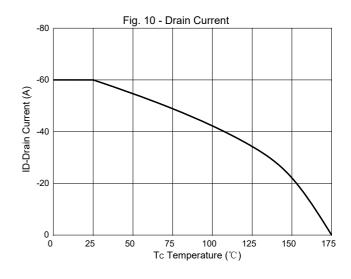


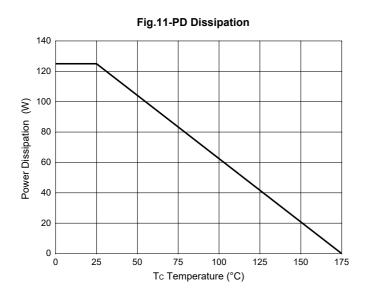
## **Curve Characteristics**













### **Curve Characteristics**



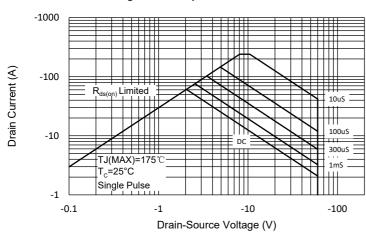
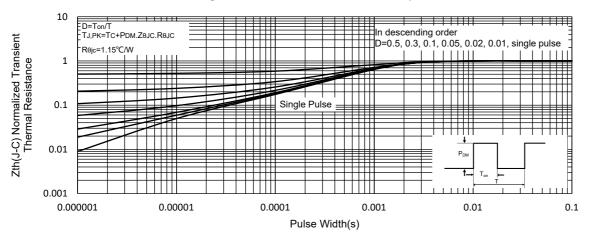


Fig.13 - Normalized Transient Thermal Impedance





# **Ordering Information**

Device	Packing	
Part Number-TP	Tape&Reel: 5Kpcs/Reel	

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