

#### **Features**

- High Density Cell Desihn for Low R<sub>DS(on)</sub>
- · Fully Characterized Avalanche Voltage and Current
- · Excellent Package for Good Heat Dissipation
- Special Process Technology for High ESD Capability
- · Epoxy Meets UL 94 V-0 Flammability Rating
- · Moisture Sensitivity Level 1
- Halogen Free. "Green" Device (Note 1)
- Lead Free Finish/RoHS Compliant ("P" Suffix Designates RoHS Compliant. See Ordering Information)

# **Maximum Ratings**

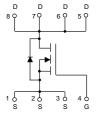
- Operating Junction Temperature Range: -55°C to +150°C
- Storage Temperature Range: -55°C to +150°C
- Thermal Resistance: 62.5°C/W Junction to Ambient<sup>(Note 2)</sup>

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	V <sub>DS</sub>	30	V
Gate-Source Volltage	V <sub>GS</sub>	±20	V
Continuous Drain Current	I <sub>D</sub>	100	Α
Pulsed Drain Current	I <sub>DM</sub>	400	Α
Total Power Dissipation (Note 2)	P <sub>D</sub>	2	W

#### Note:

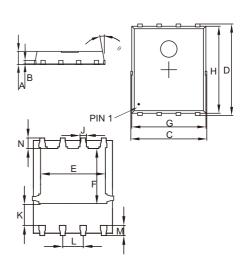
- 1. Halogen free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 2. Mounted on a Glass Epoxy Board of 25.4mm x 25.4mm x 0.8mm.

# **Internal Structure**



# N-CHANNEL MOSFET





	DIMENSIONS				
DIM	INCHES		MM		NOTE
Dilvi	MIN	MAX	MIN	MAX	NOTE
Α	0.031	0.047	0.80	1.20	
В	0.010		0.254		TYP.
С	0.193	0.222	4.90	5.64	
D	0.232	0.250	5.90	6.35	
Е	0.148	0.167	3.75	4.25	
F	0.126	0.154	3.20	3.92	
G	0.189	0.213	4.80	5.40	
Н	0.222	0.239	5.65	6.06	
K	0.045	0.059	1.15	1.50	
J	0.012	0.020	0.30	0.50	
L	0.046	0.054	1.17	1.37	
M	0.012	0.028	0.30	0.71	
N	0.016	0.028	0.40	0.71	



# Electrical Characteristics @ 25°C (Unless Otherwise Specified)

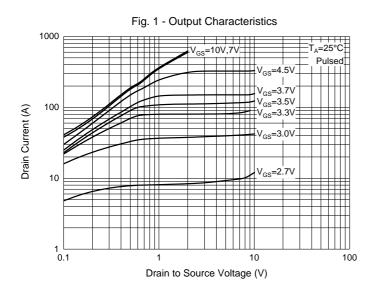
Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit	
Static Characteristics	1			ı			
Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	$V_{GS}$ =0V, $I_{D}$ =250 $\mu$ A	30			V	
Gate-Source Leakage Current	I <sub>GSS</sub>	V <sub>DS</sub> =0V, V <sub>GS</sub> =±20V			±100	nA	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =30V, V <sub>GS</sub> =0V			1	μA	
Gate-Threshold Voltage <sup>(Note 3)</sup>	V <sub>GS(th)</sub>	$V_{DS}=V_{GS}$ , $I_{D}=250\mu A$	1.2	1.7	2.5	V	
Drain-Source On-Resistance <sup>(Note 3)</sup>	В	V <sub>GS</sub> =10V, I <sub>D</sub> =20A		1.9	2.5	mΩ	
	R <sub>DS(on)</sub>	V <sub>GS</sub> =4.5V, I <sub>D</sub> =10A		2.9	3.5		
Forward Tranconductance <sup>(Note 3)</sup>	<b>g</b> <sub>FS</sub>	V <sub>DS</sub> =10V, I <sub>D</sub> =20A	32			S	
Dynamic Characteristics(Note 4)							
Input Capacitance	C <sub>iss</sub>			5000			
Output Capacitance	C <sub>oss</sub>	$V_{DS}$ =15V, $V_{GS}$ =0V,f=1MHz		1135		pF	
Reverse Transfer Capacitance	C <sub>rss</sub>			563			
Total Gate Charge	Qg			38			
Gate-Source Charge	$Q_{gs}$	V <sub>DS</sub> =15V,V <sub>GS</sub> =10V,I <sub>D</sub> =20A		9		nC	
Gate-Drain Charge	$Q_{gd}$			13			
Turn-On Delay Time	t <sub>d(on)</sub>			26			
Turn-On Rise Time	t <sub>r</sub>	$V_{DD}$ =15V, $R_L$ =15 $\Omega$		24		ns	
Turn-Off Delay Time	t <sub>d(off)</sub>	$V_{GS}$ =10 $V$ , $R_{G}$ =2.5 $\Omega$		91			
Turn-Off Fall Time	t <sub>f</sub>			39			
Drain-Source Body Diode Cha	racteristi	cs		•	•		
Continuous Body Diode Current	Is				100		
Pulsed Diode Forward Current	I <sub>SM</sub>				400	A	
Body Diode Voltage (Note 3)	V <sub>SD</sub>	I <sub>S</sub> =10A, V <sub>GS</sub> =0V			1.2	V	

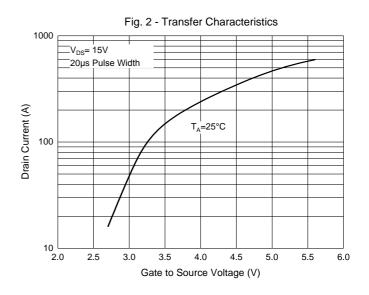
Note 3. Pulse Test : Pulse Width≤300µs, Duty Cycle ≤2%.

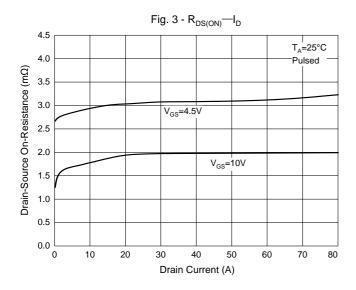
<sup>4.</sup> Guaranteed by Design, Not Subject to Production Testing.

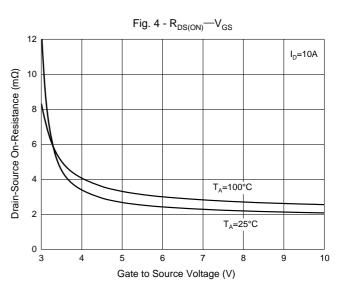


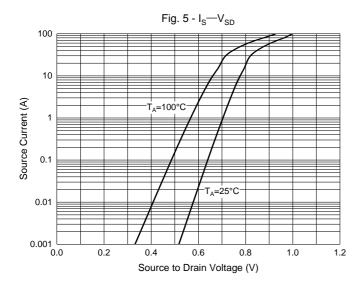
## **Curve Characteristics**

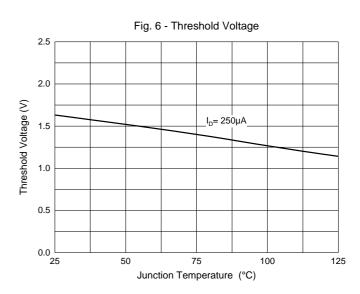














# **Ordering Information**

Device	Packing	
Part Number-TP	Tape&Reel: 5Kpcs/Reel	

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