

Features

- Split Gate Trench MOSFET Technology
- Excellent Package For Heat Dissipation
- High density cell design for low $R_{DS(ON)}$
- Moisture Sensitivity Level 1
- Halogen Free. "Green" Device ^(Note1)
- Epoxy Meets UL 94 V-0 Flammability Rating
- Lead Free Finish/RoHS Compliant ^(Note2) ("P" Suffix Designates RoHS Compliant. See Ordering Information)

Maximum Ratings

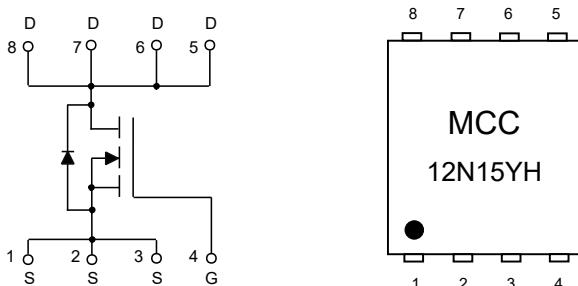
- Operating Junction Temperature Range: -55°C to +150°C
- Storage Temperature Range: -55°C to +150°C
- Thermal Resistance: 50°C/W Junction to Ambient ^(Note3)
- Thermal Resistance: 1°C/W Junction to Case

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	V_{DS}	150	V
Gate-Source Voltage	V_{GS}	± 20	V
Continuous Drain Current $T_C=25^\circ\text{C}$	I_D	70	A
		44	
Pulsed Drain Current ^(Note4)	I_{DM}	280	A
Total Power Dissipation ^(Note5)	P_D	125	W
Single Pulsed Avalanche Energy ^(Note6)	E_{AS}	361	mJ

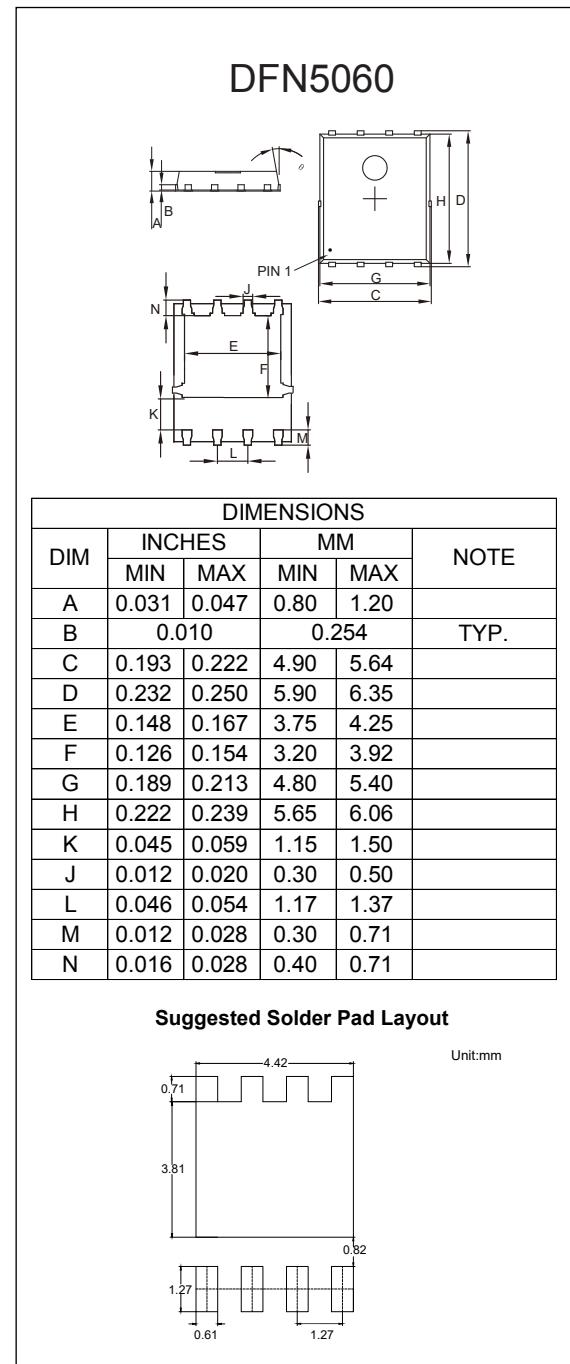
Note:

1. Halogen free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
2. High Temperature Solder Exemption Applied, see EU Directive Annex 7a.
3. The value of $R_{\theta JA}$ is measured with the device mounted on 1in² FR-4 board with 2oz. Copper, in a still air environment with $T_A = 25^\circ\text{C}$.
4. Repetitive rating; pulse width limited by max. junction temperature.
5. P_D is based on max. junction temperature, using junction-case thermal resistance.
6. $T_J=25^\circ\text{C}$, $V_{DD}=75\text{V}$, $V_{GS}=10\text{V}$, $R_G=25\Omega$, $L=2\text{mH}$.

Internal Structure and Marking Code



N-Channel MOSFET



Electrical Characteristics @ 25°C (Unless Otherwise Specified)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Static Characteristics						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS}=0V, I_D=250\mu A$	150			V
Gate-Source Leakage Current	I_{GSS}	$V_{DS}=0V, V_{GS}=\pm 20V$			± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS}=150V, V_{GS}=0V$			1	μA
Gate-Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=250\mu A$	2	3	4	V
Drain-Source On-Resistance	$R_{DS(on)}$	$V_{GS}=10V, I_D=37.5A$		9.2	11.8	$m\Omega$
Gate Resistance	R_g	f=1 MHz, Open drain		1		Ω
Diode Characteristics						
Continuous Body Diode Current	I_S				70	A
Diode Forward Voltage	V_{SD}	$V_{GS}=0V, I_S=37.5A$			1.2	V
Reverse Recovery Time	t_{rr}	$I_F=37.5A, dI_F/dt=100A/\mu s$		87		ns
Reverse Recovery Charge	Q_{rr}			245		nC
Dynamic Characteristics						
Input Capacitance	C_{iss}	$V_{DS}=75V, V_{GS}=0V, f=1MHz$		3750		pF
Output Capacitance	C_{oss}			290		
Reverse Transfer Capacitance	C_{rss}			6		
Total Gate Charge	Q_g	$V_{DS}=75V, V_{GS}=10V, I_D=37.5A$		48		nC
Gate-Source Charge	Q_{gs}			16		
Gate-Drain Charge	Q_{gd}			14		
Turn-On Delay Time	$t_{d(on)}$	$V_{DD}=75V, V_{GS}=10V$ $R_G=2.2\Omega, I_D=37.5A$		19		ns
Turn-On Rise Time	t_r			109		
Turn-Off Delay Time	$t_{d(off)}$			31		
Turn-Off Fall Time	t_f			6		

Curve Characteristics

Fig. 1 - Typical Output Characteristics

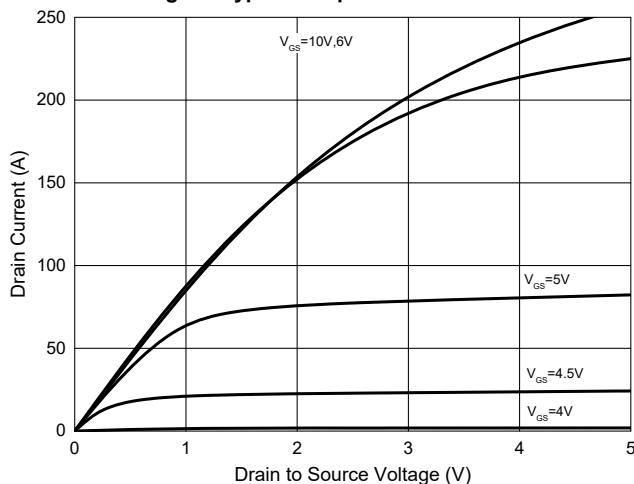


Fig. 2 - Transfer Characteristics

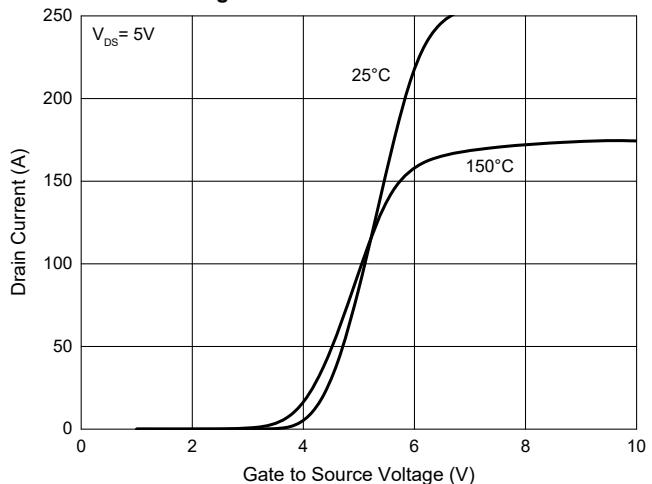


Fig. 3 - $R_{DS(ON)}$ - V_{GS}

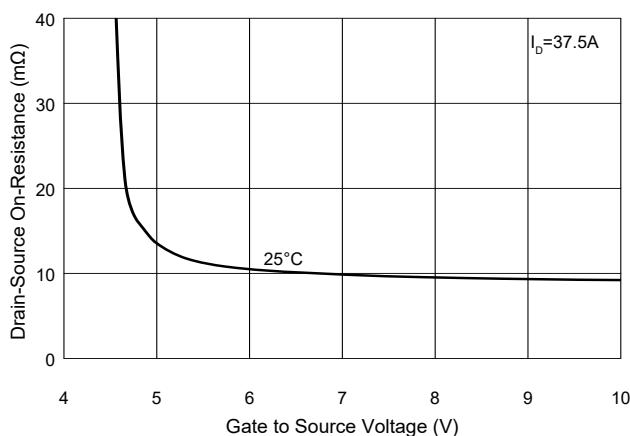


Fig. 4 - $R_{DS(ON)}$ - I_D

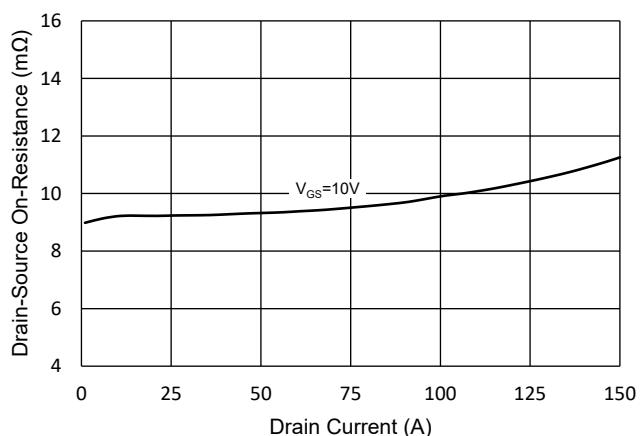


Fig. 5 - Capacitance Characteristics

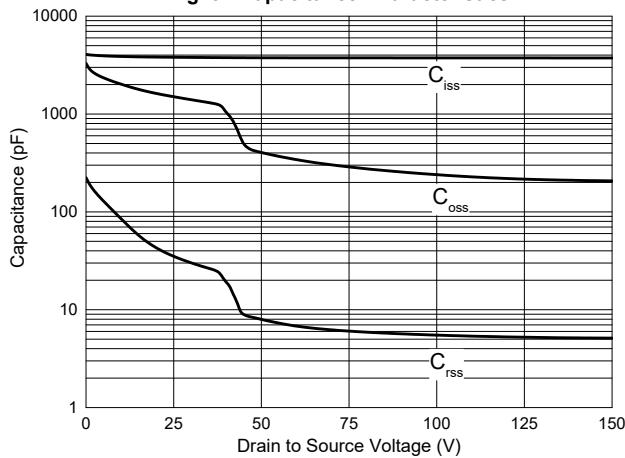
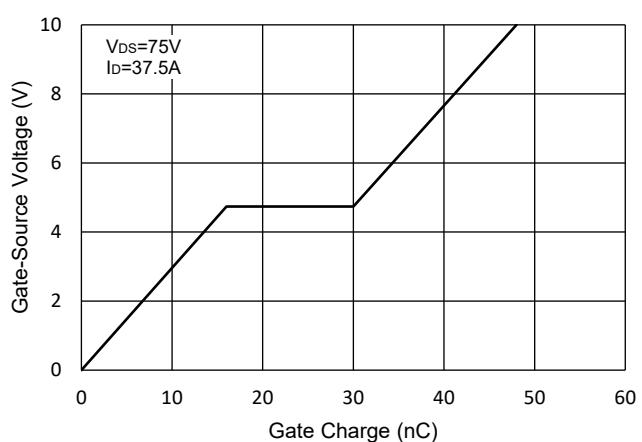


Fig. 6 - Gate Charge



Curve Characteristics

Fig.7 - Normalized Threshold Voltage

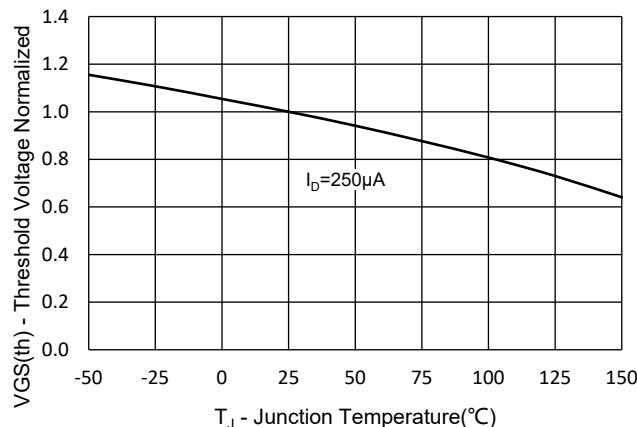


Fig.8 - Normalized On Resistance Characteristics

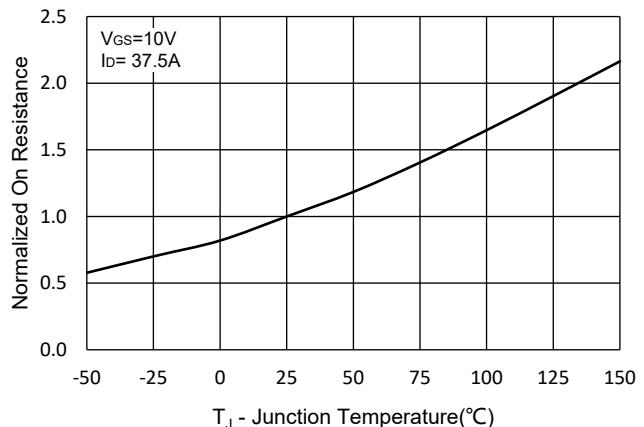


Fig.9 - I_S - V_{SD}

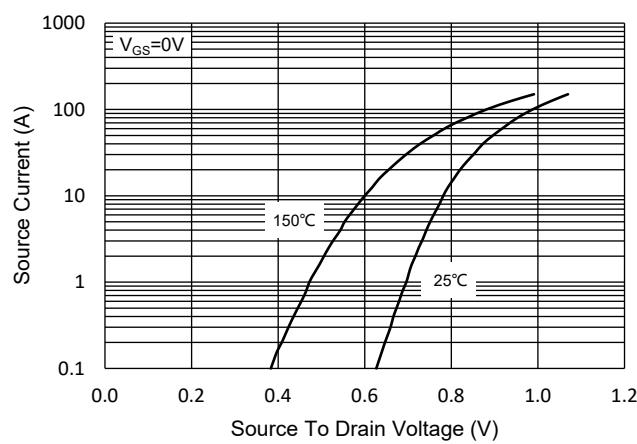


Fig. 10 - Drain Current

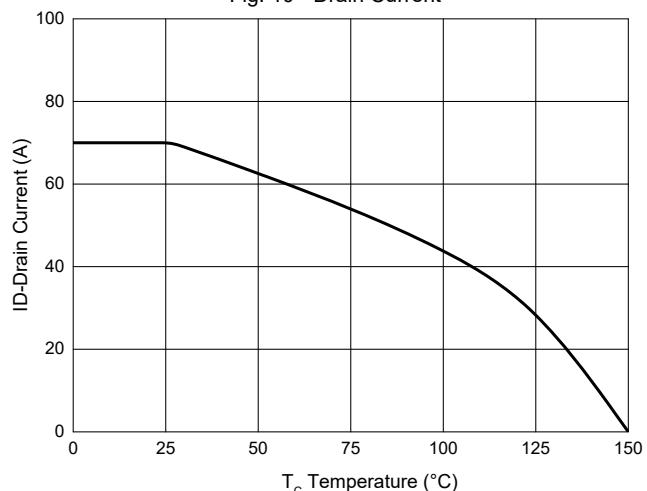
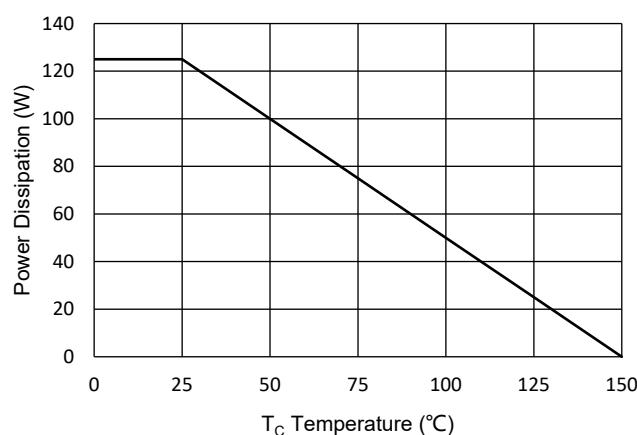
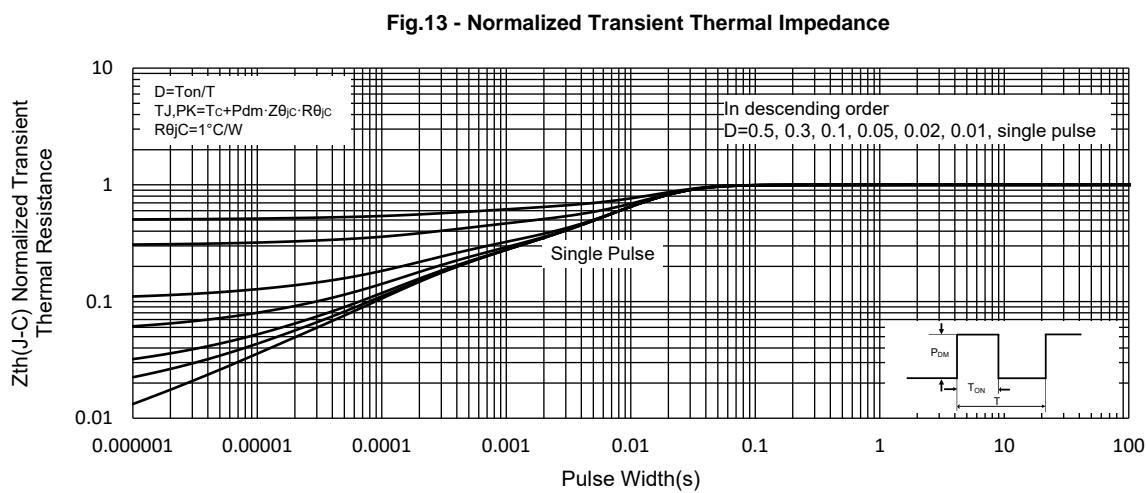
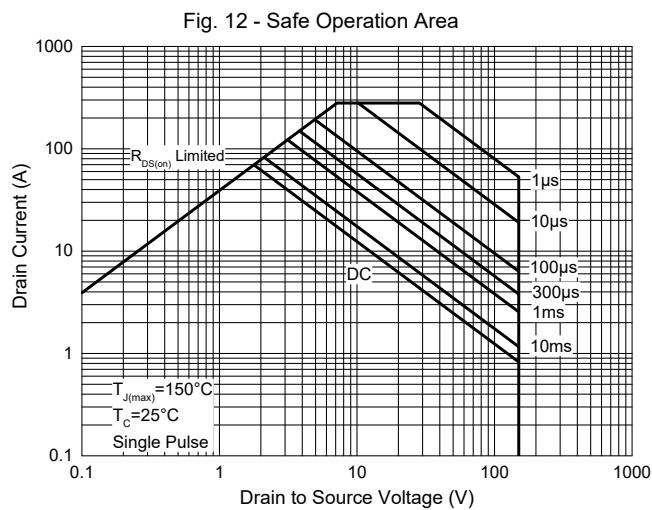


Fig.11 - PD Dissipation



Curve Characteristics



Ordering Information

Device	Packing
Part Number-TP	Tape&Reel: 5Kpcs/Reel

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