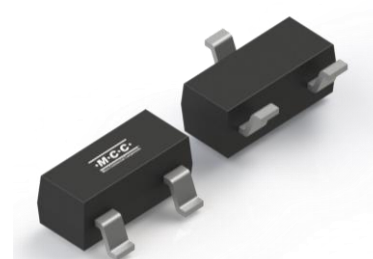


2-Line Bi-directional Ultra Low Capacitance ESD

Features

- Transient protection:
 - IEC 61000-4-2 (ESD) $\pm 16\text{kV}$ (Air), $\pm 16\text{kV}$ (Contact)
 - IEC 61000-4-5 (Lightning) 4A (8/20 μs)
- AEC-Q101 Qualified
- Bi-directional ESD protection of two lines
- Reverse working voltage, V_{RWM} : 5V
- Low capacitance: 0.4pF (typical)
- Low clamping voltage: 25V (max)
- Low reverse leakage current: 100nA max at $V_R = 5\text{V}$
- Solid-state silicon-avalanche



SOT-23



Applications

- Automotive Application
- High-speed data lines
- 10/100/1000 Ethernet

Mechanical Data

- Package: SOT-23
- Moisture Sensitivity Level 1, per J-STD-020
- Halogen Free. "Green" Device (Note1)
- Epoxy Meets UL 94 V-0 Flammability Rating
- Lead Free Finish/RoHS Compliant ("P" Suffix Designates RoHS Compliant. See Ordering Information)

Body Marking and Pin Layout

Marking Code	Simplified Outline	Internal Structure
<p>5BT.</p>	<p>Transparent top view</p>	

Ordering Information

Product Name	Packing info
ESDULC5V0T2BHE3-TP	3K pcs/reel

For packaging details, visit our website at <https://www.mccsemi.com/Package/List>

2-Line Bi-directional Ultra Low Capacitance ESD

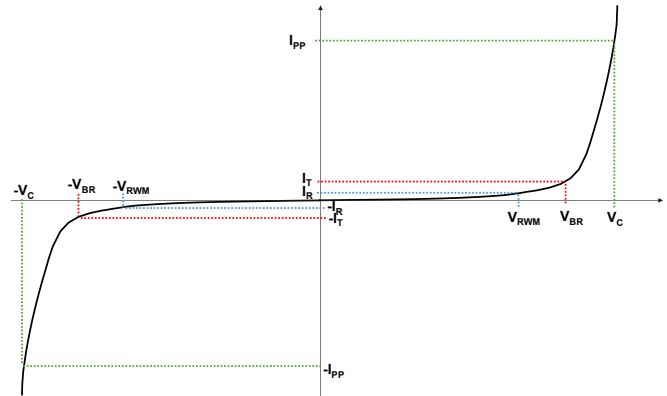
Maximum Ratings (T_A=25°C unless otherwise specified)

Parameter		Symbol	Rating	Unit
IEC61000-4-2(ESD)	Air	V _{ESD}	±16	kV
	Contact	V _{ESD}	±16	kV
Peak Pulse Current (8/20µs) (Note 2)		I _{PP}	4	A
Peak Pulse Power (8/20µs) (Note 2)		P _{PK}	100	W
Operating Temperature Range		T _J	-55 to +150	°C
Storage Temperature Range		T _{STG}	-55 to +150	°C

- Note:
- Halogen free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and 1000ppm antimony compounds.
 - Non-repetitive current pulse 8/20µs exponential decay waveform according to IEC61000-4-5.

Parameter Definition

Symbol	Parameter
V _{RWM}	Peak Reverse Working Voltage
I _R	Reverse Leakage Current @ V _{RWM}
V _{BR}	Breakdown Voltage @ I _T
I _T	Test Current
I _{PP}	Maximum Reverse Peak Pulse Current
V _C	Clamping Voltage @ I _{PP}
P _{PK}	Peak Pulse Power
C _J	Junction Capacitance



Electrical Characteristics (T_A=25°C unless otherwise specified)

Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Reverse Working Voltage	V _{RWM}				5	V
Reverse Breakdown Voltage	V _{BR}	I _T = 1mA	5.8		9.5	V
Reverse Leakage Current	I _R	V _{RWM} = 5V			0.1	µA
Clamping Voltage (Note3)	V _C	I _{PP} = 1A, t _p = 8/20µs			15	V
		I _{PP} = 4A, t _p = 8/20µs			25	
Junction Capacitance	C _J	V _R = 0V, f = 1MHz		0.4	0.6	pF
Dynamic Resistance (Note4)	R _{DYN}	TLP, t _p = 100ns		1.4		Ω

- Note:
- Non-repetitive current pulse 8/20µs exponential decay waveform according to IEC61000-4-5.
 - TLP parameter: Z₀ = 50Ω, t_p = 100ns, t_r = 2ns, averaging window from 60ns to 80ns. R_{DYN} is calculated from 4A to 16A.

2-Line Bi-directional Ultra Low Capacitance ESD

Curve Characteristics

Fig. 1 - 8 X 20µs Pulse Waveform

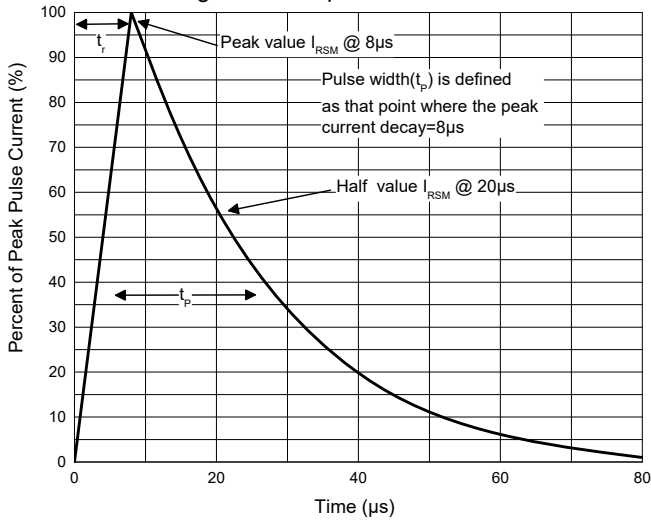


Fig. 2 - Pulse Derating Curve

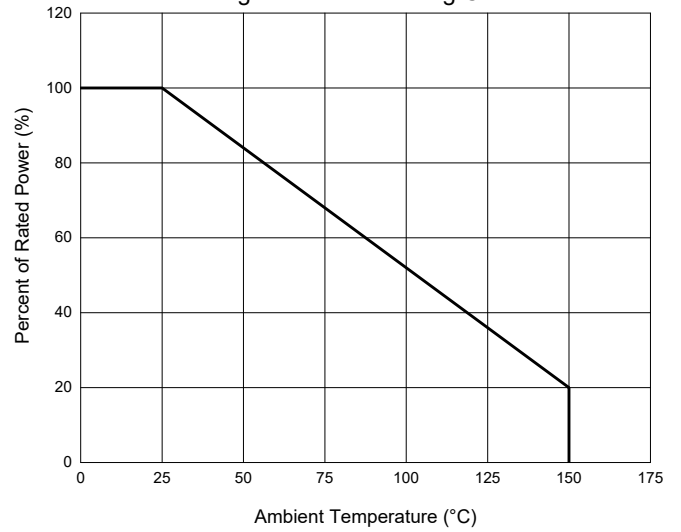


Fig. 3 - Capacitance Characteristics

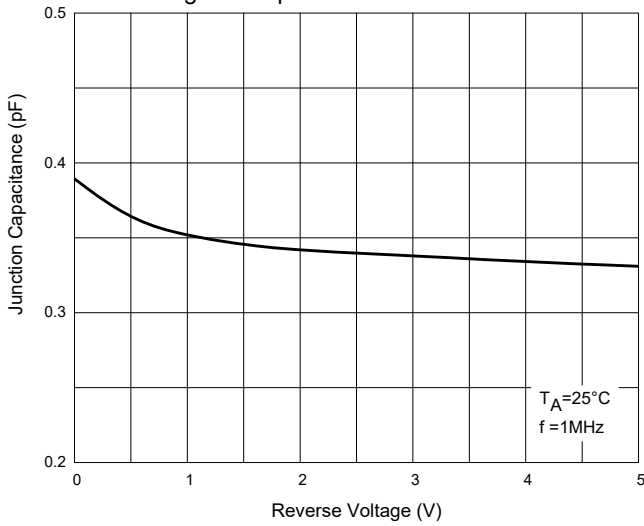


Fig. 4 - Clamping Voltage Characteristics

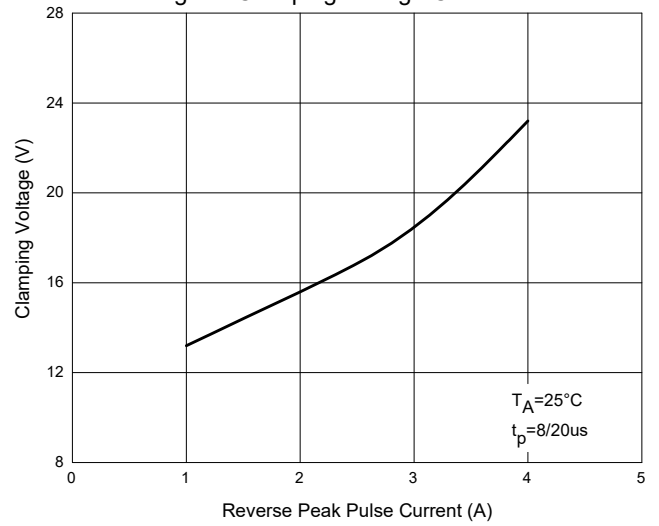
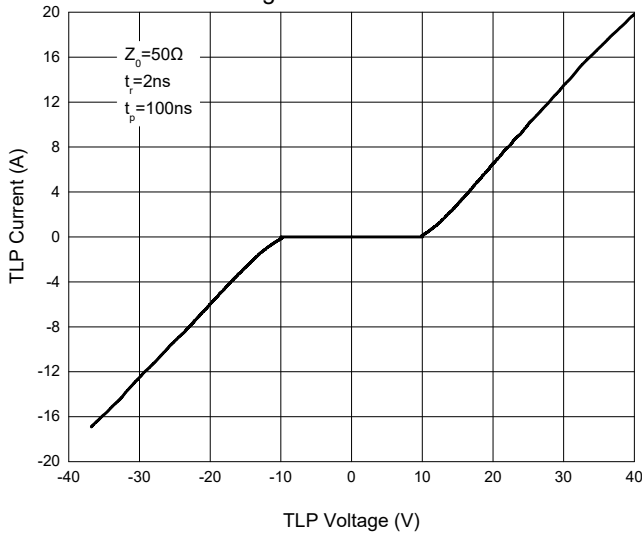
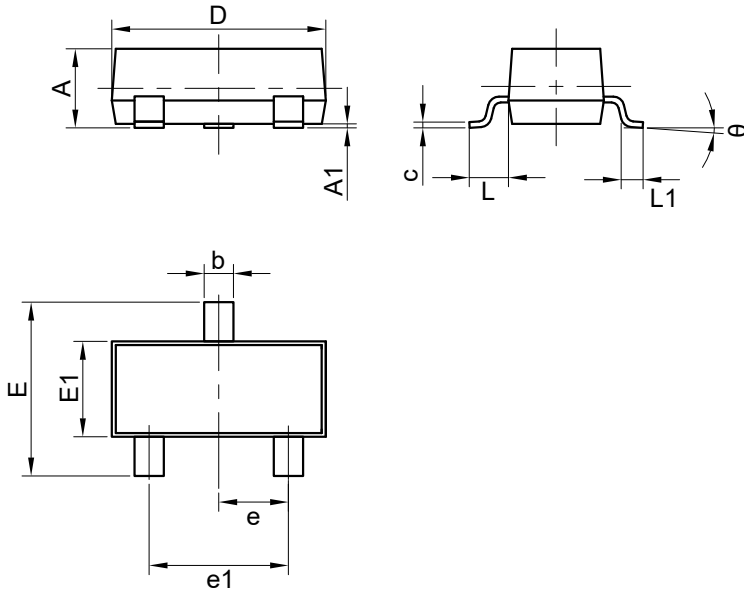


Fig. 5 - TLP Curve



Package Outline

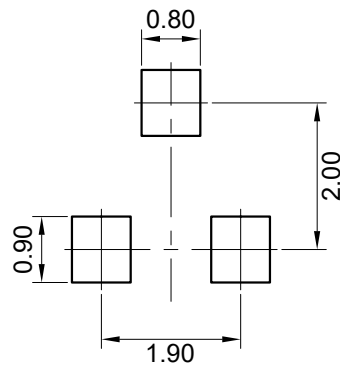


DIM	INCH		MM		NOTE
	MIN	MAX	MIN	MAX	
A	0.035	0.045*	0.90	1.15*	Note 1
A1	0.000	0.004	0.01	0.10	
b	0.012	0.020	0.30	0.50	
c	0.003	0.007	0.08	0.18	
D	0.110	0.120	2.80	3.04	
E	0.047	0.055	2.10	2.64	
E1	0.083	0.104	1.20	1.40	
e	0.037		0.95		TYP
e1	0.075		1.90		TYP
L	0.017	0.026	0.45	0.65	
L1	0.011	0.020	0.30	0.50	
theta	0°	8°	0°	8°	

Notes:

1. Dimension A for products from manufacturing site VN is controlled at maximum 1.10 mm.

Suggested Pad Layout (Unit:mm)



Notes:

1. The suggested land pattern dimensions have been provided for reference only.
2. For further information, please refer to document IPC-7351A.

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