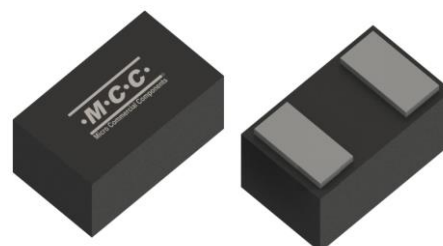


1-Line Bi-directional Ultra Low Capacitance ESD

Features

- Transient protection:
 - IEC 61000-4-2 (ESD) $\pm 15\text{kV}$ (Air), $\pm 15\text{kV}$ (Contact)
 - IEC 61000-4-5 (Lightning) 5A (8/20 μs)
- Bi-directional ESD protection of single line
- Reverse working voltage, V_{RWM} : 18V
- Low capacitance: 0.5pF
- Low clamping voltage: 8V
- Low reverse leakage current: 100nA max at $V_{\text{R}} = 18\text{V}$
- Solid-state silicon-avalanche



CSP1006-2



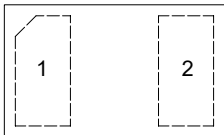
Applications

- Display Ports
- MDDI Ports
- USB Ports
- Digital Video Interface

Mechanical Data

- Package: CSP1006-2
- Moisture Sensitivity Level 1, per J-STD-020
- Halogen Free. "Green" Device (Note1)
- Epoxy Meets UL 94 V-0 Flammability Rating
- Lead Free Finish/RoHS Compliant ("P" Suffix Designates RoHS Compliant. See Ordering Information)

Body Marking and Pin Layout

Marking Code	Simplified Outline	Internal Structure
18B	 Transparent top view	

Ordering Information

Product Name	Packing info
CSPSBULC18VLB-TP	10K pcs/reel

For packaging details, visit our website at <https://www.mccsemi.com/Package/List>

1-Line Bi-directional Ultra Low Capacitance ESD

Maximum Ratings (T_A=25°C unless otherwise specified)

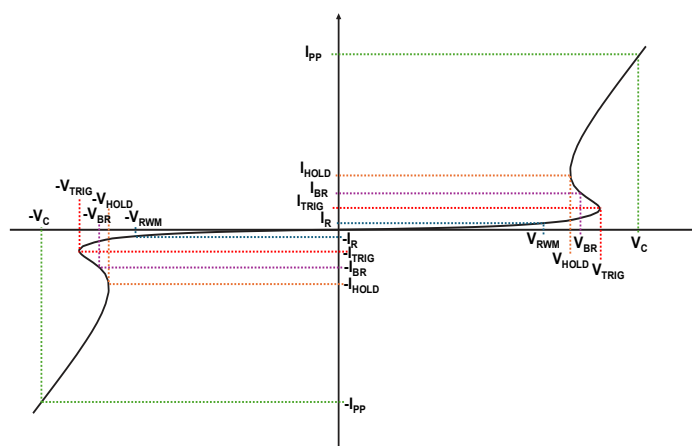
Parameter	Symbol	Rating	Unit
IEC61000-4-2(ESD)	Air	V _{ESD}	±15
	Contact	V _{ESD}	±15
Peak Pulse Current (8/20μs) (Note 2)	I _{PP}	5	A
Peak Pulse Power (8/20μs) (Note 2)	P _{PK}	35	W
Operating Temperature Range	T _J	-55 to +125	°C
Storage Temperature Range	T _{STG}	-55 to +150	°C

Note:

- Halogen free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and 1000ppm antimony compounds.
- Non-repetitive current pulse 8/20μs exponential decay waveform according to IEC61000-4-5.

Parameter Definition

Symbol	Parameter
V _{RWM}	Peak Reverse Working Voltage
I _R	Reverse Leakage Current @ V _{RWM}
V _{BR}	Breakdown Voltage @ I _T
I _T	Test Current
I _{PP}	Maximum Reverse Peak Pulse Current
V _C	Clamping Voltage @ I _{PP}
V _{TRIG}	Reverse Trigger Voltage
I _{TRIG}	Reverse Trigger Current
V _{HOLD}	Reverse Holding Voltage
I _{HOLD}	Reverse Holding Current
C _J	Junction Capacitance
P _{PK}	Peak Pulse Power



Electrical Characteristics (T_A=25°C unless otherwise specified)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Reverse Working Voltage	V _{RWM}				18	V
Reverse Breakdown Voltage	V _{BR}	I _T = 1mA	19	22	25	V
Reverse Leakage Current	I _R	V _{RWM} = 18V			0.1	μA
Clamping Voltage (Note3)	V _C	I _{PP} = 1A, t _p = 8/20μs		4	5	V
		I _{PP} = 5A, t _p = 8/20μs		6	8	
Clamping Voltage (Note4)	V _C	I _{PP} = 4A(TLP)		5		V
		I _{PP} = 16A(TLP)		8		
ESD Trigger Voltage	V _{TRIG}	t _p = 100ns, T _A = 25°C		23		V
Reverse Holding Voltage	V _{HOLD}			2.9		V
Junction Capacitance	C _J	V _R = 0V, f = 1MHz		0.5	0.8	pF
Dynamic Resistance (Note4)	R _{DYN}	TLP, t _p = 100ns		0.26		Ω

Note:

- Non-repetitive current pulse 8/20μs exponential decay waveform according to IEC61000-4-5.
- TLP parameter: Z₀ = 50Ω, t_p = 100ns, t_r = 2ns, averaging window from 60ns to 80ns. R_{DYN} is calculated from 4A to 16A.

Curve Characteristics

Fig. 1 - 8 X 20 μ s Pulse Waveform

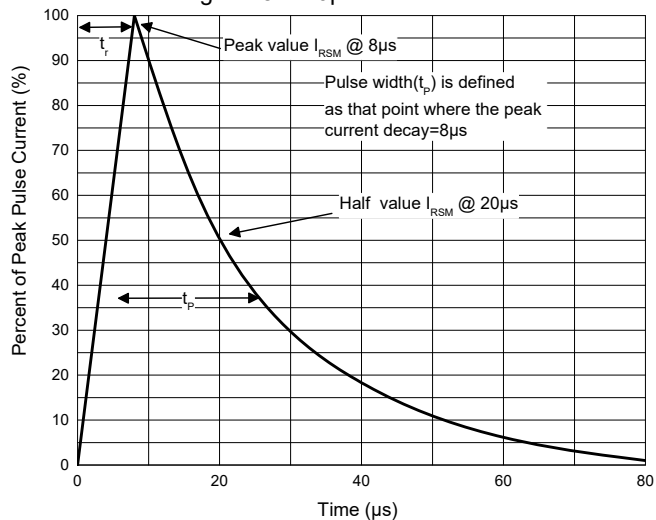


Fig. 2 - Pulse Derating Curve

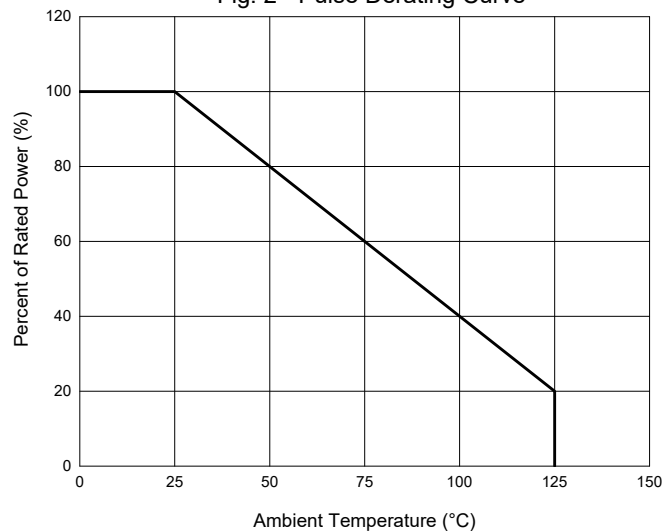


Fig. 3 - Capacitance Characteristics

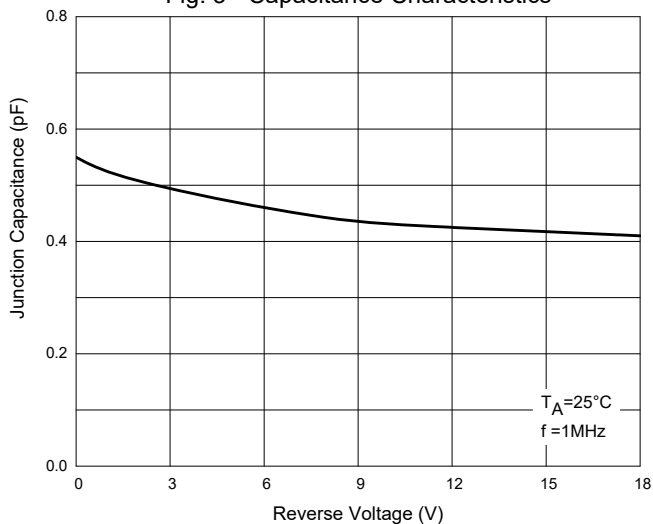


Fig. 4 - Clamping Voltage Characteristics

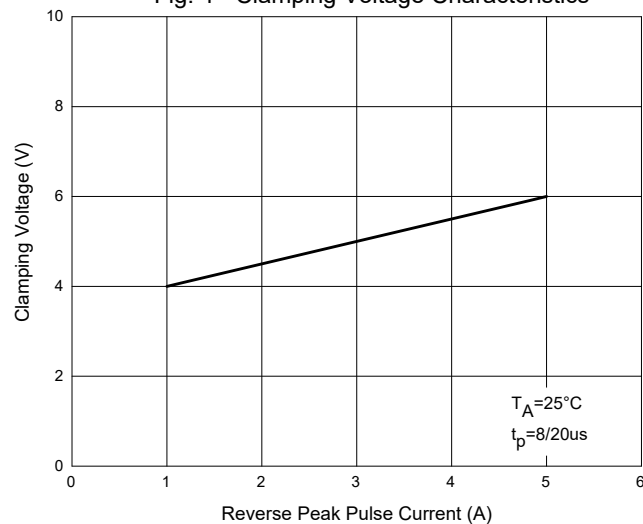


Fig. 5 - TLP Curve

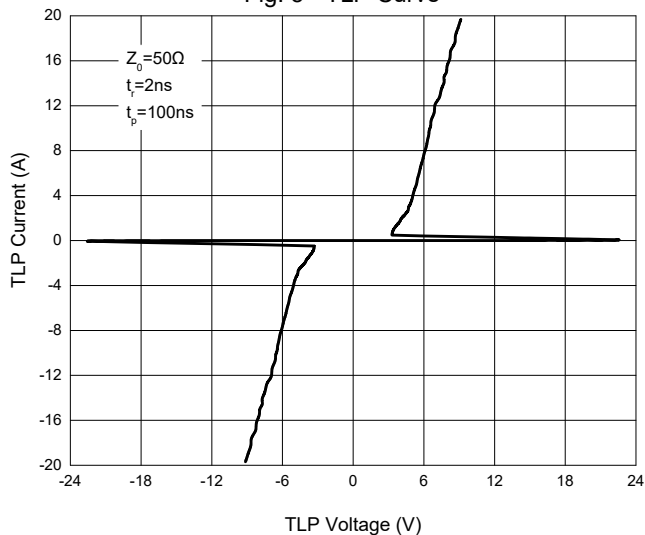
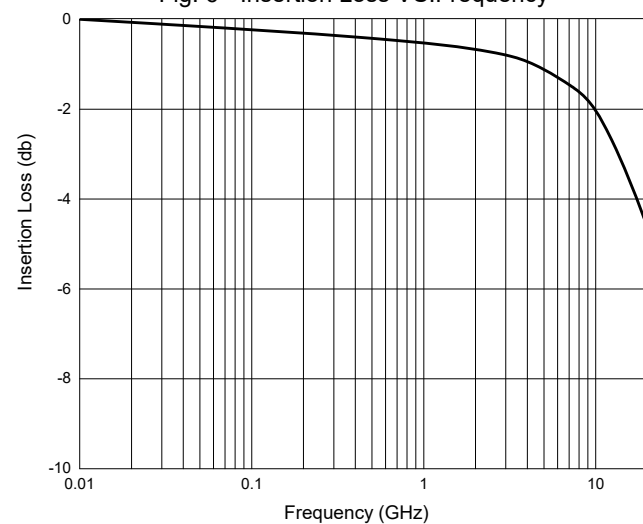


Fig. 6 - Insertion Loss VS.Frequency



1-Line Bi-directional Ultra Low Capacitance ESD

Fig. 7- Return Loss VS.Frequency

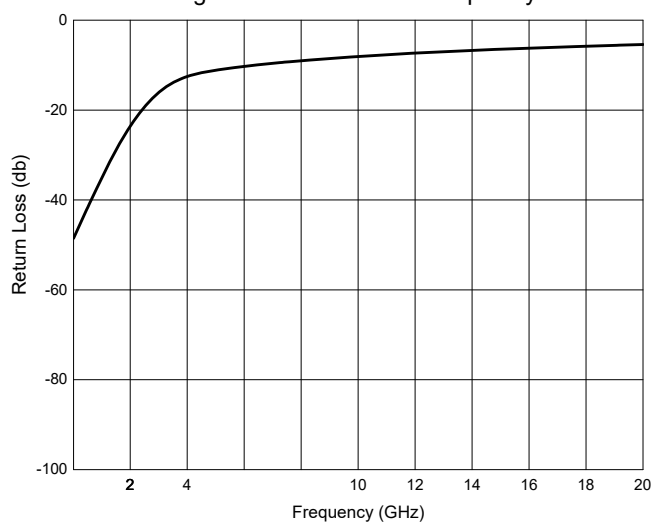
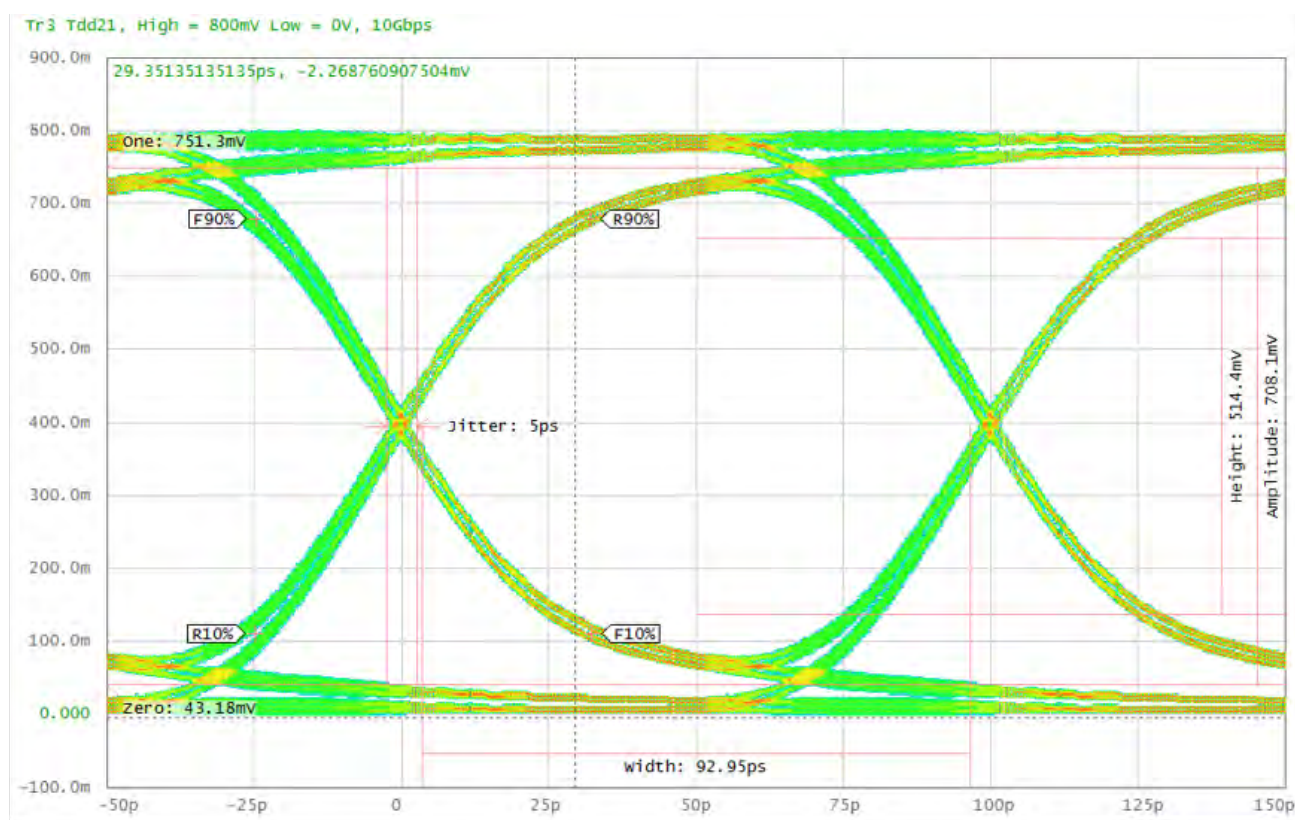
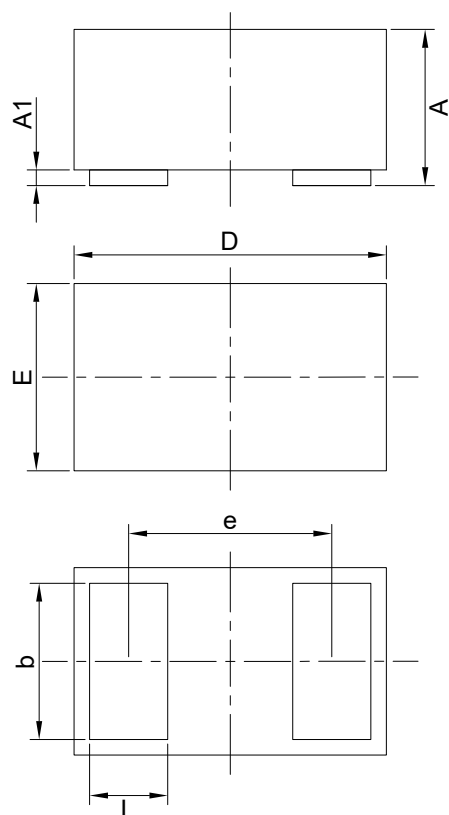


Fig. 8 - Eye Diagram (10 Gbps)

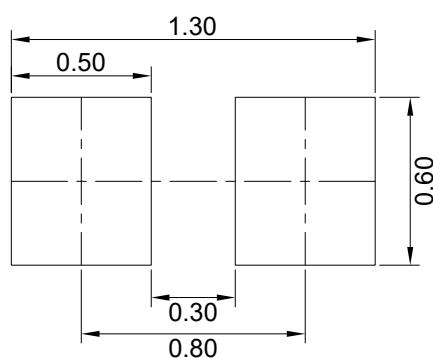


Package Outline



DIM	INCH		MM		NOTE
	MIN	MAX	MIN	MAX	
A	0.016	0.022	0.40	0.55	
A1	0.000	0.002	0.00	0.05	
b	0.018	0.022	0.45	0.55	
D	0.037	0.041	0.95	1.05	
E	0.022	0.026	0.55	0.65	
e	0.026		0.65		TYP
L	0.008	0.012	0.20	0.30	

Suggested Pad Layout (Unit:mm)



Notes:

1. The suggested land pattern dimensions have been provided for reference only.
2. For further information, please refer to document IPC-7351A.

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